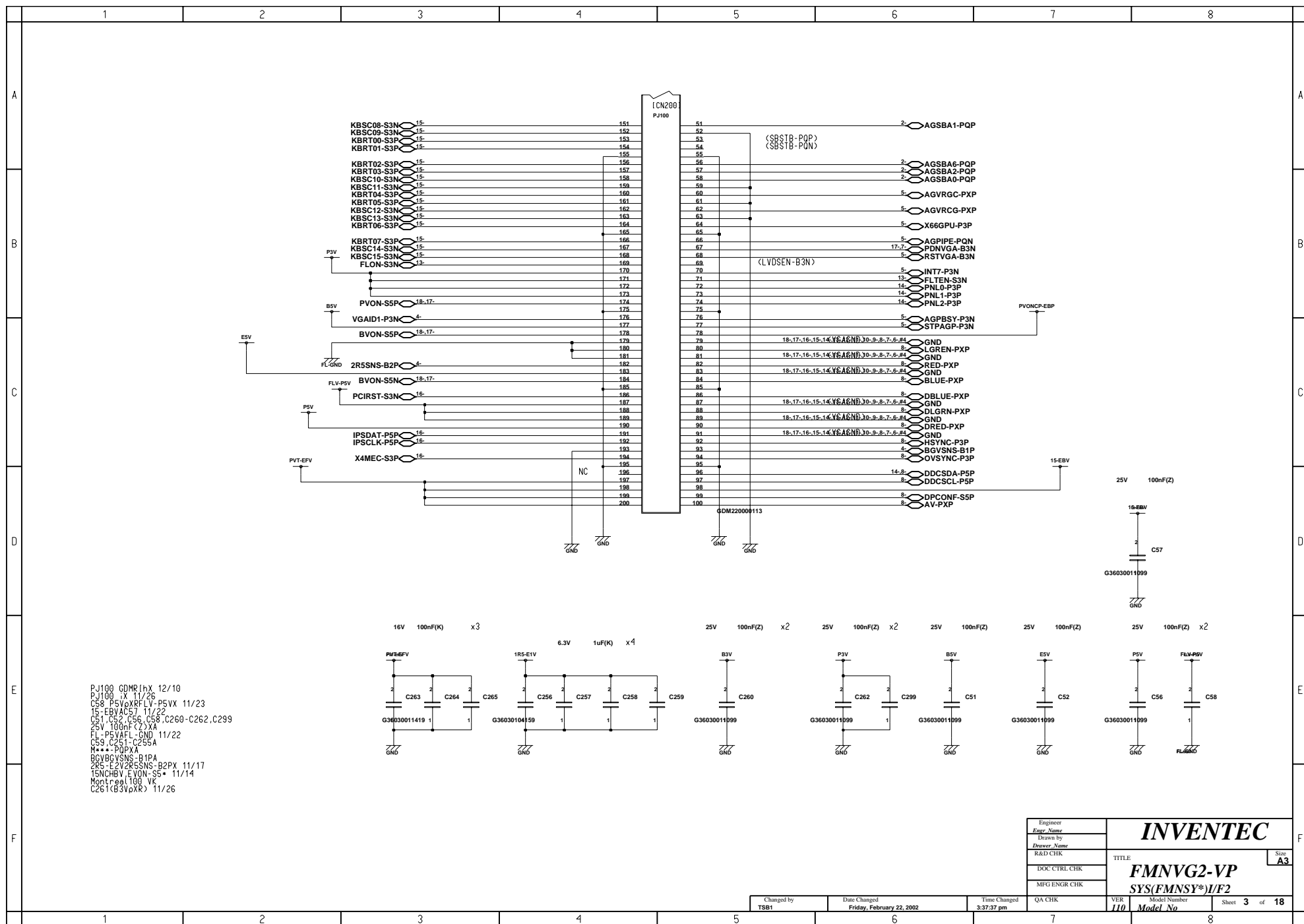
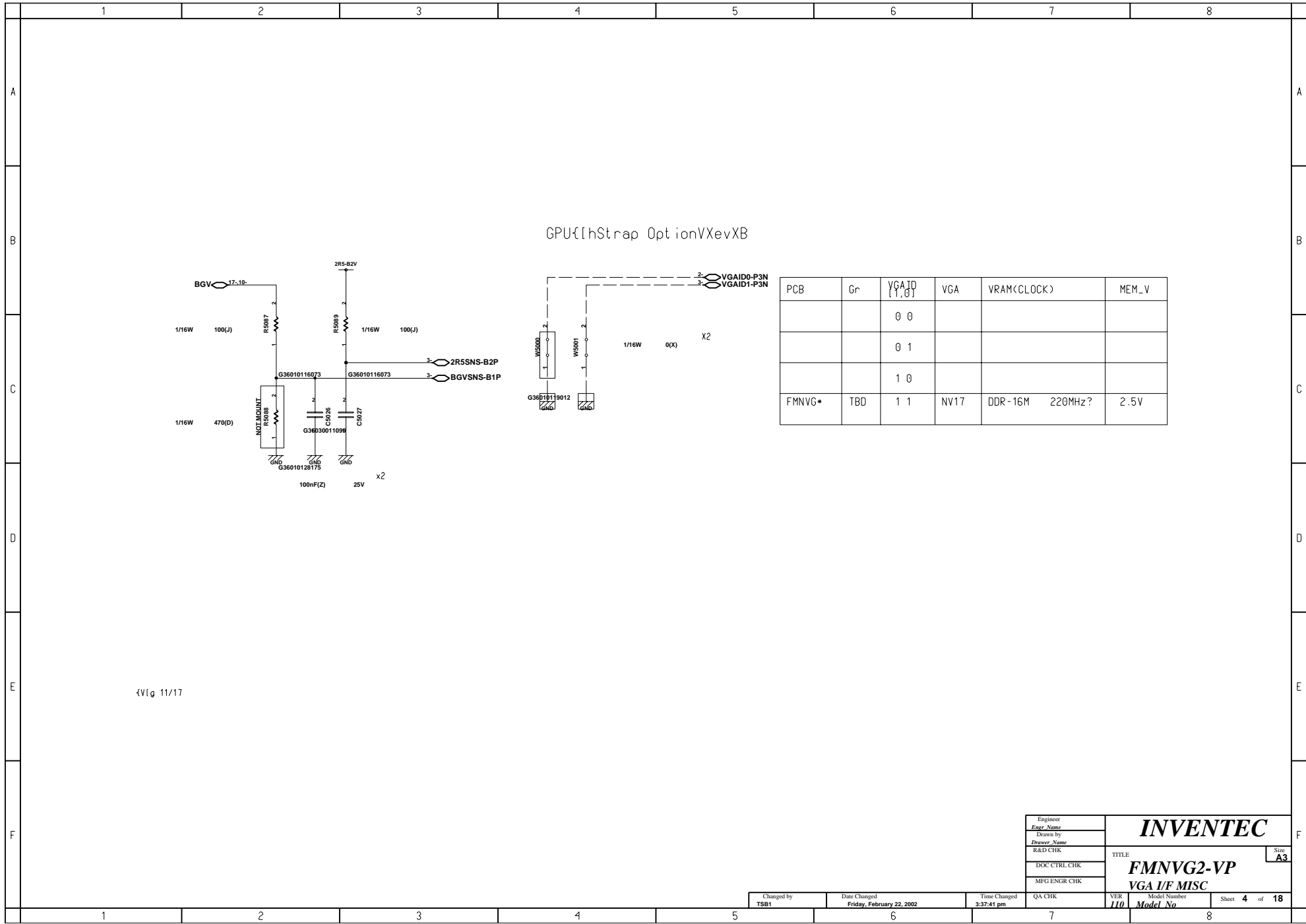
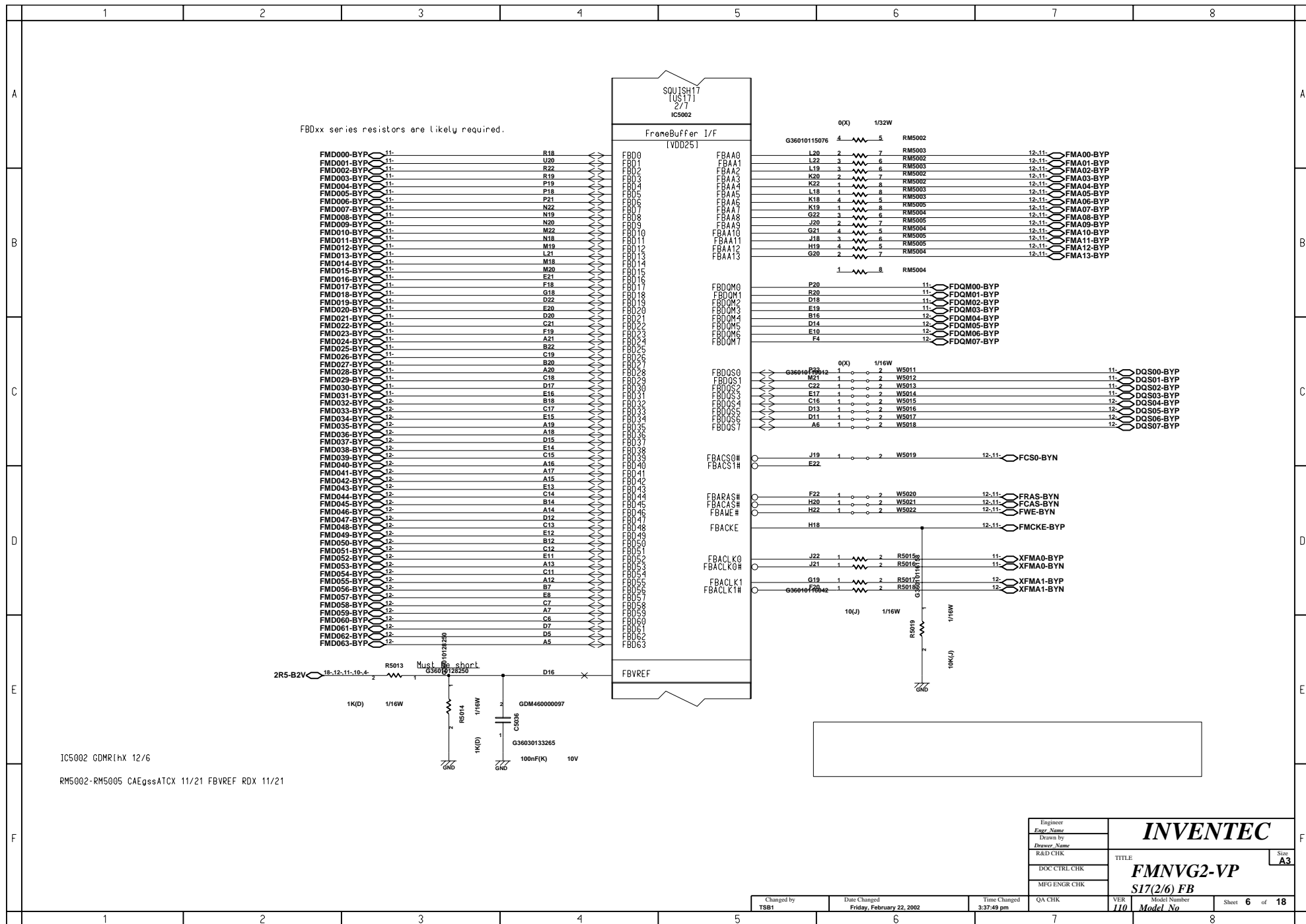


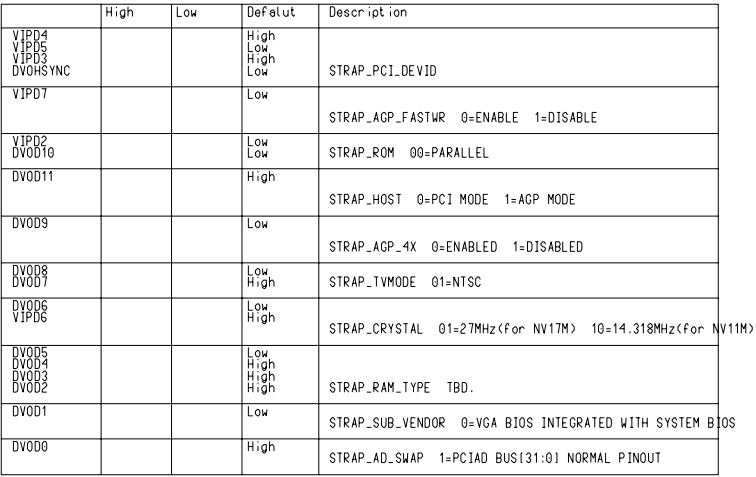
PJ100 GDMRHX 12/10
 PJ100 X 11/26
 RM5000 RM5001PULLUP*XC 11/23
 RM5000 RM5001 CAEGsX 11/21
 4PINVGA100-P3PA
 N***PQX 11/17
 RM5000 RM5001 NOT MOUNTX 11/15
 1*4PIN, 101 104PIN 2R5-B2VONDX11/14
 Montreal100VK

Engineer Eng Name	INVENTEC TITLE: FMNVS2-VP SYS(FMNSY*)/F1 Model No	Size A3
Drawn by Drawer Name		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		
Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:37:32 pm
QA CHK	VER 110	Model Number Sheet 2 of 18

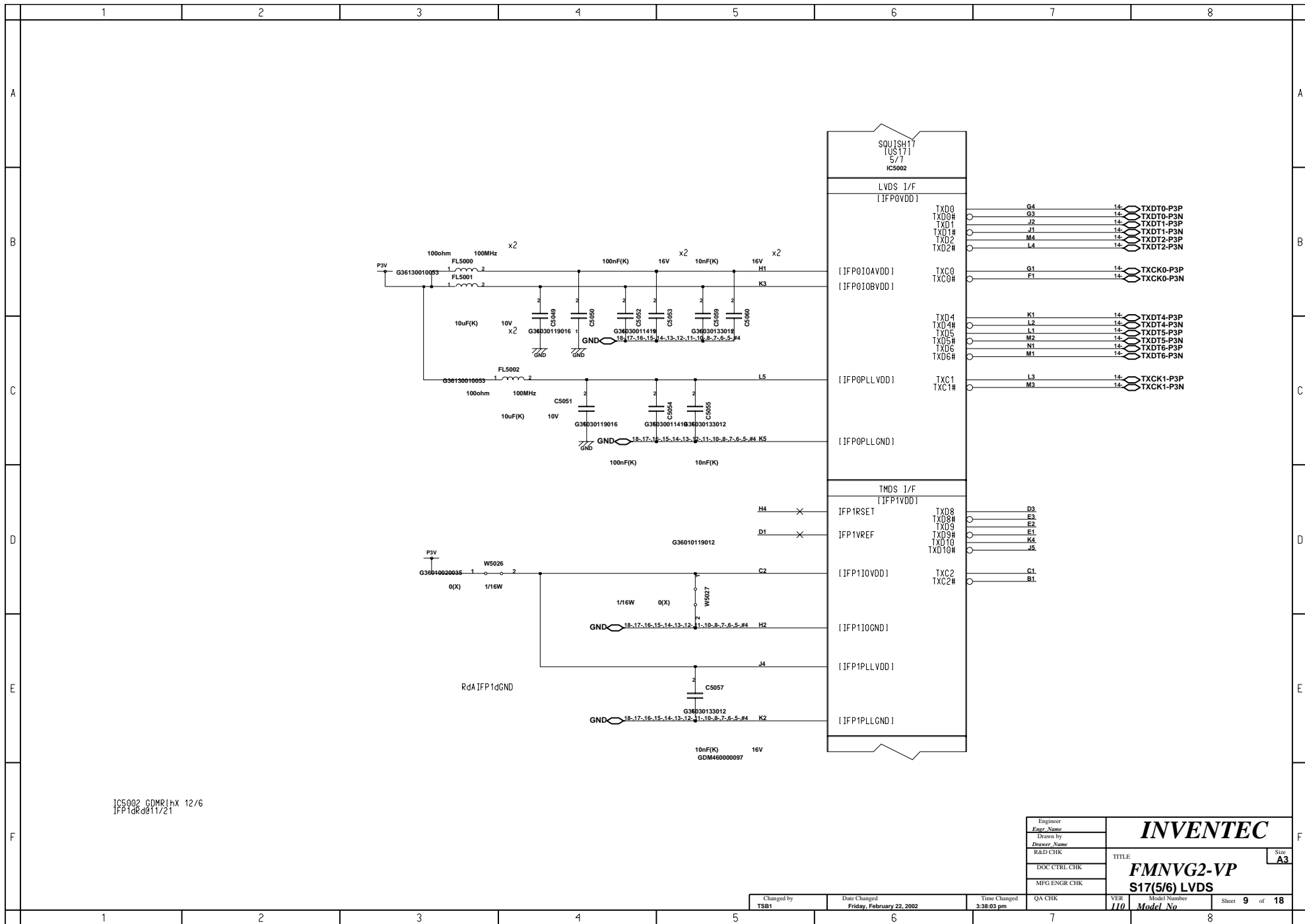


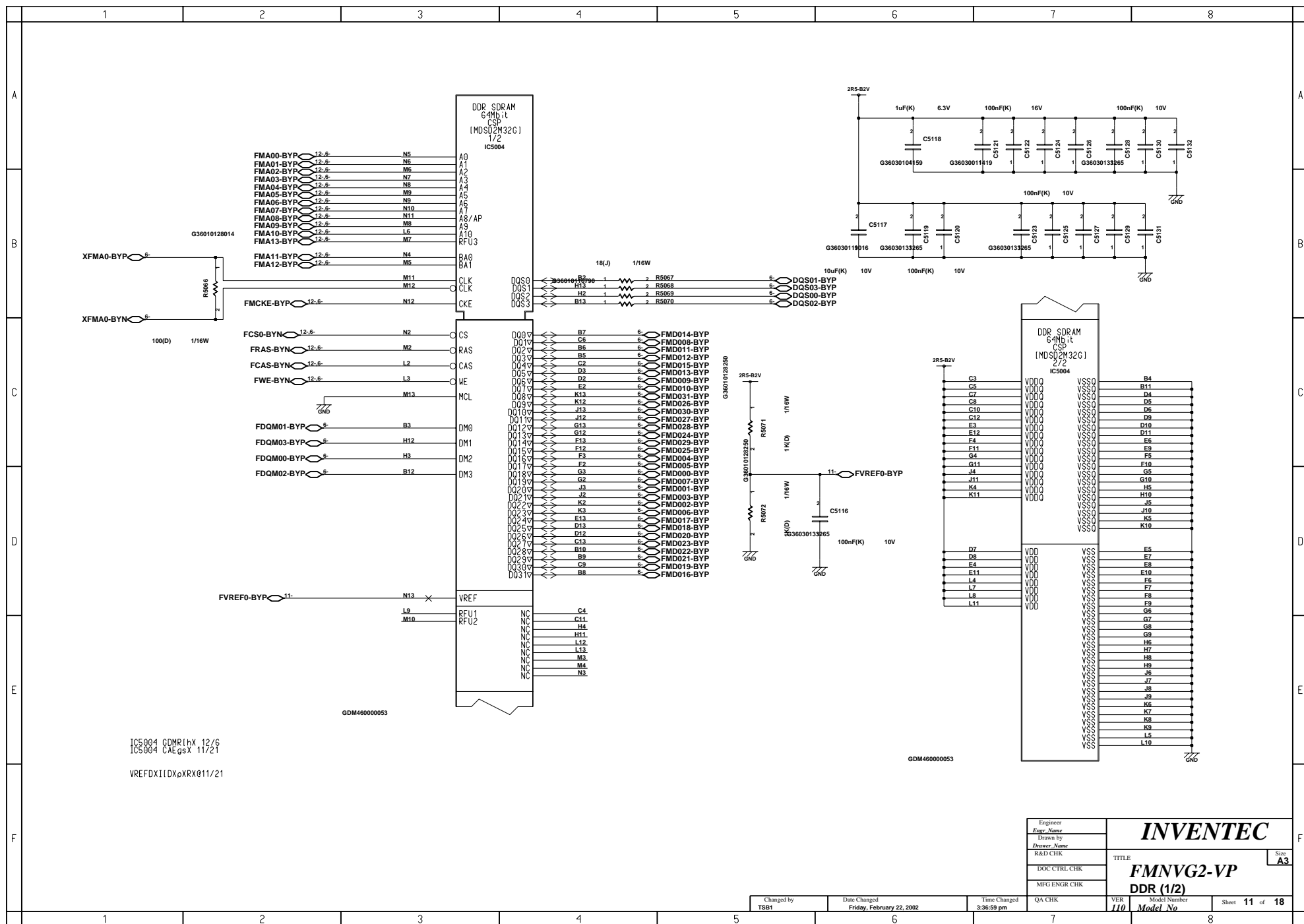




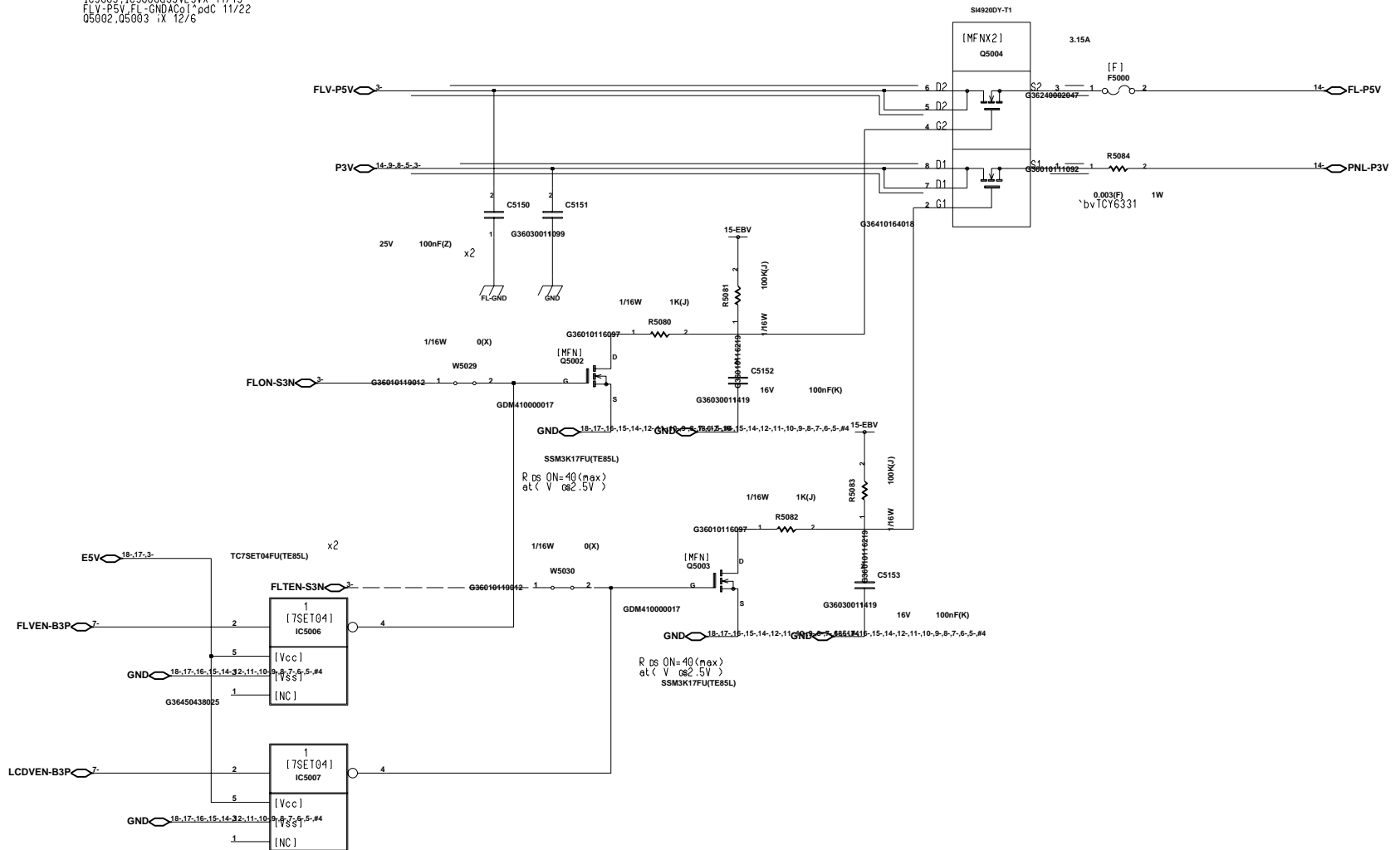


Engineer <i>Engr. Name</i>	<div style="text-align: center;"> <h1>INVENTEC</h1> </div>		
Drawn by <i>Dwgsr. Name</i>			
R&D CHK	TITLE	Size A3	
DOC CTRL CHK	<div style="text-align: center;"> <h2>FMNVG2-VP</h2> <h3>S17(3/6) MISC</h3> </div>		
MFG ENGR CHK			
QA CHK			
VER 110	Model Number <i>Model No</i>	Sheet 7 of 18	





060205X 2001/10/5
IC5005, IC5006dSSVE5VX 11/15
FLV-P5V, FL-GNDACol*pdC 11/22
05002, 05003 iX 12/6



Engineer Engr Name	INVENTEC		
Drawn by Drawer Name			
R&D CHK			
DOC CTRL CHK			
MFG ENGR CHK			
TITLE		Size A3	
FMNVG2-VP			
LCD POWER			
Changed by TSB1	Date Changed Friday, February 22, 2002	Time Changed 3:37:07 pm	QA CHK
VER 110	Model Number Model No	Sheet 13 of 18	

