CS2810 – PC Architecture

Interrupt Vector Decoding Worksheet

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Practice IVT decoding by completing the exercises below using the following dump of the beginning of an Interrupt Vector Table

0000:0000 68 10 A7 00 8B 01 70 00-16 00 9D 03 8B 01 70 00   h.....p.......p.

0000:0010 8B 01 70 00 B9 06 10 02-40 07 10 02 FF 03 10 02   ..p.....@.......

0000:0020 46 07 10 02 0A 04 10 02-3A 00 9D 03 54 00 9D 03   F.......:...T...

0000:0030 6E 00 9D 03 88 00 9D 03-A2 00 9D 03 FF 03 10 02   n..............

0000:0040 A9 08 10 02 99 09 10 02-9F 09 10 02 5D 04 10 02   ....]...

0000:0050 A5 09 10 02 0D 02 DE 02-B8 09 10 02 8B 05 10 02   ....

0000:0060 02 0C 10 02 08 0C 10 02-13 0C 10 02 AD 06 10 02   ....

0000:0070 AD 06 10 02 A4 F0 00 F0-37 05 10 02 37 23 00 C0   ....7...7#..

Interrupt	Segment:Offset	20 bit
03H	0070 : 018B	0088B
09H	0210 : 040A	0250A
0FH	0210 : 03FF	024FF
13H	0210: 045D	0255D
1AH	0210: 0C13	02D13