

Bloques
Daniel .E. Zamora Sifredo / CUJAE / FIAB

Sheet: /
File: AutoPLC-v1.sch

Title: AutoPLC-v1

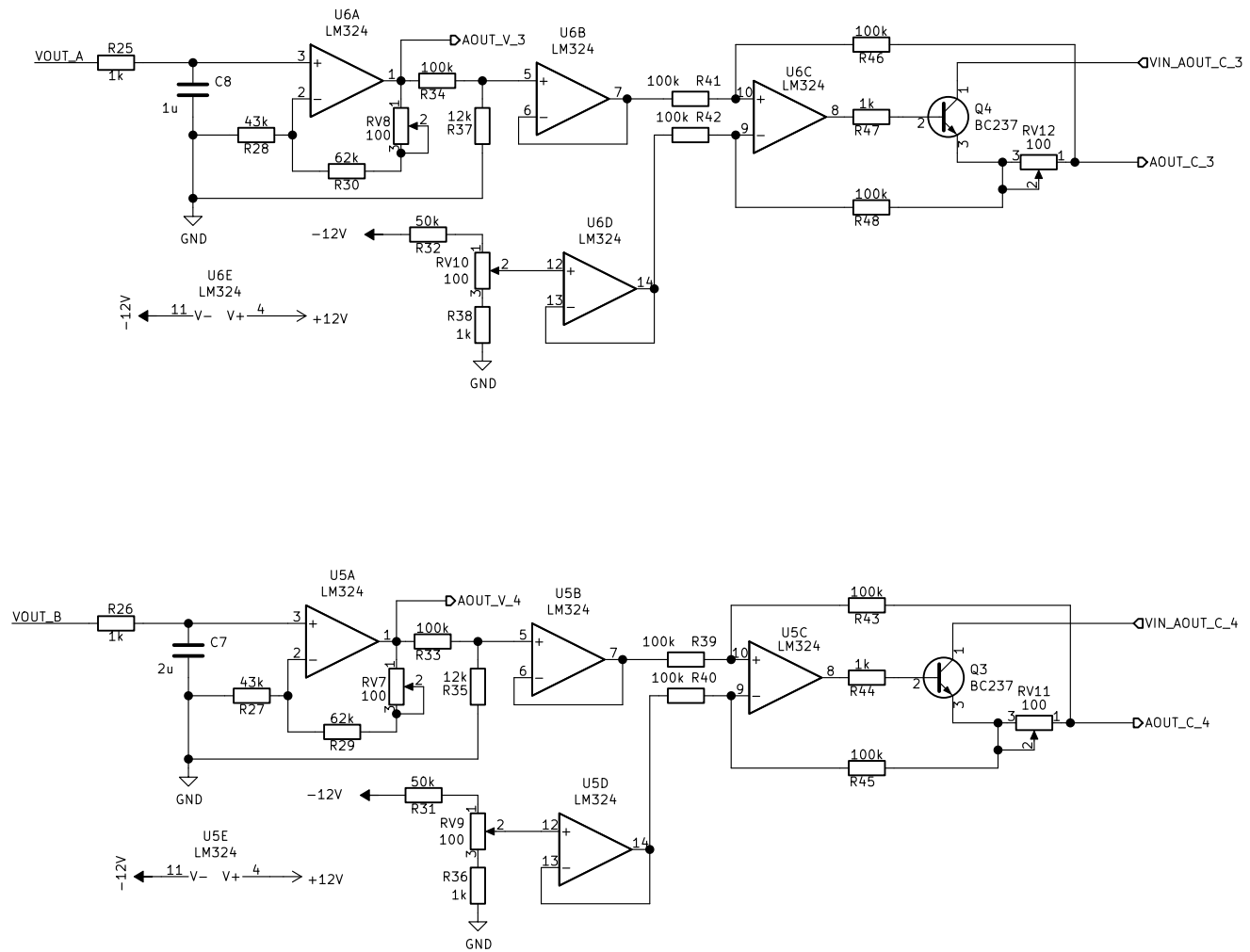
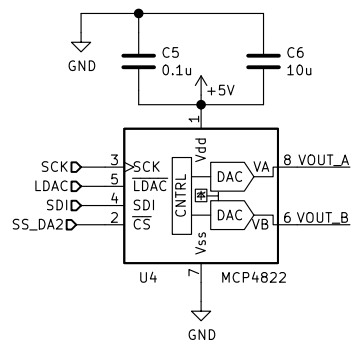
Size: A4 Date: 2021-12-13

KiCad E.D.A. kicad (5.1.5)-3

Rev: 1.13

Id: 1/9

Id: 2/9



(4-20mA/0-10V)

Salidas 3 y 4

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Sheet: /S_Analog_2/

File: S_Analog_2.sch

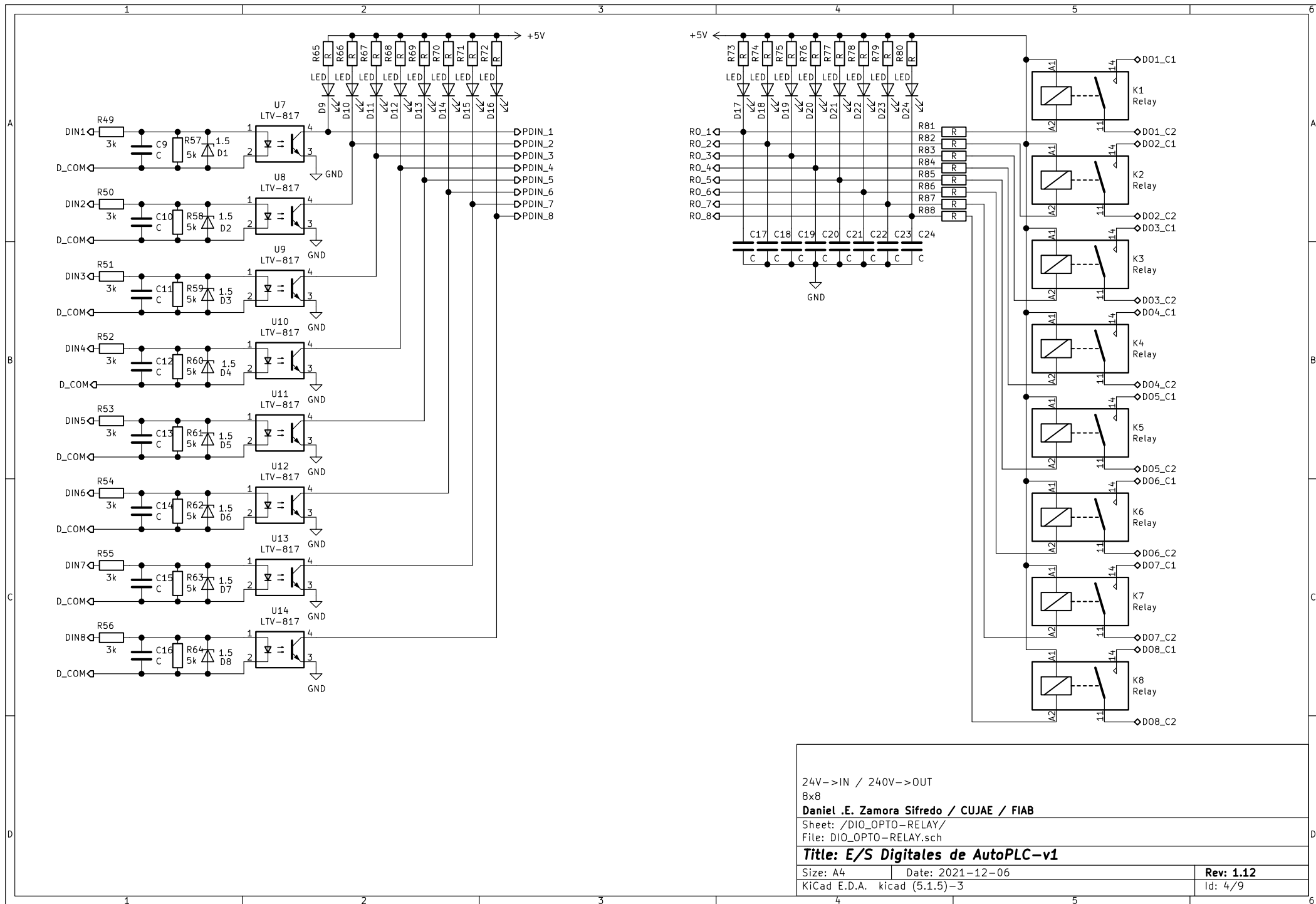
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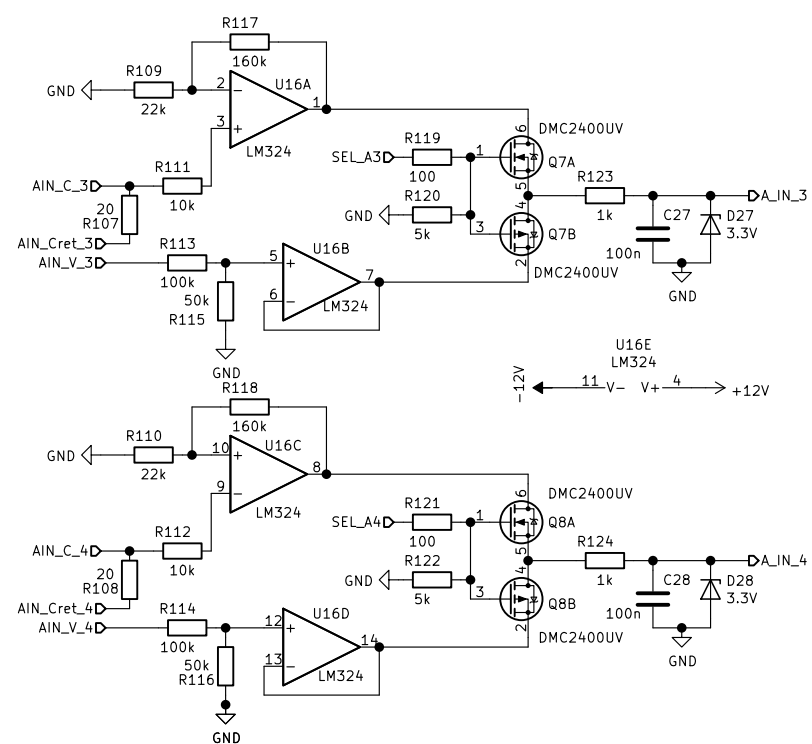
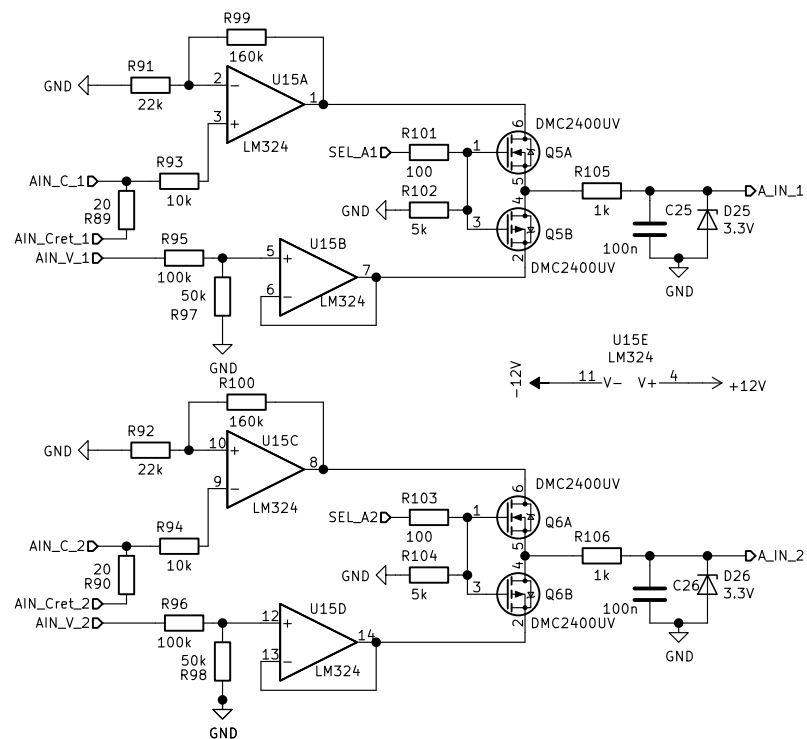
Size: A4 Date: 2021-12-06

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Rev: 1.13

Id: 3/9





(4-20mA/0-10V)

Entradas 1,2,3,4

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Sheet: /E_Analog_1/

File: E_Analog_1.sch

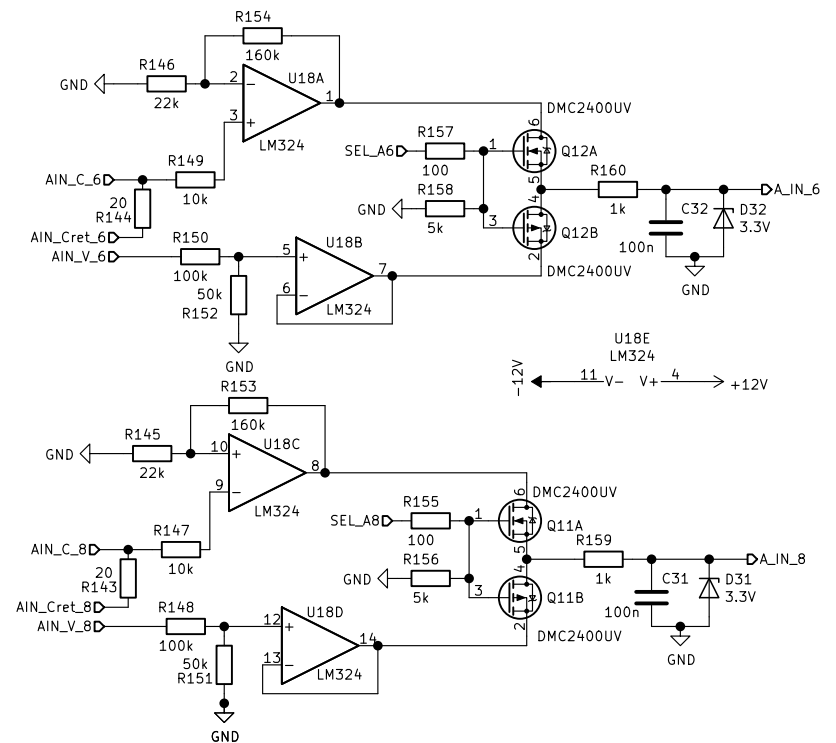
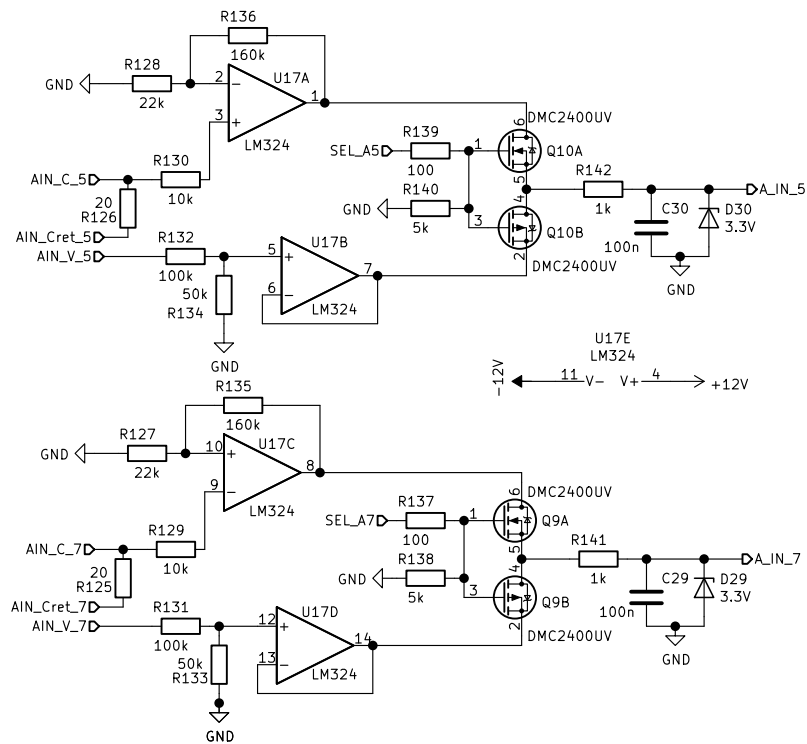
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Size: A4 Date: 2021-12-06

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Rev: 1.12

Id: 5/9



(4-20mA/0-10V)

Entradas 5,6,7,8

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Sheet: /E_Analog_2/

File: E_Analog_2.sch

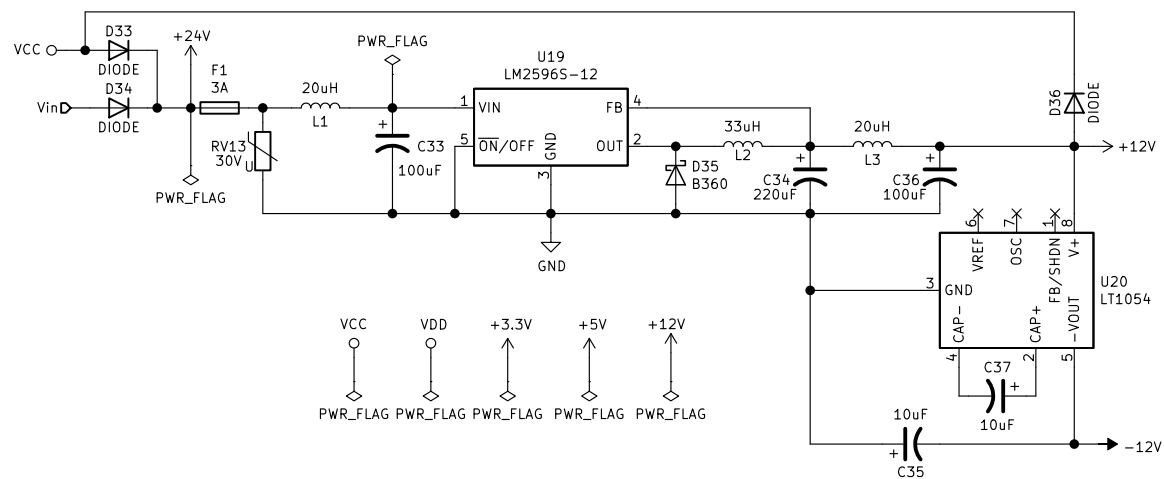
Title: Entradas Analógicas de AutoPLC-v1

Size: A4 Date: 2021-12-06

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Rev: 1.12

Id: 6/9



Fuente +12V hasta 3A
Fuente -12V solo para AOPs
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Sheet: /Power/
File: Power.sch

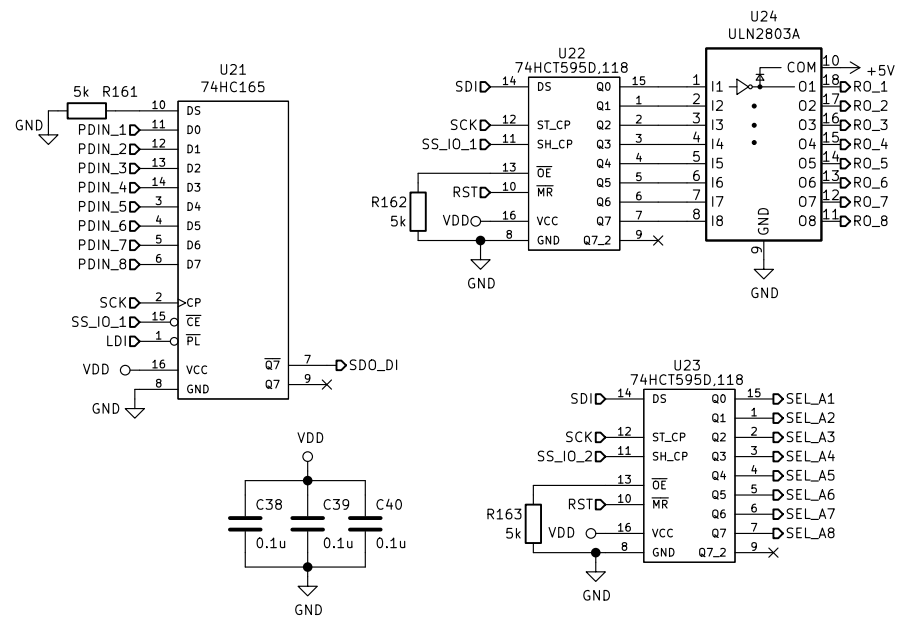
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Size: A4 Date: 2021-12-06

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Rev: 1.12

Id: 7/9



Capacitores de desacoplo
 Selectores de entradas analógicas (SEL_Ax)
 E/S Digitales

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Sheet: /SPI_DIO/

File: SPI_DIO.sch

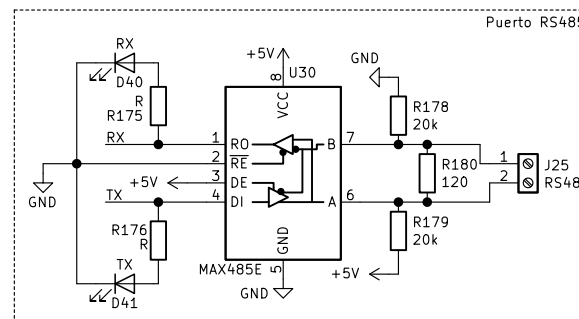
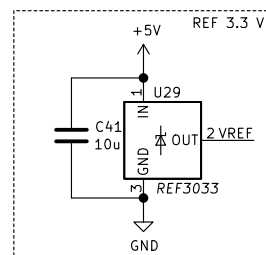
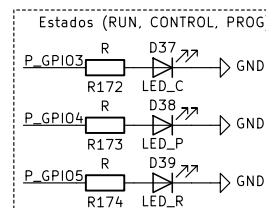
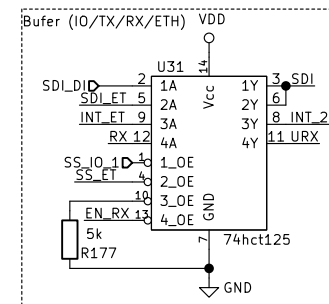
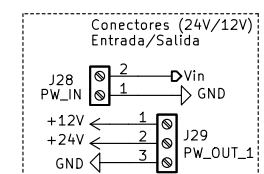
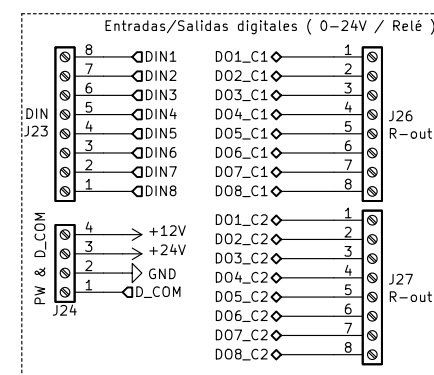
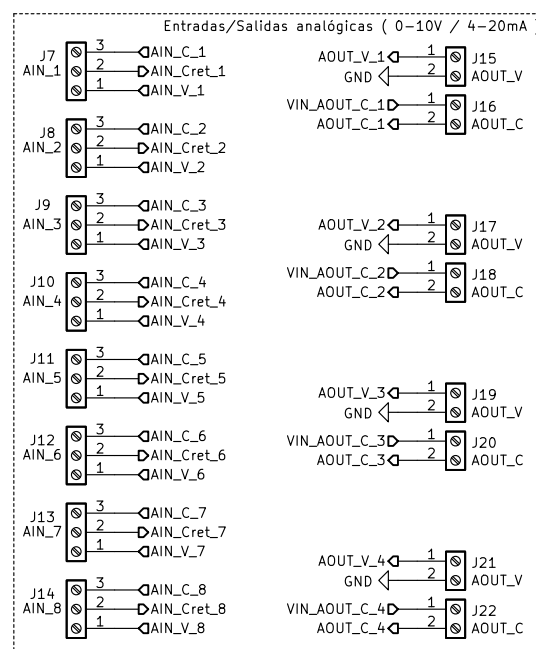
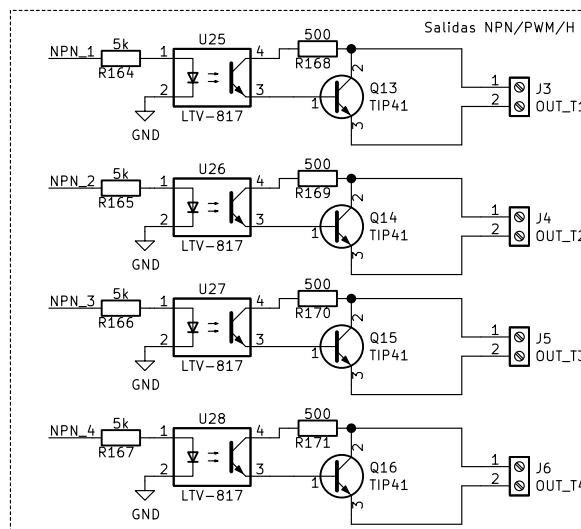
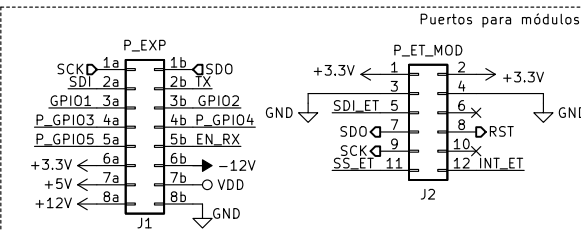
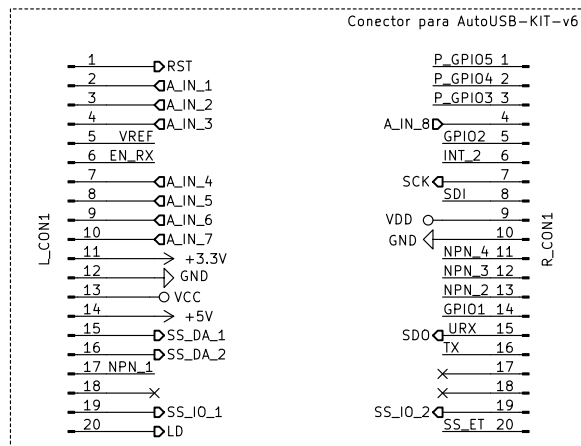
Title: Expansión de pueros

Size: A4 Date: 2021-12-13

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Rev: 1.13

Id: 8/9



Referencia de 3.3V
Puertos para módulos y puerto RS485
LEDs indicadores de estado
Salidas a transistor para PWM y puente H
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Sheet: /Terminales/
File: Terminales.sch

Title: Terminales

Size: A4 Date: 2021-12-13
KiCad E.D.A. kicad (5.1.5)-3

Rev: 1.14
Id: 9/9