

Bloques

Daniel .E. Zamora Sifredo / CUJAE / FIAB

Sheet: /

File: AutoPLC-v1.sch

**Title: AutoPLC-v1**

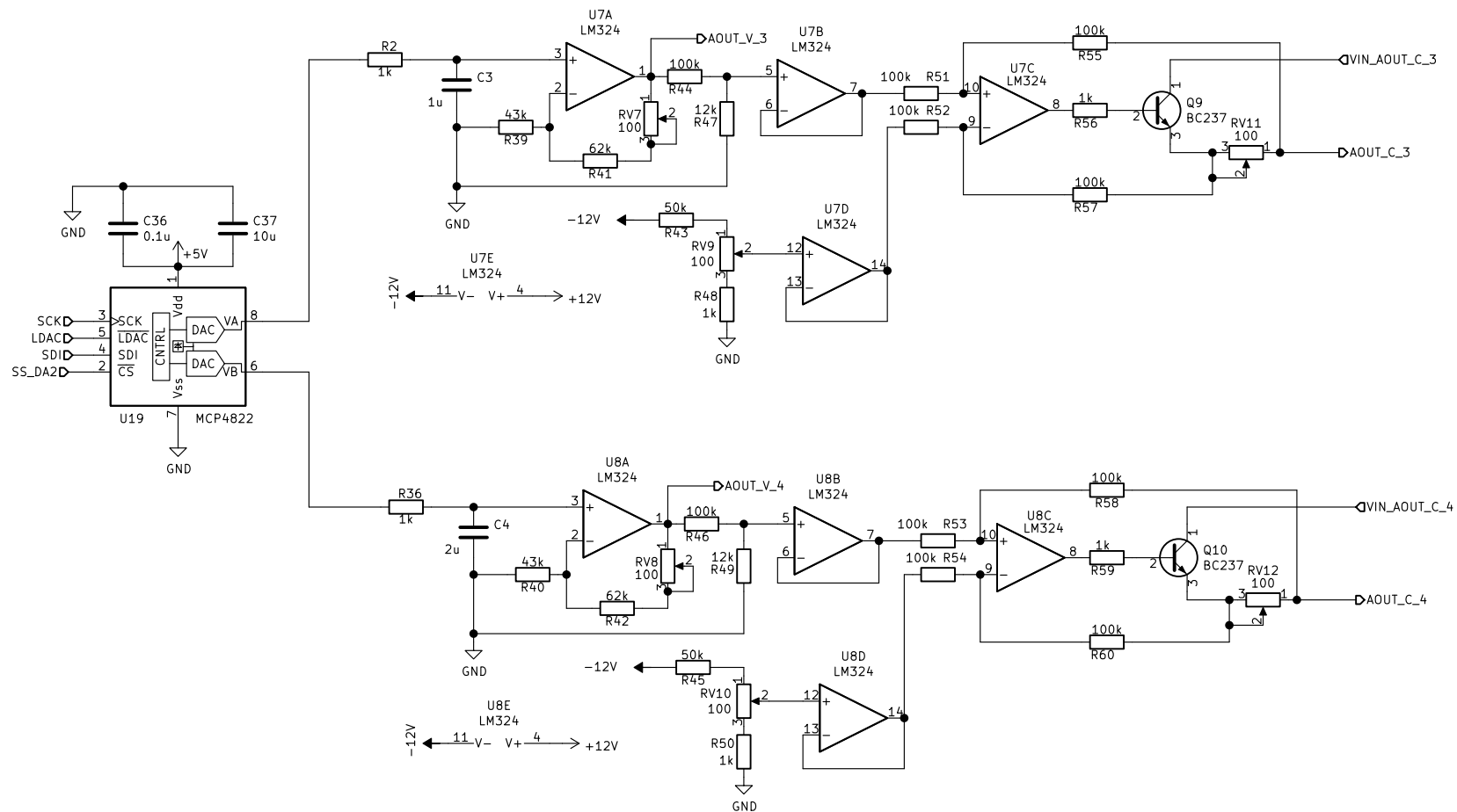
Size: A4 Date: 2021-12-06

KiCad E.D.A. kicad (5.1.5)-3

**Rev: 1.12**

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(4-20mA/0-10V/NPN)

Salidas 3 y 4

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Sheet: /S\_Analog\_2/

File: S\_Analog\_2.sch

**Title: Salidas Analógicas/PWM de AutoPLC-v1**

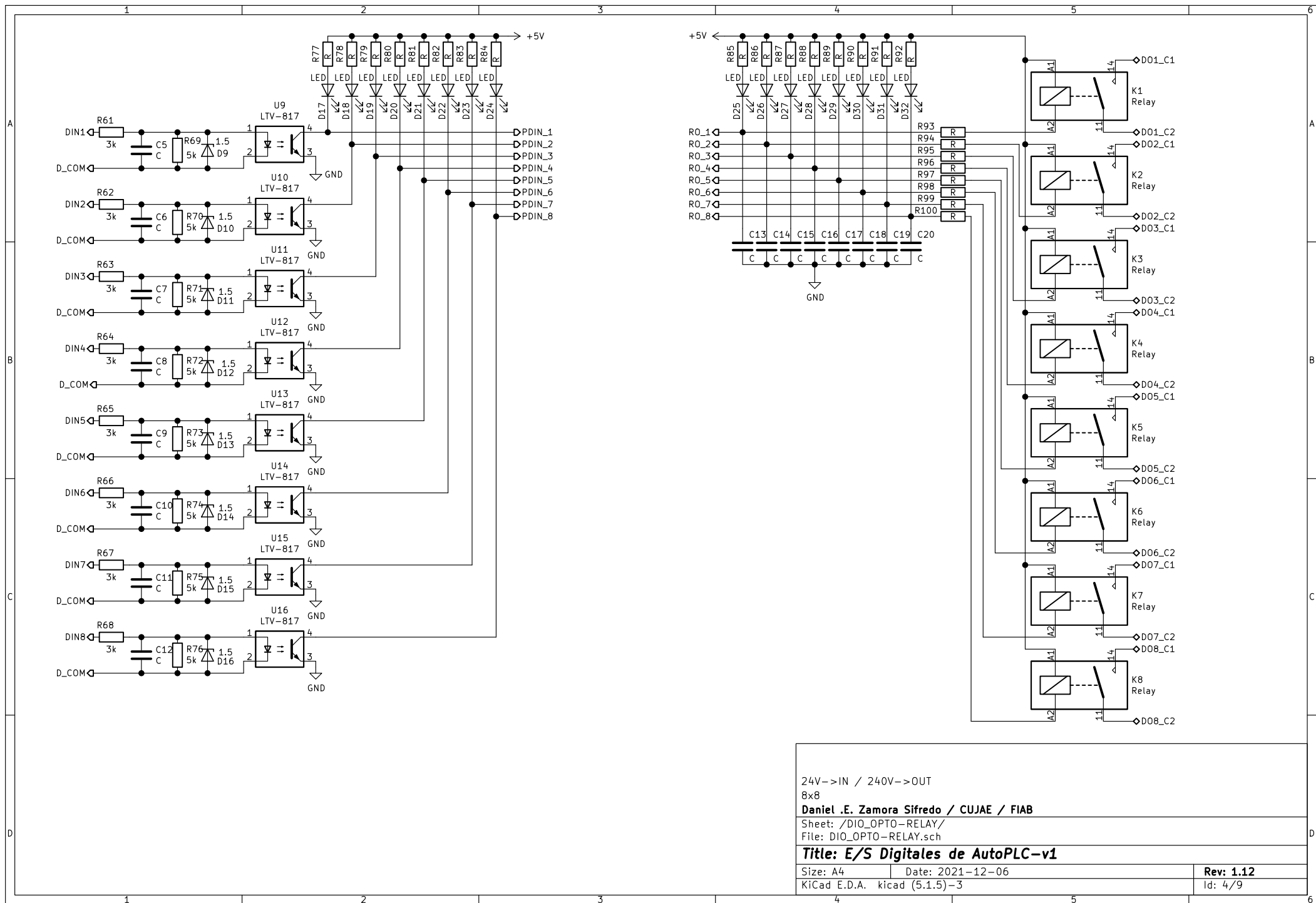
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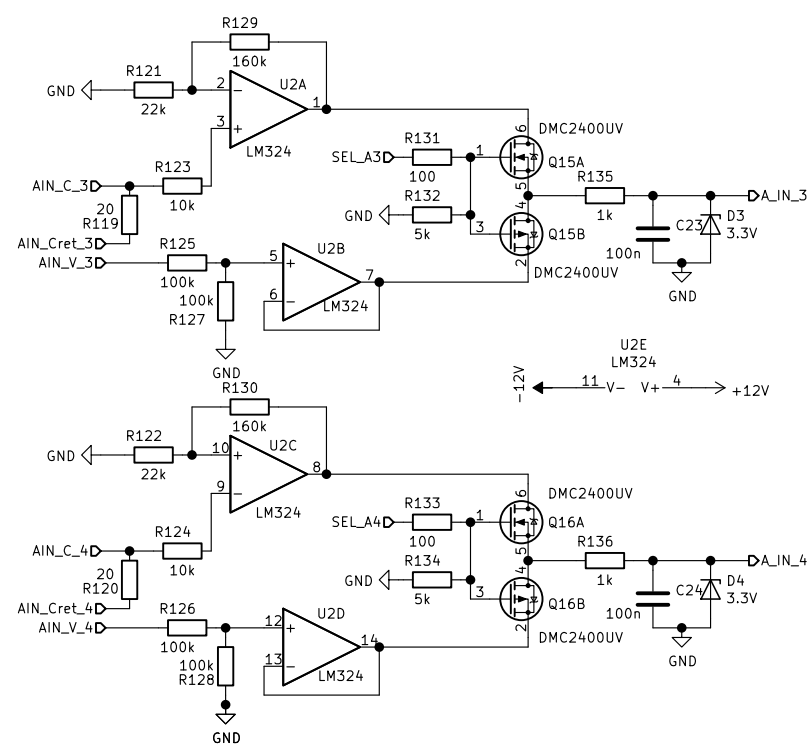
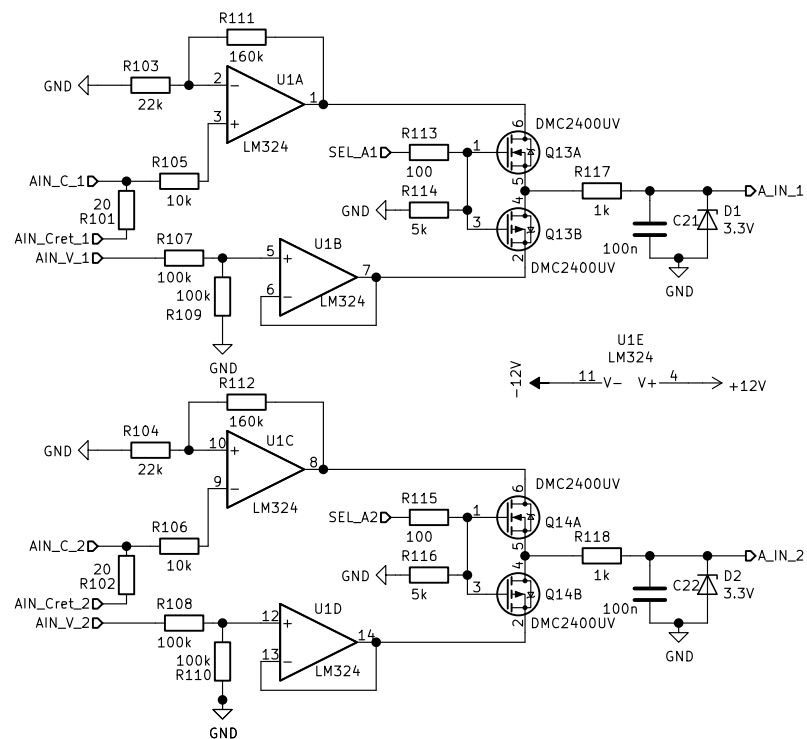
Date: 2021-12-06

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(4-20mA/0-10V)

Entradas 1,2,3,4

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Sheet: /E\_Analog\_1/

File: E\_Analog\_1.sch

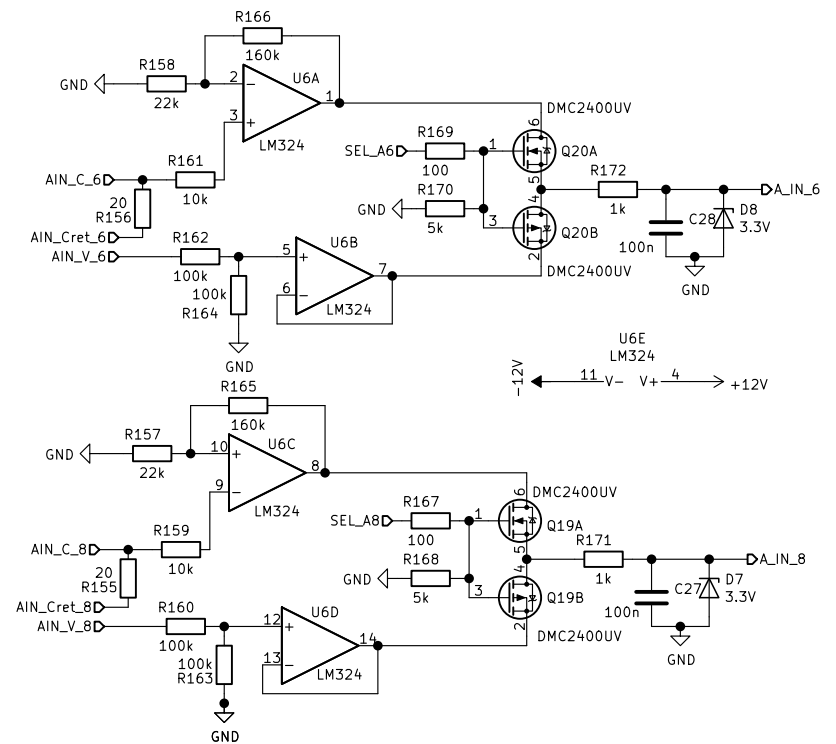
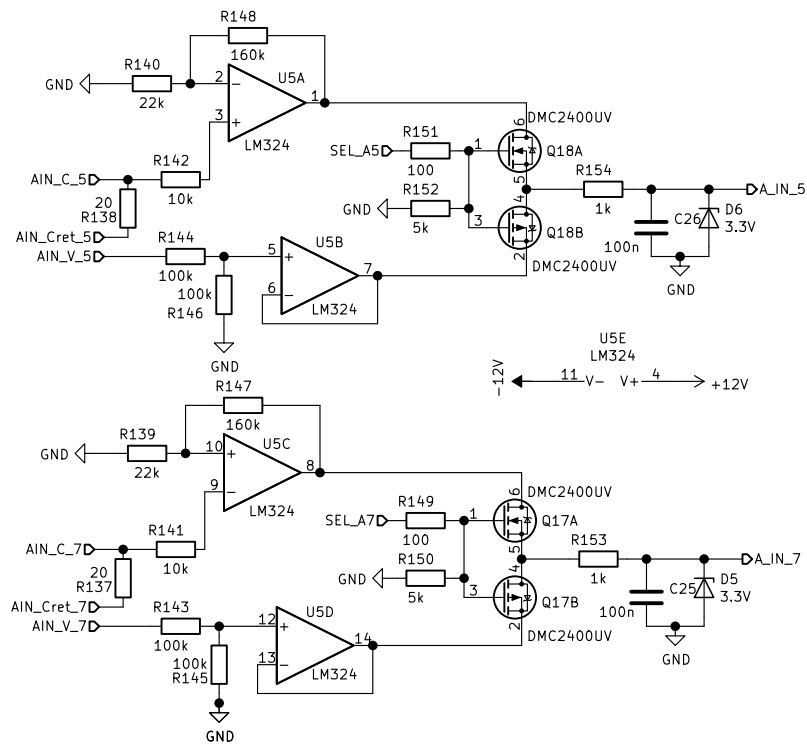
**Title: Entradas Analógicas de AutoPLC-v1**

Size: A4 Date: 2021-12-06

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Rev: 1.12

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(4-20mA/0-10V)

Entradas 5,6,7,8

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Sheet: /E\_Analog\_2/

File: E\_Analog\_2.sch

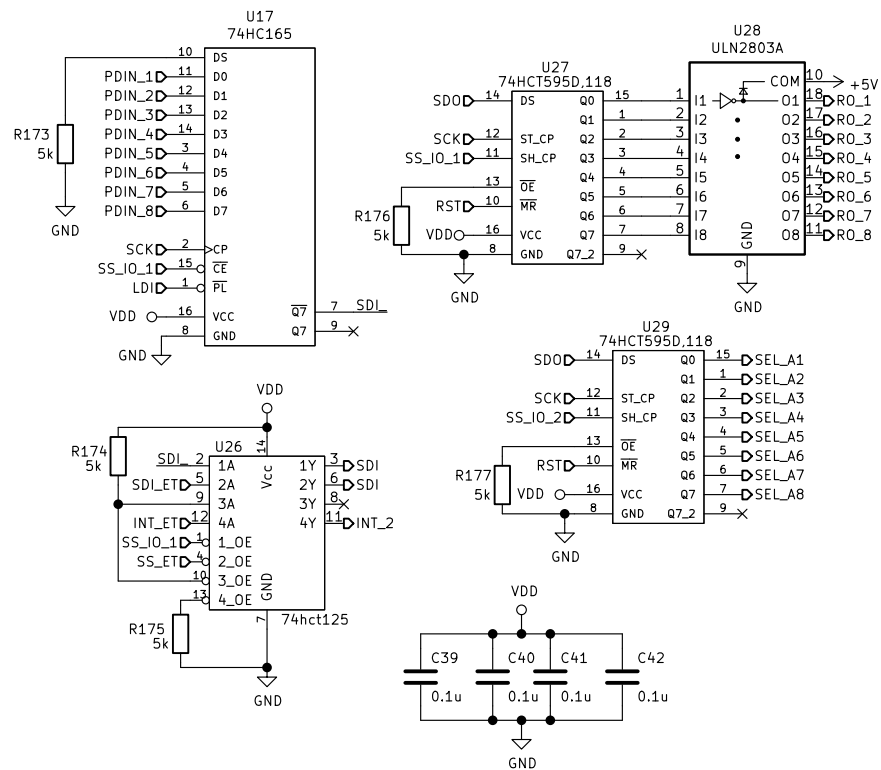
**Title: Entradas Analógicas de AutoPLC-v1**

Size: A4 Date: 2021-12-06

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**Rev: 1.12**

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\*las entradas digitales ya no están invertidas  
 \* SS\_IO\_2 es para el puerto de configuración de entradas analógicas  
 \* LDI es el bit de carga paralelo  
 \* Señales SPI del módulo Ethernet (3.3V -> VDD)

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Sheet: /DIO\_SPI/  
 File: DIO\_SPI.sch

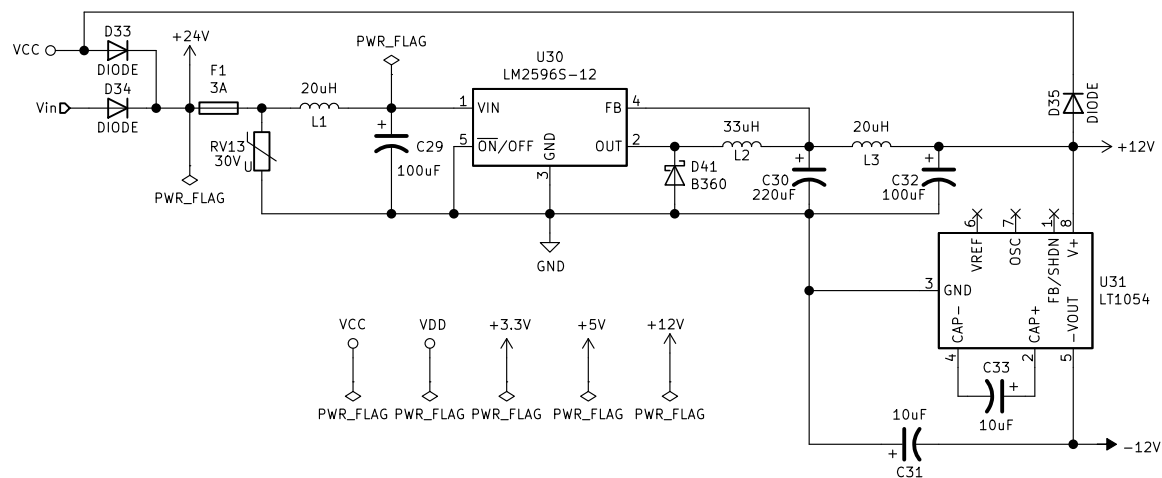
**Title: Puertos SPI, AutoPLC-v1**

Size: A4 Date: 2021-12-06

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Fuente +12V hasta 3A  
Fuente -12V solo para AOPs  
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Sheet: /Power/  
File: Power.sch

**Title: Fuente +12V/-12V (AutoPLC-v1)**

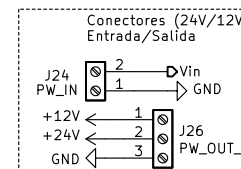
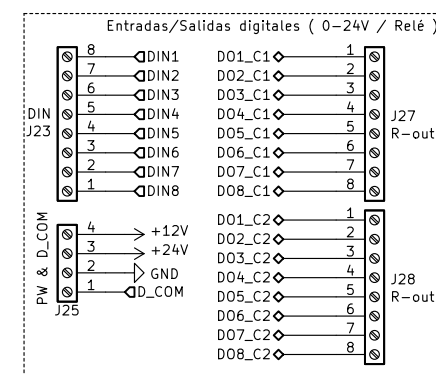
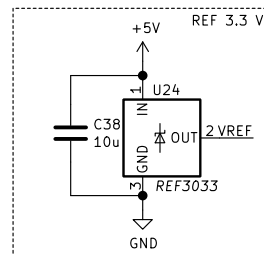
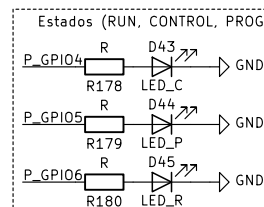
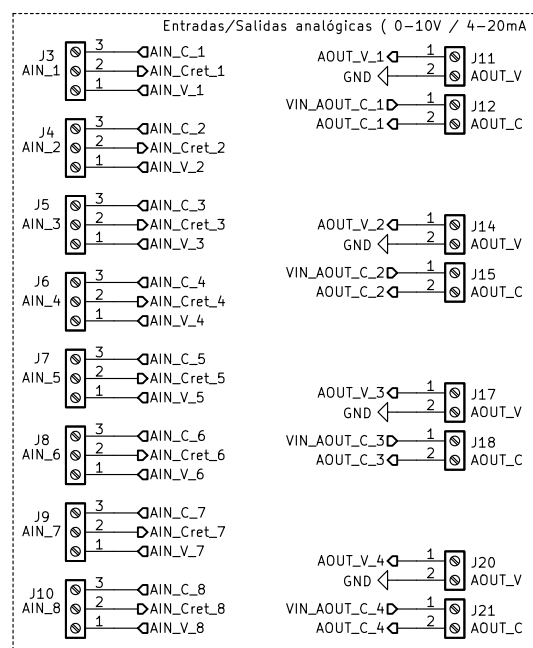
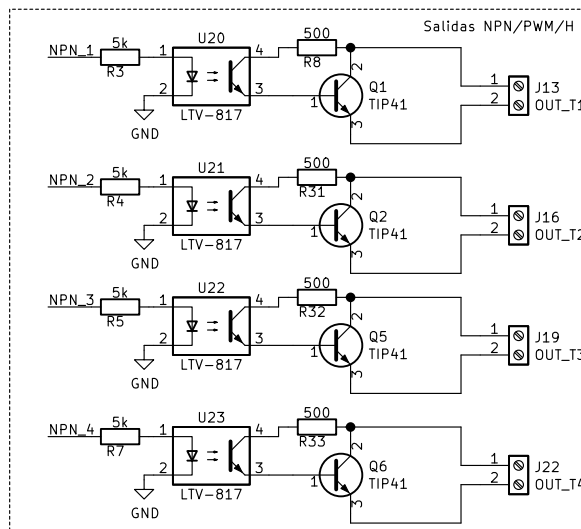
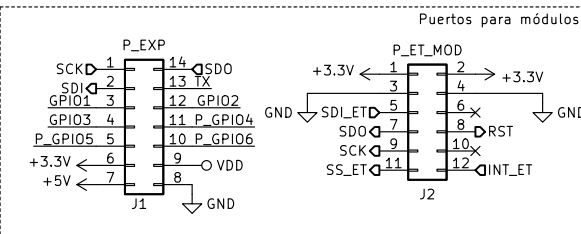
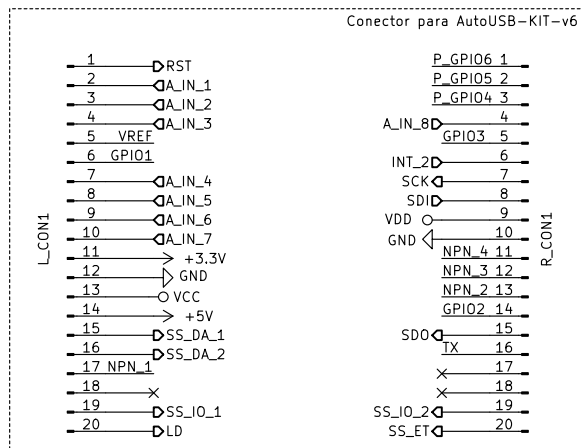
Size: A4 Date: 2021-12-06

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**Rev: 1.12**

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Referencia de 3.3V  
Puertos para módulos  
LEDs indicadores de estado  
Salidas a transistor para PWM y puente H  
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Sheet: /Terminales/  
File: Terminales.sch

**Title: Salidas Analógicas/PWM de AutoPLC-v1**

Size: A4 Date: 2021-12-06

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**Rev: 1.12**

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