

Bloques

Daniel .E. Zamora Sifredo / CUJAE / FIAB

Sheet: /

File: AutoPLC-v1.sch

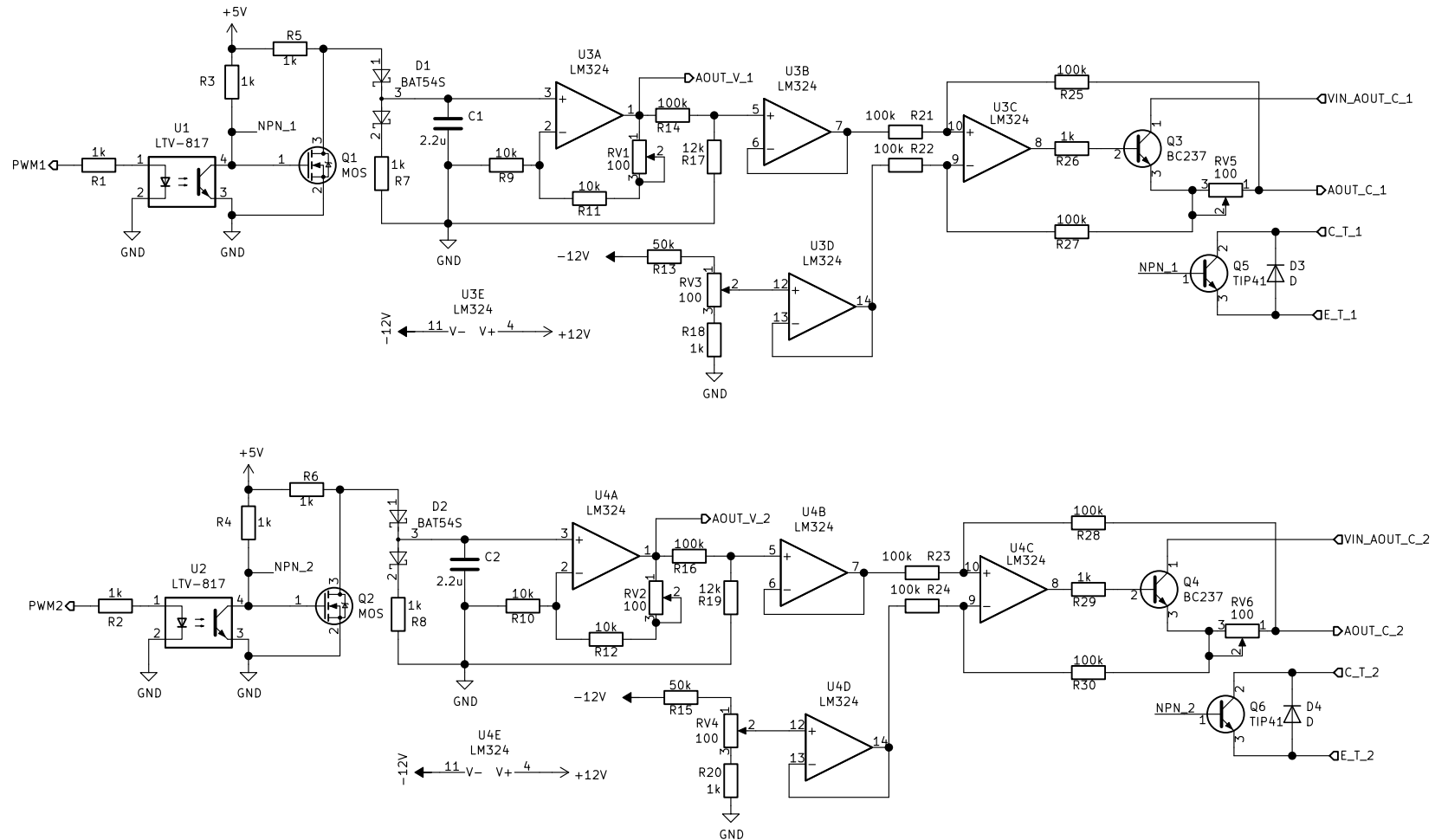
Title: AutoPLC-v1

Size: A4 Date: 2021-11-06

KiCad E.D.A. kicad (5.1.5)-3

Rev: 1.0

Id: 1/9



(4–20mA/0–10V/NPN)

Salidas 1 y 2

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Sheet: /S_Analog_1/

File: S_Analog_1.sch

Title: Salidas Analógicas/PWM de AutoPLC–v1

Size: A4

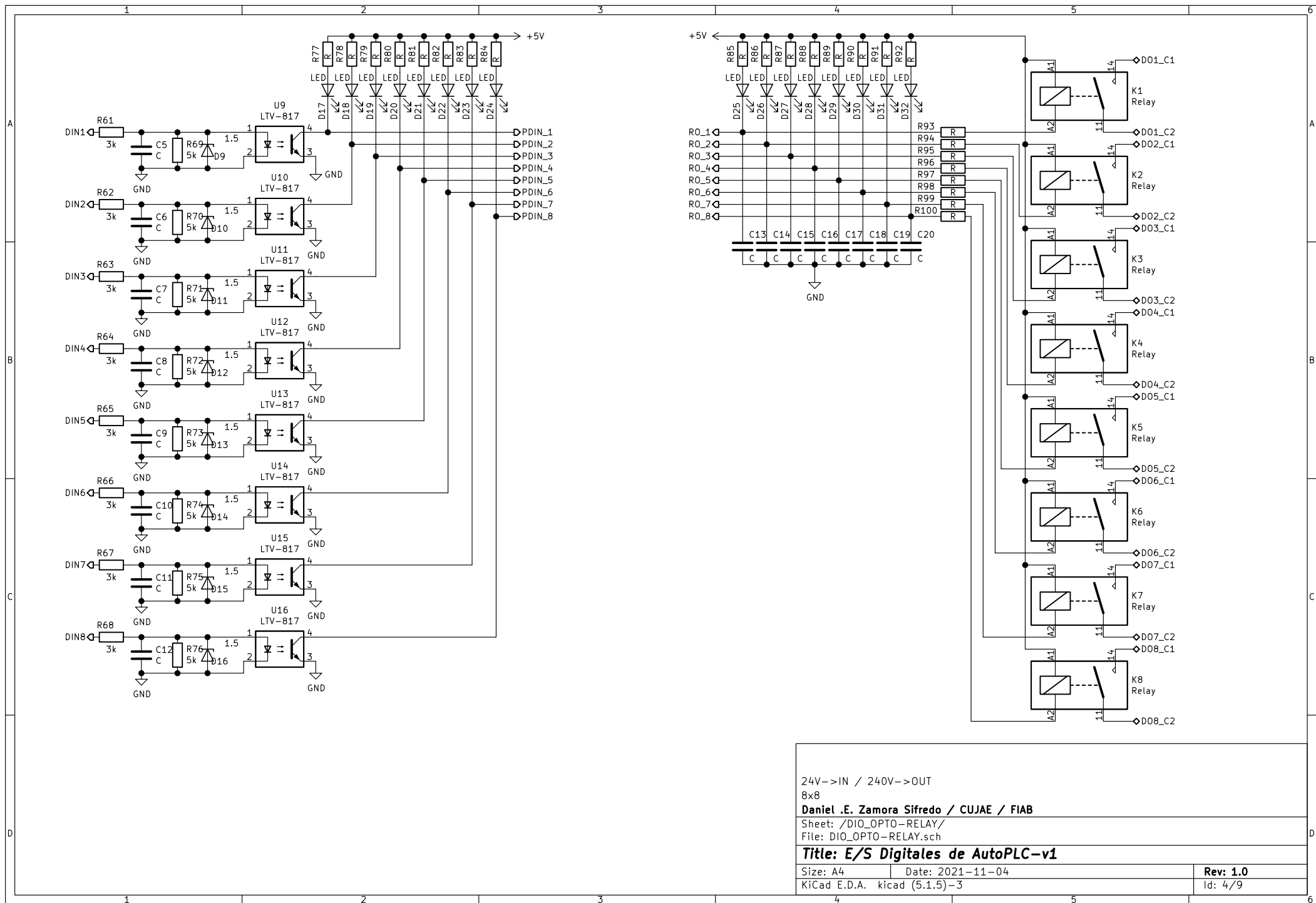
Date: 2021–11–04

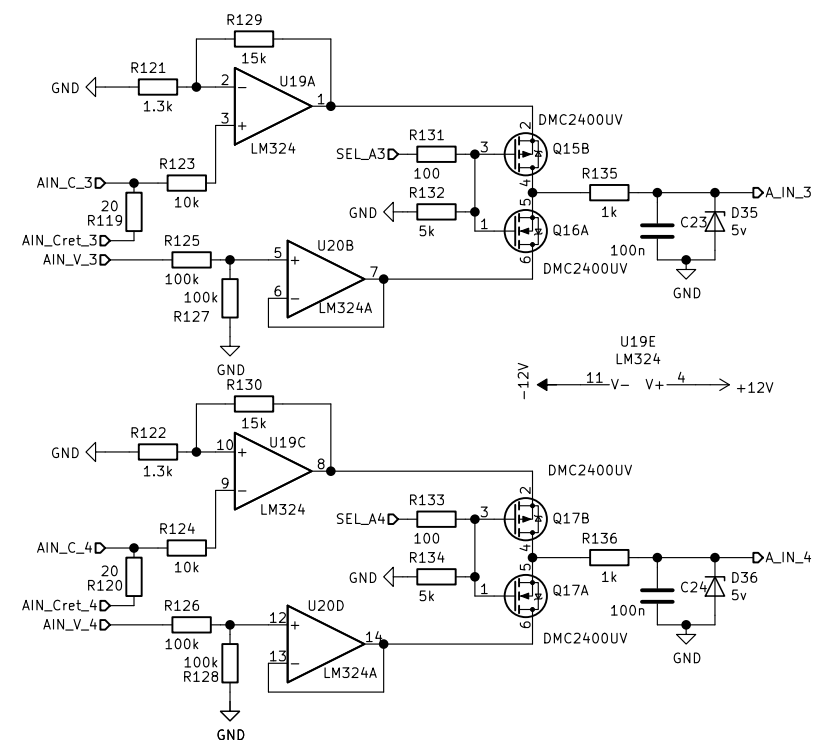
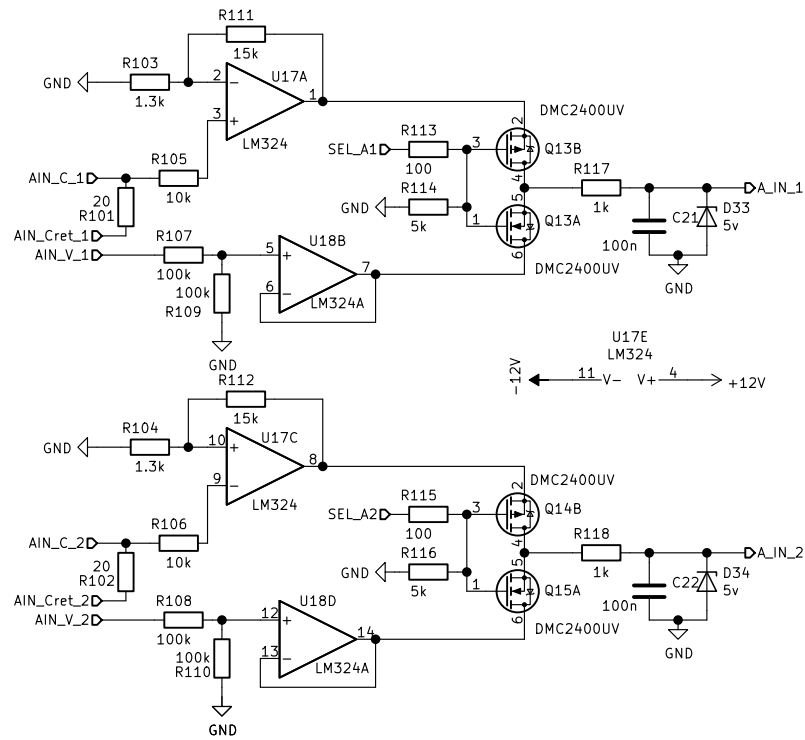
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(4-20mA/0-10V)

Entradas 1,2,3,4

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Sheet: /E_Analog_1/

File: E_Analog_1.sch

Title: Entradas Analógicas de AutoPLC-v1

Size: A4 Date: 2021-11-04

KiCad E.D.A. kicad (5.1.5)-3

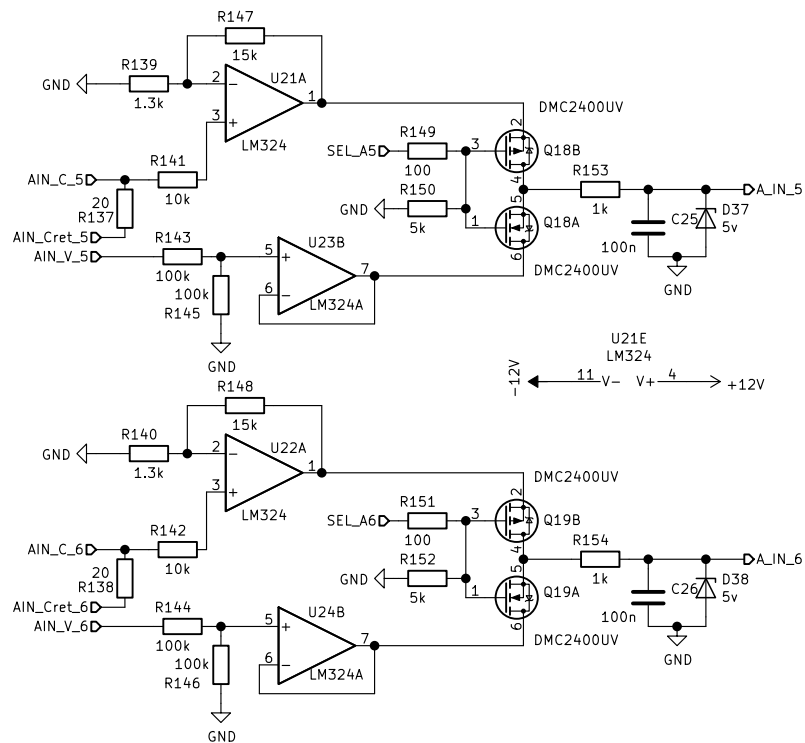
Rev: 1.0

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1 2 3 4 5 6

A

A

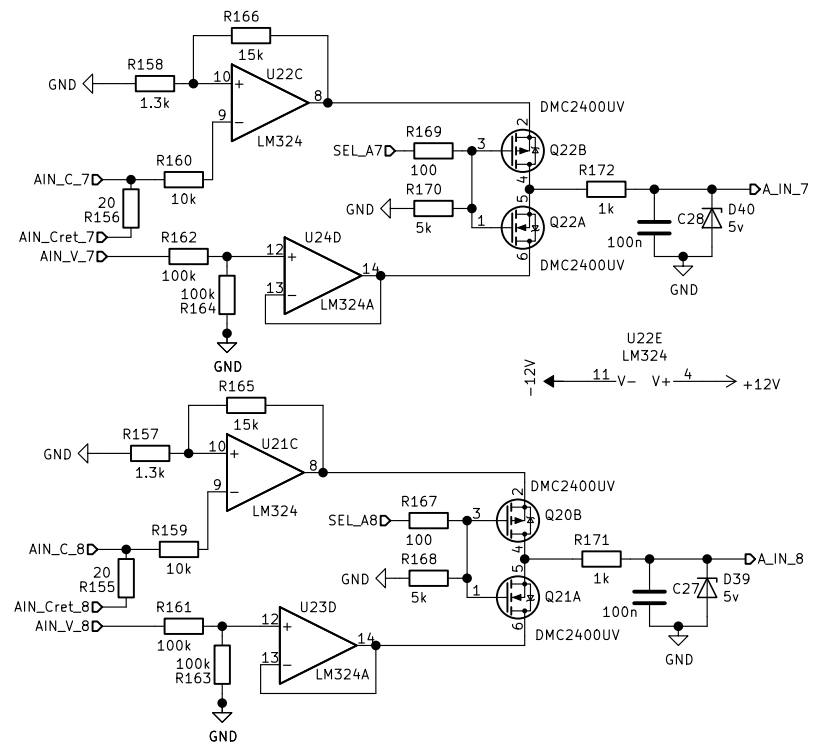


B

B

C

C



U22E
LM324
-12V 11 V- V+ 4 +12V

(4-20mA/0-10V)

Entradas 5,6,7,8

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Sheet: /E_Analog_2/

File: E_Analog_2.sch

Title: Entradas Analógicas de AutoPLC-v1

Size: A4 Date: 2021-11-04

KiCad E.D.A. kicad (5.1.5)-3

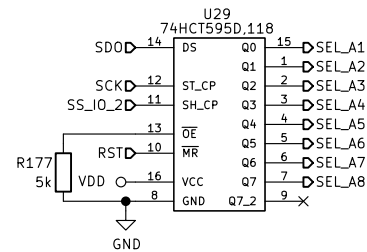
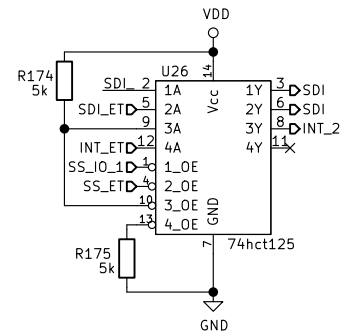
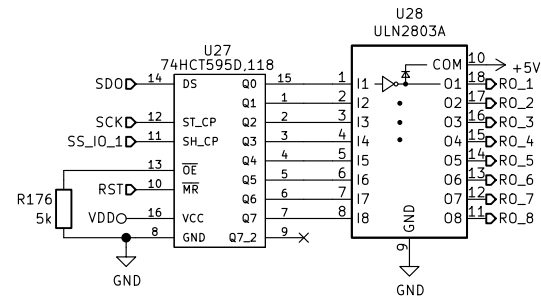
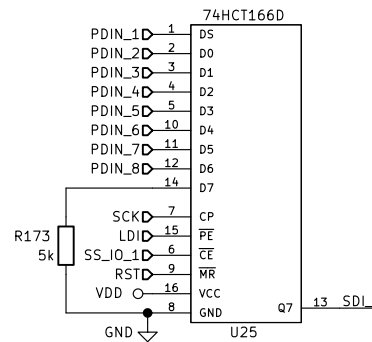
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1 2 3 4 5 6

D

D



- * Se debe dar un pulso de SCK para carga paralelo antes de leer entradas
- * SS_IO_2 es para el puerto de configuración de entradas analógicas
- * LDI es el bit de carga paralelo. Se debe activar despues de SS_IO_1
- * Señales SPI del módulo Ethernet (3.3V -> VDD)

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Sheet: /DIO_SPI/

File: DIO_SPI.sch

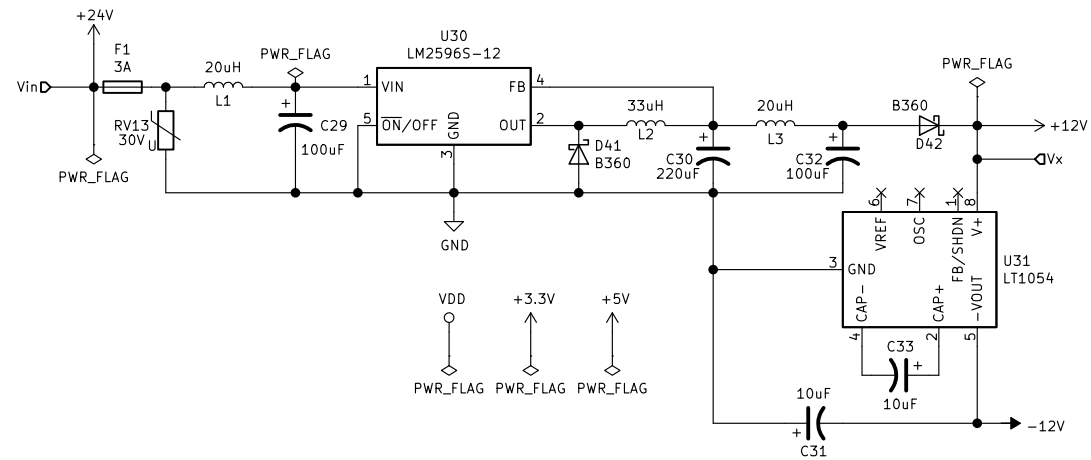
Title: Puertos SPI, AutoPLC-v1

Size: A4 Date: 2021-11-06

KiCad E.D.A. kicad (5.1.5)-3

Rev: 1.0

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Fuente +12V hasta 3A
Fuente -12V solo para AOPs
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Sheet: /Power/
File: Power.sch

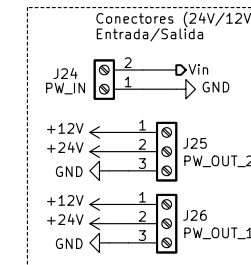
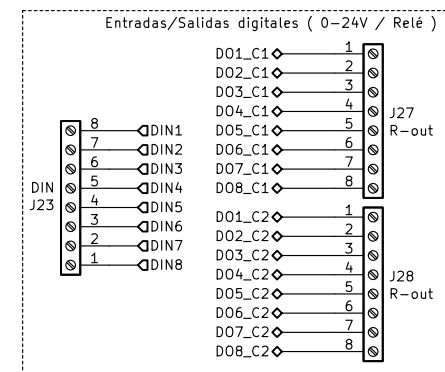
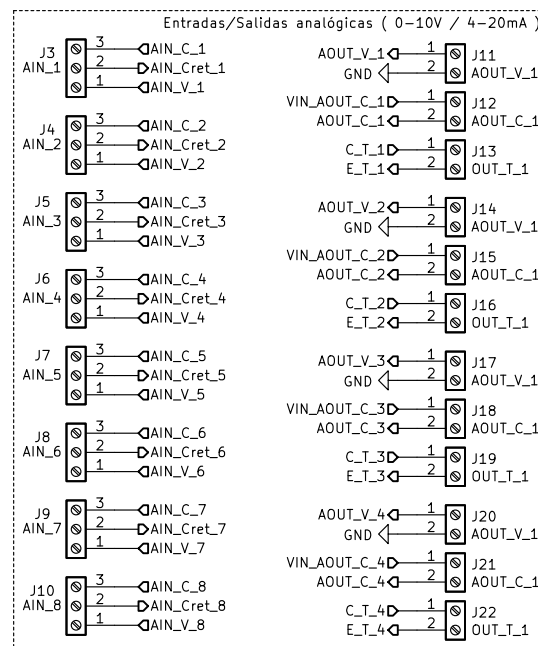
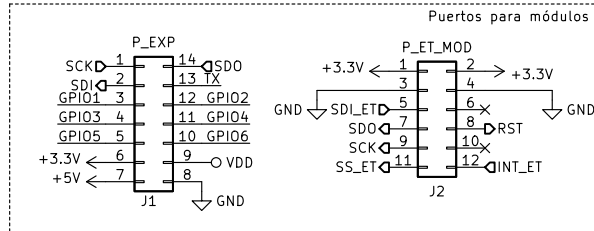
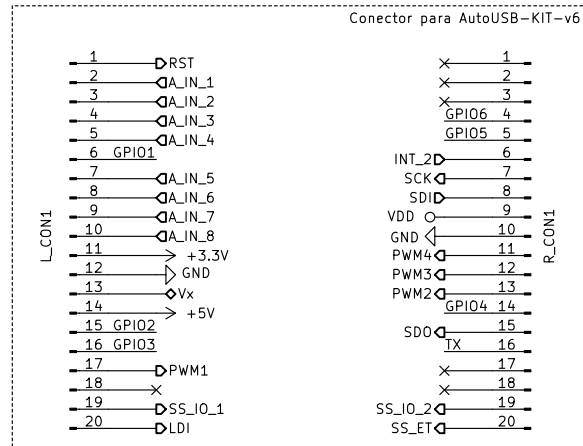
Title: Fuente +12V/-12V (AutoPLC-v1)

Size: A4 Date: 2021-11-04

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Rev: 1.0

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Puerto para módulo Ethernet
(4-20mA/0-10V/NPN)
Salidas 3 y 4

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Sheet: /Terminales/

File: Terminales.sch

Title: Salidas Analógicas/PWM de AutoPLC-v1

Size: A4 Date: 2021-11-04

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Rev: 1.0

Id: 9/9