Attenuator
Amplifier
PreAmp
Ground

F1
F2
F3
P1

D1
D2
D3

R3
R31 R32

C8

R2 R29 R30
C6
R1 R27 R28
C2 C1
C4
TVS2
U1
R33
R34
TVS1
U3
Z2
Z1

U4
R35
TVS3 C3
U5
Z3

U6

C7
C11
C5
C10

C9
C12

0 1 2 3 4 5

D9 D8 D7 D6 D5 D4

R17 R18 | Current Limiting Resistors

JP1
Latch Enable
I/O — ON

JP2
Serial — Parrallel

DIP1

D0 D1 D2 D3 D4 D5

P2

GNDD SDI CLK LE
SDO
+5V Attenuator

R9 R8 R7 R6 R5 R4
Pull Down Resistors

RF Array Signal Chain Amplifier and Attenuator
Motherboard
Revision 6.  2018 — 01 — 19
Brian Markley — University of Alberta
CERN Open Hardware Licence v1.2

P3
P5
P7
P9
P11
P13
P15
P17
P19
P21
P23
P25
P27
P29
P31
P33
P35
Edge of PCB

P4
P6
P8
P10
P12
P14
P16
P18
P20
P22
P24
P26
P28
P30
P32
P34
P36
Edge of PCB