Dariusz Fertyk

Personal Info

Date of birth
17.06.1990

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503563821

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dariusz.fertyk@gmail.com

Experience

10.2015 - Novamedia Innovision Sp. z o.o.
present *Technical Leader of Hardware and Firmware Department*
Leading team of engineers, development and debugging of RTOS based applications for STM32 and PIC32 microcontrollers, presenting new devices during business meetings.

07.2014 - Novamedia Innovision Sp. z o.o.
09.2015 *Hardware and Firmware Engineer*
Hardware and firmware design of wireless routers and LED displays.

06.2013 - Samsung Electronics Poland
06.2014 *Junior Software Engineer*
Developing new features and low-level software debugging of set-top boxes.

03.2013 - Samsung Electronics Poland
05.2013 *Intern*
Engineer Algorithm internship.

07.2012 - Agregaty Polska SP. z o.o.
02.2013 *Firmware Engineer*
Development of low-level libraries for AVR microcontrollers.

Education

2013 - Master of Science Engineer
2015 Poznan University of Technology, Faculty of Electronics and Telecomunications, very good.
Thesis title: Measurement systems and firmware design for flying rotorcraft model.

2008 - Bachelor of Science Engineer
2013 Poznan University of Technology, Faculty of Electronics and Telecomunications, good plus.
Thesis title: Functional testing of D/A converters with serial interfaces.

Skills

Skill	Level
C programming	●●●●○
Assembly programming	●●●●○
SW/WH debugging skills	●●●●○
Digital electronics	●●●●○
Verilog	●●●○○
RTOS knowledge	●●●○○
Linux program development	●●●○○
Git, SVN	●●●○○
ARM Cortex-M3 core architecture	●●○○○
C++ programming	●●○○○

Languages

English ●●●●○

French ●○○○○

Interests

Dance

Astronomy

Robotics

Polecony przez: **Szymon Wawrzyniak**