Monday Tuesday			Wednesday		Thursday		Friday		
8:00	Worlday	8:00	raccaay	8:00	Woundeday	8:00	maroday	8:00	Thady
AM		AM	EGR 304	AM		AM	EGR 304	AM	
11		04	Microprocessor Interfacing	31		44	Microprocessor Interfacing	51	
8:50		21	Room SB108	8:50		41	Room SB108	8:50	
9:00	ECD 117.01	0.45		9:00		0.45		9:00	EGD 117 01
AM	EGR 117-91 Introduction to	9:15 9:25		AM	Office	9:15 9:25		AM	EGR 117-91 Introduction to
12	Engineering Electronics CL1320	AM		32		AM		52	Engineering Electronics CL1320
9:50	(March 7 through May 2)			9:50				9:50	(March 7 through May 2)
10:00 AM	EGR 117-92	22		10:00 AM		42		10:00 AM	EGR 117-92
	Introduction to	40.40				40.40			Introduction to
13	Engineering Electronics CL1320	10:40		33		10:40 10:50		53	Engineering Electronics CL1320
10:50	(March 7 through May 2)	11.00	Free Time	10:50			Free Time	10:50	(March 7 through May 2)
11:00 AM		11:00 AM		11:00 AM		11:00 AM		11:00 AM	
14	Office			34	CHAPEL			54	Office
		23				43		1	
11:50 12:00				11:50 12:00			Lunch	11:50 12:00	
PM	EGR 363	12:15		PM	EGR 363	12:15		PM	EGR 363
15	Introduction to Communication Systems	12:25 PM	Lunch	35	Introduction to	12:25 PM	EGR 363	55	Introduction to Communication Systems
	Room CL1318	FIVI	Lulicii		Communication Systems Room CL1318		Introduction to		Room CL1318
12:50 1:00		24		12:50		44	Communication Systems Room SB 235	12:50	
PM						1:15	Room SD 233		
16	Lunch	1:40		36	Lunch	1:40		56	Lunch
1:50			Free Time	1:50			Free Time	1:50	
2:00		2:00		2:00		2:00		2:00	
PM	2:00 – 5:00 PM	PM		PM		PM		PM	
17	EGR 304L			37				57	
2:50		2:50		2:50		2:50		2:50	
3:00 PM		3:00 PM		3:00 PM		3:00 PM		3:00 PM	
		FIVI				FIVI			
18	Microprocessor Interfacing lab			38				58	Engineering Department
3:50	8	3:50		3:50		3:50		Room	Meeting
	Room SB233	4:00 PM		4:00 PM		4:00 PM		4:00 PM	
19				39				59	
		4.50							
4:50 5:00		4:50 5:00		4:50 5:00		4:50 5:00		4:50 5:00	
PM		PM		PM		PM		PM	
01		02		03		04		05	
6:45		6:45		6:45		6:45		6:45	
PM		PM		PM		PM		PM	
10		20		30		40		50	