

PSD@CBM firmware description (draft, for personal use)

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Актуальная версия документа доступна для скачивания по ссылке
https://github.com/dfinogee/FIT-readout-manual/raw/master/FIT_readout_manual.pdf

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1 ADC data processing

1.1 Channel data collecting

Each channel collect data in FIFO in hit packet format and emite ready signal after data pushed. Ready signal is synchronous to signal treshold crossing and used for event ADC timestamp 1.

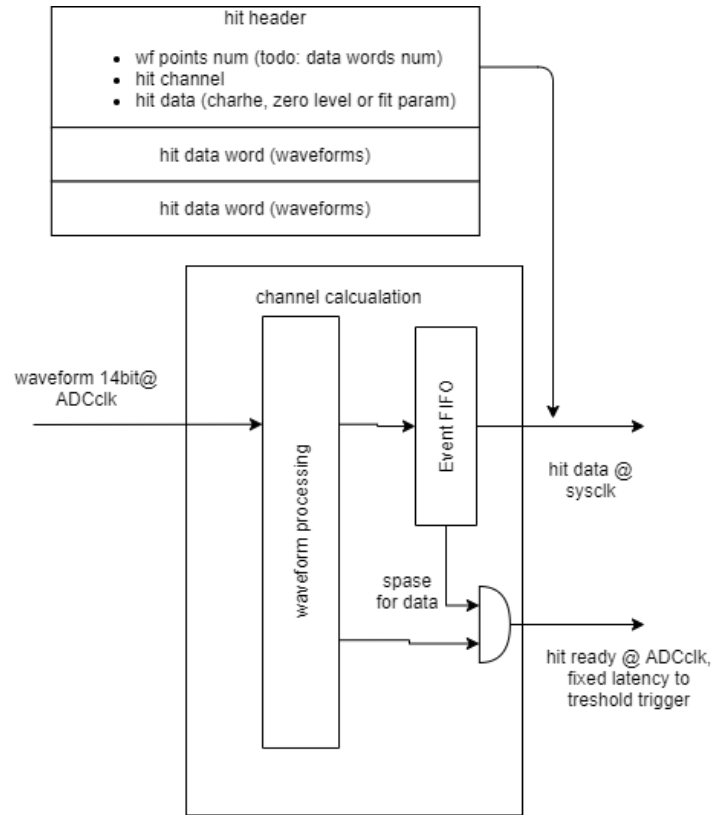


Рис. 1: Data collecting scheme for single channel