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CPSC 260: Assignment #8 Responses

Problem 1:

movl	%eax	(%rsp)
movw	(%rax)	%dx
movb	\$0xFF	%bl
movb	(%rsp, %rdx, 4)	%dl
movq	(%rdx)	%rax
movw	%dx	(%rax)

^{*}answers are highlighted in yellow

Problem 2:

movb	\$0xF	(%ebx)	%ebx is not an address register, can't be dereferenced
movl	%rax	(%rsp)	Instruction suffix and register ID do not match in size
movw	(%rax)	4(%rsp)	Memory references can't be both the source and destination
movb	%al	%sl	%sl does not exist in register library
movq	%rax	\$0x123	Immediate/literal values cannot be destination
movl	%eax	\$rdx	Wrong size for the destination operand
movb	%si	8(%rbp)	Instruction suffix and register ID do not match

^{*}answers are highlighted in yellow

Problem 3 (Screenshots):

```
~/cpsc260-hw8$ gcc -c -g switch_case.s
~/cpsc260-hw8$ ld switch_case.o -o switch_case
~/cpsc260-hw8$ ./switch_case
foobar
~/cpsc260-hw8$
58 subb $32, %bl # make current character
59 movb %bl, (%r9) # store result as pointer
60 retq
63 to_lower_case:
64 addb $32,
65 movb %bl,
      addb $32, %bl  # make current character lowercase
movb %bl, (%r9)  # store result as pointer
        reta
                .equ mylen, (. - mystr)
Shell × □ switch_case.s × +
                                                                                                                                                               ~/cpsc260-hw8$ gcc -c -g switch_case.s
~/cpsc260-hw8$ ld switch_case.o -o switch_case
~/cpsc260-hw8$ ./switch_case
FOO_BAR!
~/cpsc260-hw8$
 switch case.s
57 to_upper_case:
58 subb $32, %bl  # make current character uppercase
59 movb %bl, (%r9) # store result as pointer
60 retq
      addb $32, %bl # make current character
movb %bl, (%r9) # store result as pointer
        retq
68 .data
69 mystr: .ascii "foo_bar!\n"
                   .equ mylen, (. - mystr)
```

Problem 4 (Screenshots):

```
C sort.c □ × □ selection_sort.s × +
                                                                   ··· 

Shell × +
                                                                      ~/cpsc260-hw8$ gcc -g -z execstack sort.c selection_sort.s -o sort
~/cpsc260-hw8$ ./sort
1 2 2 4 5 5 7 9
1 2 3 4 9
0 1 2 3 3 4 5 6 8 9
~/cpsc260-hw8$
  3 void selection_sort(int array[], int length);
  4 void c_sort(int array[], int length);
  6 v int main(){
        int array1[] = {2, 5, 7, 2, 4, 5, 9, 1};
        int array2[] = {1, 3, 9, 2, 4};
        int array3[] = {8, 4, 1, 3, 9, 3, 5, 6, 0, 2};
        selection_sort(array1, 8);
        for (int i = 0; i < 8; i++){
         printf("%d ", array1[i]);
        printf("\n");
        selection_sort(array2, 5);
 22 ,
         printf("%d ", array2[i]);
        printf("\n");
        selection_sort(array3, 10);
         for (int i = 0; i < 10; i++){
         printf("%d ", array3[i]);
        printf("\n");
        return 0;
```

Problem 5 (Screenshots and Observations):

Object Dump Screenshots:

```
test %esi,%esi
jle 401318 <csort+0xa8>
push %r12
xor %r10d,%r10d
push %rbp
lea 0x4(%rdi),%rbp
push %rbx
mov %rbp,%r9
mov %rdi,%rbx
nopl 0x0(%rax,%r11d
mov %r10d,%eax
mov %r9,%rdx
movslq %r10d,%r8
mov %r11d,%edi
add $0x1,%eax
cmp %eax,%esi
jle 40120d <c sort+0x40
mov (%rdx),%ecx
add $0x4,%ray
cmp %ecx,%edi
jle 4012a0 <c sort+0x30>
movslq %r10d,%r10d
mov %r11d,%r10d
mov %r11d,%r2d
mov %r1x,%eax
cmp %ecx,%edi
jle 4012a0 <c sort+0x30>
movslq %eax,%r3
add
$0x1,%eax
cmp %ecx,%edi
cmp %eax,%r3
add
$0x1,%eax
mov %ecx,%edi
cmp %eax,%r3
add
$0x1,%eax
cmp %ecx,%edi
cmp %eax,%r8
add
$0x1,%eax
cmp %eax,%esi
jg 4012a7 <c_sort+0x30>
mov %r3x),%edx
mov %edx,%esi
jg 4012a7 <c_sort+0x30>
mov %r3x),%edx
mov %edx,%r10d,%eax
cmp %eax,%r8idi
je 4012e0 <c_sort+0x70>
mov %eax,%r10d
jmp 401290 <c_sort+0x20>
mov %eax,%r10d
jmp 401290 <c_sort+0x20>
mov %e(xrax,%rax,1)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             Qt
                                                                                                                                                                                                                                                                              4012a0 <__sort+0x30>

%eax,%r8

$0x1,%eax

%ecx,%edi

%eax,%es;

4012a7 <c_sort+0x37>

(%rbx,%r9,4),%rax

$0x4,%r9

(%rax),%edx

%edx,-0x8(%r9)

%r11d,(%rax)

9x1(%r10),%eax

%eax,%esi

4012e0 <c_sort+0x70>

%eax,%r10d

401290 <c_sort+0x20>

9x0(%rax,%rax,1)
                                                                                                                                                                                                                                       jmp
nopl
lea
lea
                                                                                                                                                                                                                                                                                   0x0(%rax,%rax,1)
0x0(%rbp,%r10,4),%r12
0xd18(%rip),%rbp
                                                                                                                                                                                                                                                                                                                                                                                                                                           # 402004 <_I0_std
        in used+0x4>
                                                                     0f 1f 40 00 8b 13 48 89 ee bf 01 00 00 00 31 c0 48 83 33 04 64 65 bf df ff ff 4c 39 e3 75 e6 5b bf 0a 00 00 00 5d 41 5c e9 18 fd ff ff bf 0a 00 00 00 e9 0e fd ff ff
             4012ec:
4012f0:
4012f2:
4012f5:
4012f6:
4012fc:
401300:
401305:
401308:
40130a:
401310:
401311:
401313:
                                                                                                                                                                                                                                      mov
mov
mov
xor
add
call
cmp
jne
pop
mov
pop
                                                                                                                                                                                                                                                                                  (%rbx),%edx
%rbp,%rsi
$0x1,%edi
%eax,%eax
$0x4,%rbx
40:1060 <_printf_chk@plt>
%r12,%rbx
40:12f0 <<_sort+0x80>
%rbx
$0xa,%edi
                                                                                                                                                                                                                                                                                   %r12
401030 <putchar@plt>
                401318:
40131d:
                                                                                                                                                                                                                                                                                     $0xa,%edi
401030 <putchar@plt>
```

Testing Results Screenshot:

```
C sort.c □ × □ selection_sort.s × +

    Shell × +

                                                                             ~/cpsc260-hw8$ gcc -g -z execstack sort.c selection_sort.s -o sort
~/cpsc260-hw8$ ./sort
C Program Sort:
1 2 2 4 5 5 7 9
1 2 3 4 9
0 1 2 3 3 4 5 6 8 9
~/cpsc260-hw8$ ■
C sort.c > f main
  3 void selection_sort(int array[], int length);
      void c_sort(int array[], int length);
   6 v int main(){
         // Test arrays for both programs
         int array1[] = {2, 5, 7, 2, 4, 5, 9, 1};
         int array2[] = {1, 3, 9, 2, 4};
  10
         int array3[] = {8, 4, 1, 3, 9, 3, 5, 6, 0, 2};
         printf("C Program Sort:\n");
         c_sort(array1, 8);
         c_sort(array2, 5);
         c_sort(array3, 10);
```

Observations between Object Dump and My Code:

One of the first observations that I made between my own code and the C-compiled assembly is that it decided to push registers onto the stack as they were needed as variables in the function. I think the reasoning behind this is that I had time to think about the structure of my function and what registers I would use ahead of time. Since the compiler is simply reading in lines of my C code, it does not have this luxury and, therefore, pushes available registers onto the stack before altering them.

Another observation was the compiler's use of the lea function when loading effective addresses. An interesting tactic that I noticed was the use of a negative shift in line 401290. It calls the dereferenced %r9 register with a -4 shift: -0x4(%r9), %r11d. I have never worked with a shift like this and I am curious to know how this could possibly optimize my own function.

Lastly, I noticed the use of a nopl assembly call, which I have not seen before. This would be another call to investigate its purpose as it was often associated with the dereferencing of a register on multiple occasions. Overall, though there are some instances of optimization, the object dump revealed that my own code and its implementation were roughly the same length and, given the restraints of selection sort, roughly follow the same path but maybe use different registers in completing the same task.