

## ***CPUInfo***

```
+getCacheL1Size(): int  
+getCacheL2Size(): int  
+getCacheL3Size(): int  
+getArchName(): string  
+getSIMDCapabilities(): vector<string>  
+getDigitBitWidth(): int  
+getAvailableCPUs(): int
```



<<singleton>>

**CPUInfo\_x86**

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**CPUInfo\_x86\_64**

...

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**CPUInfo\_generic**