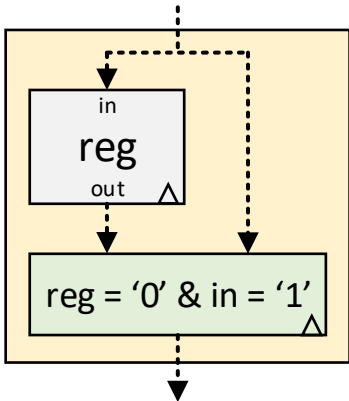


Input spike polarity



Phase-locked spike