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Releasing your creativity

STM32F3 series

Mainstream 32-bit MCUs

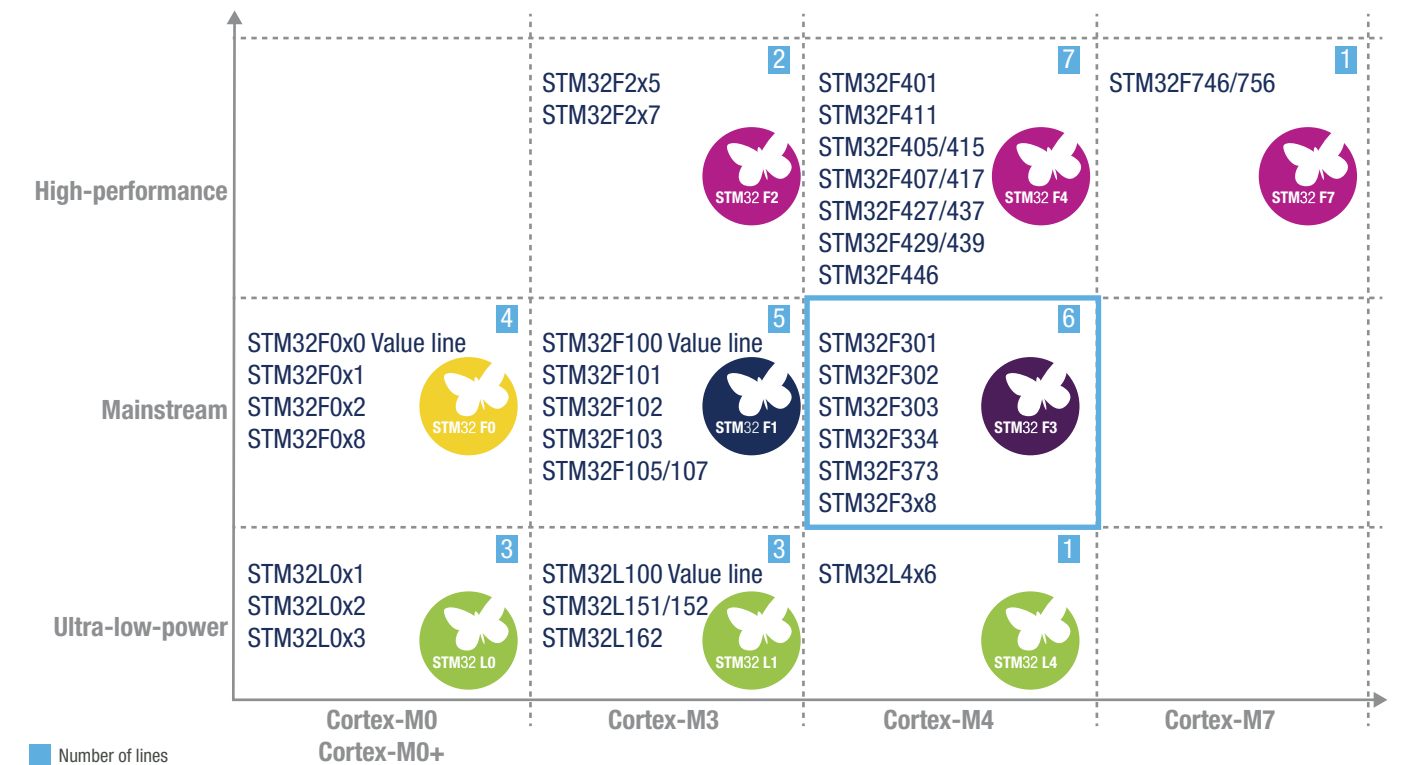


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By choosing one of ST's microcontrollers for your embedded application, you gain from our leading expertise in MCU architecture, technology, multi-source manufacturing and long-term supply.

The STM32® portfolio offers an extraordinary variety of options, now including ARM® Cortex®-M cores (M0, M0+, M3, M4 and M7), giving developers flexibility to find the perfect STM32 for their applications. Particular attention is paid to accommodate porting of applications from one device to another. The binary compatibility combined with the similar pinout assignment, hardware IPs proliferation and higher level programming language makes the development job far more convenient when dealing with the STM32 families.

9 PRODUCT SERIES - 32 PRODUCT LINES



The Mainstream family addresses a large variety of needs found in general-purpose applications.

The STM32 portfolio offers the possibility to boost the performance with more MIPS or better ultra-low power specifications than other microcontroller families. The STM32F3 series is the upgraded class in the Mainstream family thanks to the powerful Cortex-M4 core combined with its advanced digital and analog peripheral set.

The pin compatibility between STM32F1, F0 and F3 series makes navigation across the board extremely convenient



The F3 series extends the scope of ST's STM32 family by allowing designers to tackle mixed-signal control applications. The STM32 F3 series is optimized for efficient handling and processing of mixed signals in applications such as three-phase motor controls, biometrics and industrial sensors, sonars and audio as well as digital power applications including power supplies, lighting, and welding.

STM32F3 key benefits

MAIN FEATURES AND BENEFITS

Features	Benefits
Performance and architecture <ul style="list-style-type: none">72 MHz / 63 DMIPS (from Flash) or 90 DMIPS (from CCM-SRAM)ARM Cortex-M4 with single cycle DSP MAC and floating point unit (FPU)Routine Booster (CCM-SRAM for Core Coupled Memory-SRAM): SRAM mapped to the instruction busAll SRAM with parity bitMemory Protection Unit (MPU)Embedded Trace Macrocell (ETM)Interconnect matrixDMA controllersFlexible Static Memory Controller (FSMC)	<ul style="list-style-type: none">Boosted execution of control algorithmsBetter code efficiencyFast time to marketElimination of scaling and saturationMore performance for critical routines with zero wait state execution from safe CCM-SRAMData and code reliabilityAdvanced debug functionsPeripheral connection flexibility and code size reductionLarge set of external memory accessible up to 36 MHz giving more flexibilityNo code size limit
Outstanding power efficiency <ul style="list-style-type: none">Stop mode down to 6.7 µA (typ.)RTC down to 0.5 µA (typ.) in V_{BAT} mode2.0 to 3.6 V or 1.8 V ± 8% power supply range	<ul style="list-style-type: none">Flexibility to reduce power consumption for applications requiring advanced analog peripherals and low-power modesIdeal for running at low voltages or on a rechargeable battery
Superior and innovative peripherals <ul style="list-style-type: none">Analog: Fast 12-bit ADC at 5 Msps (0.2 µs), Precise 16-bit sigma-delta ADC, Fast and ultra-fast comparators (25 ns), Op amp with PGA (4 gains, 1% accuracy), 12-bit DACsUp to 18 timers: 16- and 32-bit resolution running up to 144 MHzAudio: Simplex or full duplex I²S interfacesLarge set of communication interfaces including USART (9 Mbit/s), SPI/I²S (18 Mbit/s), I²C (1 MHz fast mode plus), CAN (1 Mbit/s), and full-speed USBCyclic redundancy check (CRC)Capacitive touch sensing (24 keys)High-resolution timer (217 ps) with complex waveform builder and multi-event handler	<ul style="list-style-type: none">Mixed signal management within one chipBOM cost reductionReduced MCU layout footprintCode reliabilityEases digital power conversionControl loop
STM32 compatibility and scalable portfolio <ul style="list-style-type: none">Pin compatibility and same API with STM32F0 peripheralsFrom 16 up to 512 Kbytes of Flash memoryFrom 32 to 144 pins in QFN, LQFP, BGA, and WLCSP packagesAmbient temperature range: -40 °C to 105 °C (125 °C junction)	<ul style="list-style-type: none">Eases platform development strategy from Cortex-M0 (F0 series) up to Cortex-M4 (F3 series) coresIndustrial grade



An upgraded MCU class

The STM32F3 family of mixed-signal MCUs with DSP and FPU instructions

The STM32 F3 series shakes up the digital signal controller world by combining a 32-bit ARM® Cortex®-M4 core (DSP, FPU) running at 72 MHz with a high number of integrated analog and digital peripherals leading to cost reduction at application level and simplifying application design. The STM32F3 Series consists of six lines:

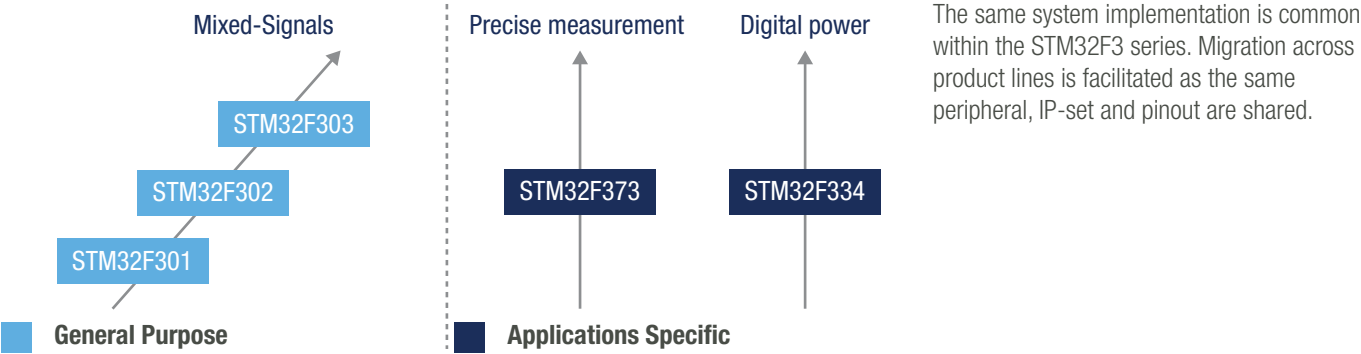
- The **STM32F301, STM32F302, STM32F303 lines** are general-purpose MCUs ranging from a basic, cost-efficient peripheral set to devices with more performance and analog functions.
- The **STM32F334 line** includes a versatile high-resolution timer (217 ps) for digital power conversion applications, such as D-SMPS, lighting, welding, solar and wireless charging.
- The **STM32F373 line** with its 16-bit sigma-delta ADC is designed for high-precision measurements in applications such as biometric sensors or smart metering.
- The **STM32F3x8 line** supporting 1.8 V operations.



STM32F3 PRODUCT LINES

Product line	FLASH (KB)	RAM (KB)	CCM-SRAM	Power supply	ADC		12-bit DAC	Fast and Ultra Fast Comp.	Op-Amp (PGA)	Advanced 16-bit PMW Timer	High-Resolution timer
					12-bit	16-bit					
STM32F301	32 to 64	16		2.0 to 3.6 V	Up to 2		1	3	1	1	
STM32F302	32 to 512	16 to 64		2.0 to 3.6 V	Up to 2		1	Up to 4	Up to 2	1	
STM32F303	32 to 512	16 to 80	•	2.0 to 3.6 V	Up to 4		Up to 3	Up to 7	Up to 4	Up to 3	
STM32F3x4 Digital Power	16 to 64	16	•	2.0 to 3.6 V	2		3	2x Ultra Fast	1	1	• 10ch
STM32F373 Precision measurement	64 to 256	32		2.0 to 3.6 V	1	3	3	2			
STM32F3x8 1.8 V +/- 8%	64 to 512	16 to 80	•	1.8 V +/- 8%	Up to 4		Up to 3	Up to 7	Up to 4	Up to 3	

A flexible interconnect matrix allows autonomous communication between peripherals and saves CPU resources and power consumption.



Note: The same devices are found in the STM32F3x8 line and operate at 1.8 V.

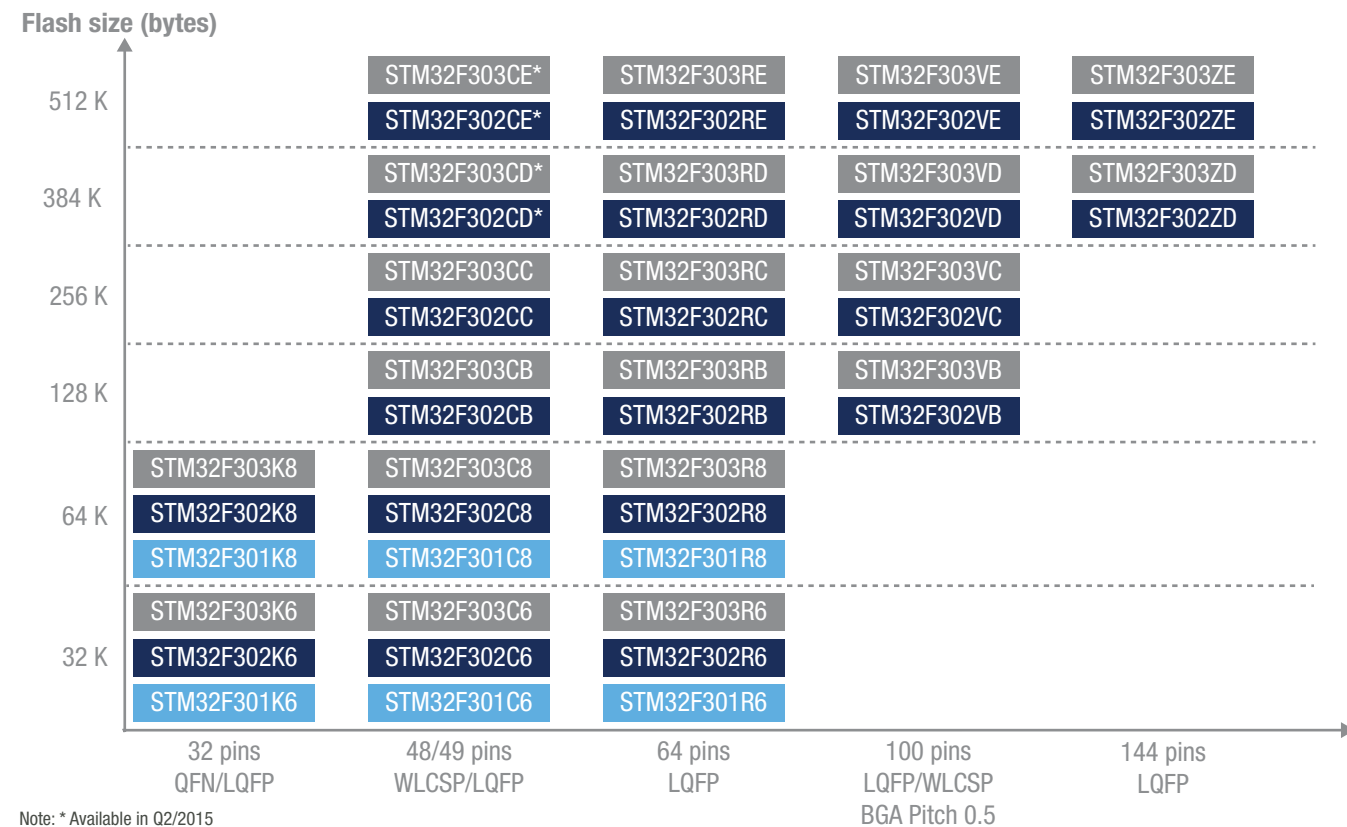


STM32F301 /2/3 line

General-purpose MCUs ranging from basic to increased performance devices

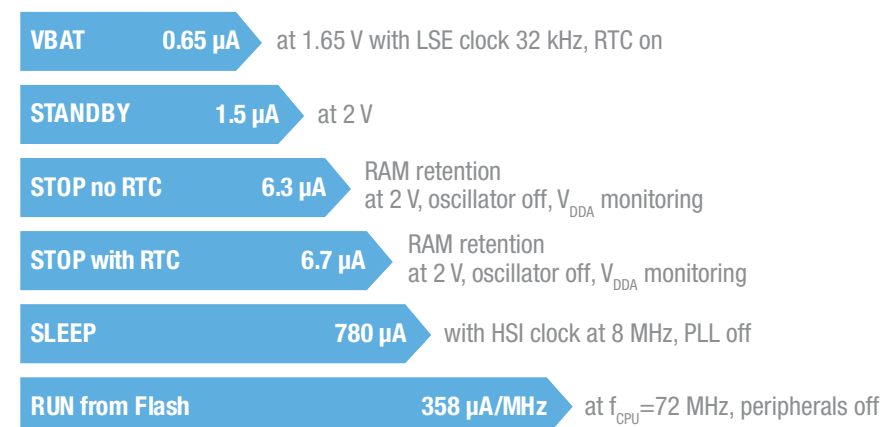
The STM32F301/2/3 mixed-signal MCUs featuring an ARM® Cortex®-M4 core (DSP, FPU) at 72 MHz are tailored to address general-purpose applications in the continuity of the successful STM32F101/102/103 product lines.

STM32F30X PRODUCT LINES PORTFOLIO

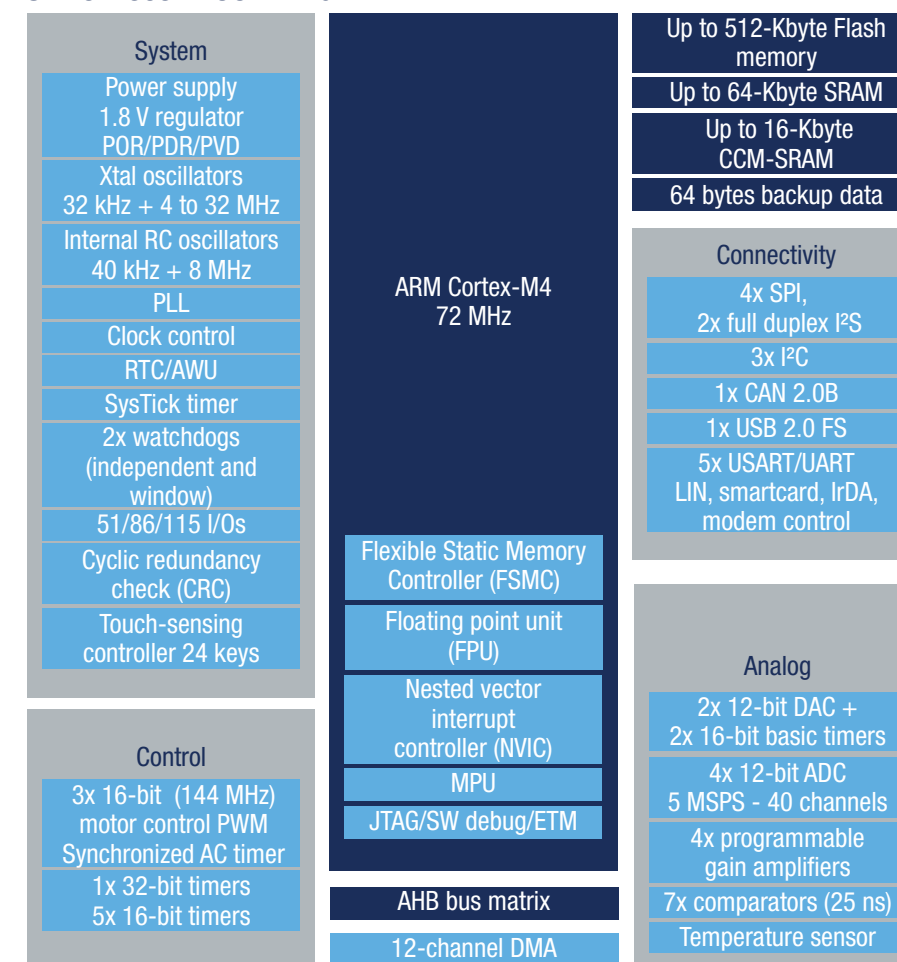


- **STM32F301:** STM32 Cortex-M4 entry level. First sub-dollar Cortex-M4 devices with FPU, the STM32F301 access line has from 32 to 64 Kbytes of on-chip Flash and 16 Kbytes of SRAM, offering an easy way to step into Cortex-M4-core-based MCU development.
- The **STM32F302 and STM32F303** MCUs, compatible with, but more powerful than the STM32F103, operate between 2.0 and 3.6 V. They integrate different levels of analog peripherals. The STM32F303 brings the capability to boost the execution of critical routines with its CCM-SRAM.

TYPICAL CONSUMPTION VALUES ACROSS STM32F3 POWER MODES



STM32F303 BLOCK DIAGRAM



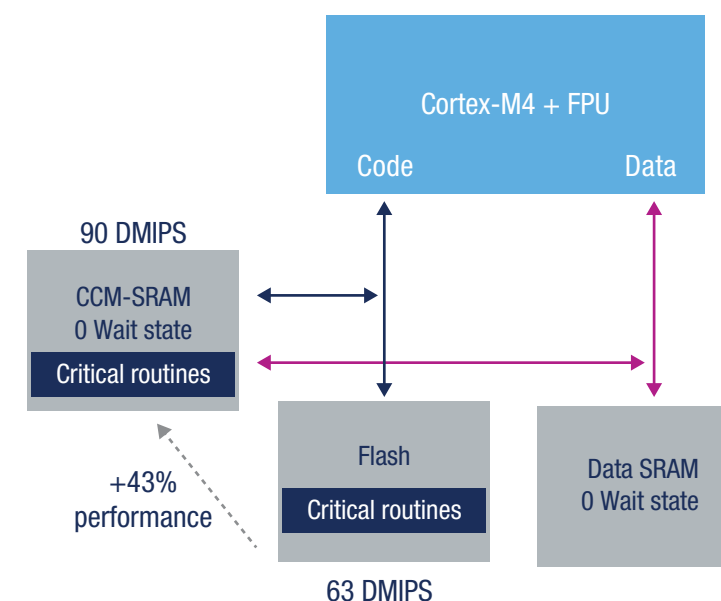
ADVANCED PERIPHERALS

- Up to seven fast and ultra-fast comparators (down to 25 ns)
- Up to four op amps with programmable gain (PGA) at 1% accuracy
- Up to four ultra-fast 12-bit ADCs with 5 Msps and 21 ns sampling time (up to 18 Msps in Interleaved mode)
- Up to three fast 144 MHz motor control timers (resolution < 7 ns)
- SRAM with Parity bit and Polynomial CRC for better reliability
- Fast communication peripherals: 9 Mbits/s USART, 18 Mbits/s SPI, and 1 MHz I²C
- Full duplex I²S for audio applications

The high mathematical computation brought by the Cortex-M4 core, combined with its rich and advanced analog peripherals set, make STM32F30x devices ideal for control loops such as in motor control applications.



CCM-SRAM: THE ROUTINE BOOSTER



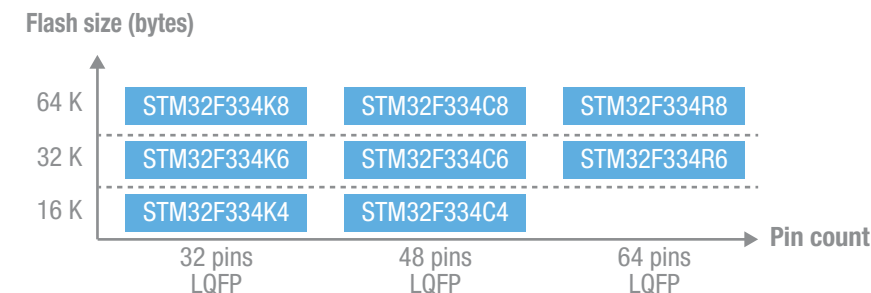
The routine booster (CCM-SRAM) accelerates the execution of critical routines. It consists of an SRAM plugged on both instruction and data buses where code is executed without any wait state, thus providing 43% more performance compared to Flash execution. This 'static cache' offers 90 DMIPS or 245 Core Mark (equivalent to devices with CPU frequency > 100 MHz).

Digital power line

The STM32F334 boosts digital power conversion

The STM32F334 product line specifically addresses digital power conversion applications, such as D-SMPS, lighting, welding, inverters for solar systems and wireless chargers, thanks to its agile high-resolution timer (HRTIM) providing 217 ps resolution on all operating modes with embedded powerful waveform generator and event handler. A complete ecosystem has been designed to ease digital power conversion based on STM32F334 devices.

STM32F334 PORTFOLIO



Developing with the STM32F334 lets you manage complex PWM waveforms and handle numerous external events thanks to:

High-resolution timer with waveform builder and event handler (HRTIM)

- 217 ps high resolution (4.6 GHz equivalent) guaranteed on all channels vs voltage, temperature or manufacturing deviations
 - High resolution on all channels and any timing
 - 10-channel timer made of 6 timings units that can be cross-coupled or work independently
- Advanced PWM waveform generation with minimized software
 - Smart functions, such as a hardware burst mode controller
 - One DMA channel per timer
 - One parameter modification can change multiple events (timer chaining)
- Complex event management
 - 10 external events inputs and 5 fault inputs
- Numerous interconnects

High-speed ADCs for precise and accurate control

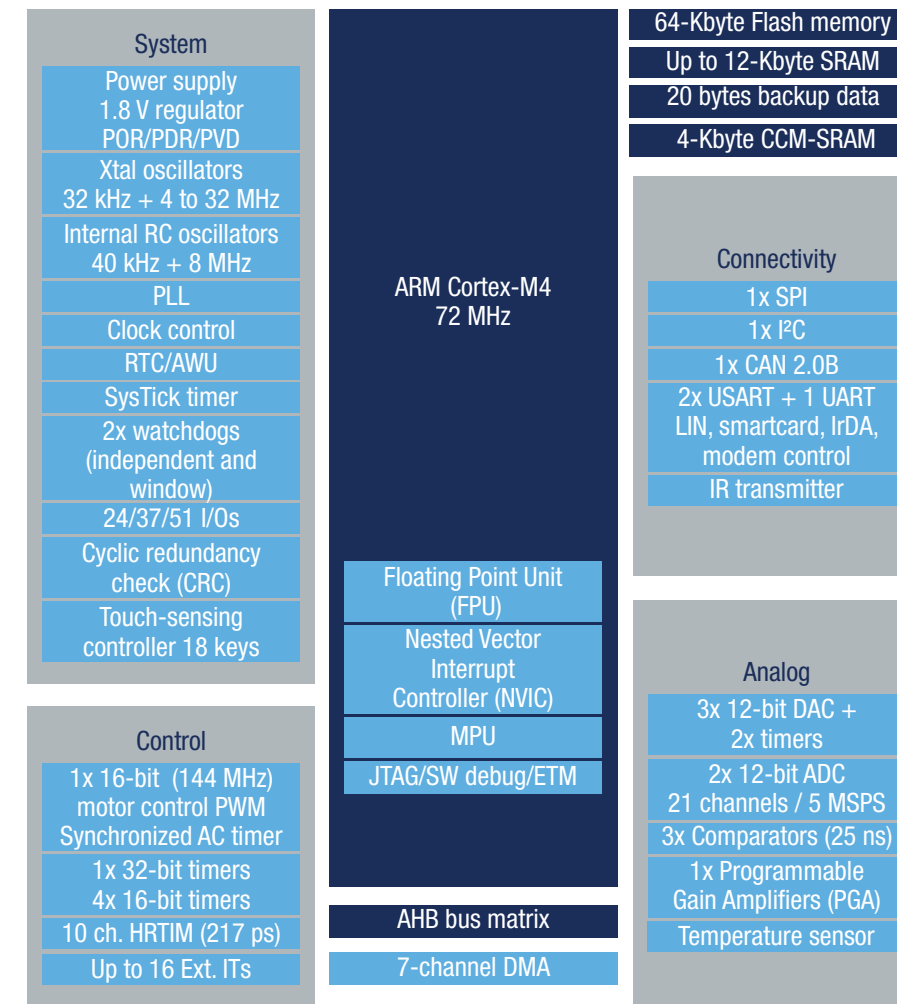
- 12-bit SAR – 5 Msps, single-ended and differential inputs
- Sampling time down to 21 ns
- Multiple triggers for PWM

Built-in analog peripherals for signal conditioning and protection

- Ultra-fast comparators (25 ns)
- 12-bit digital-to-analog converter (DAC)

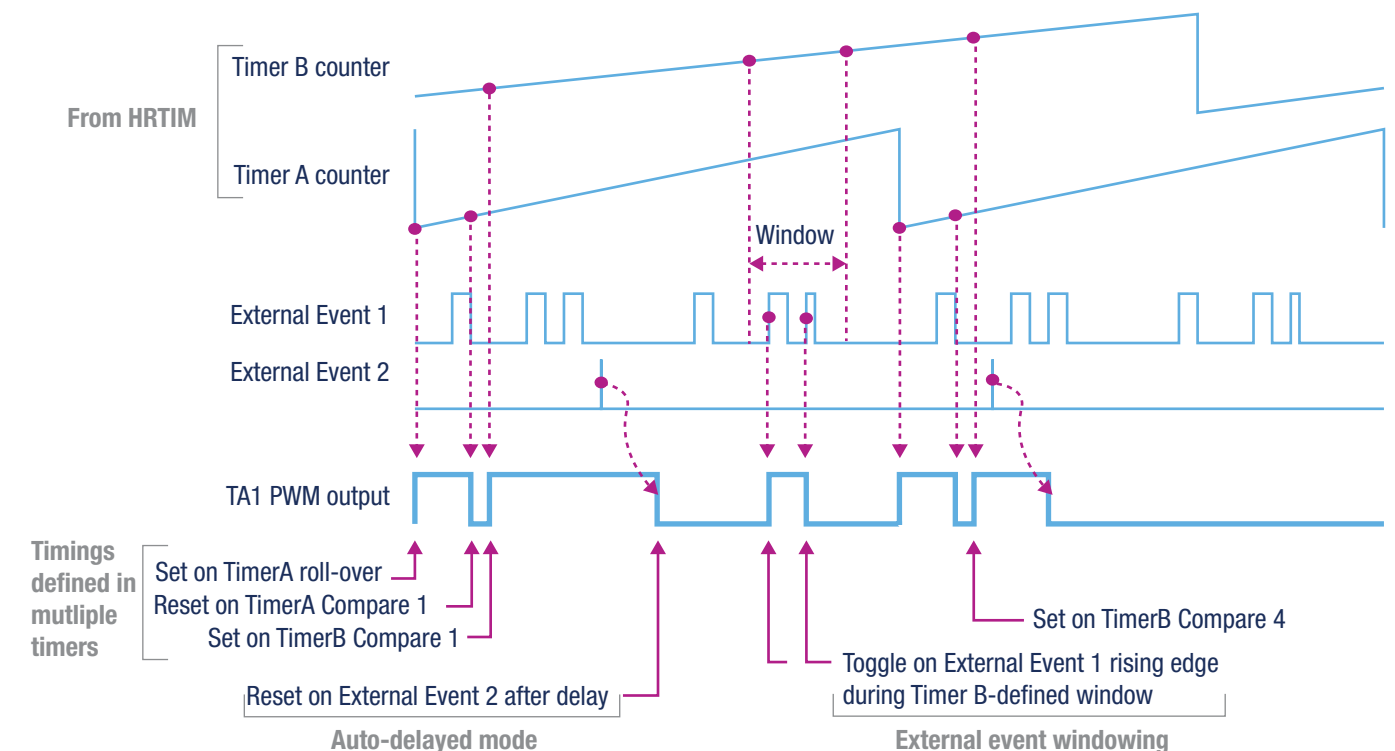
A High-Resolution timer cookbook (AN4539), a dedicated Discovery kit (32F3348DISCOVERY) plus and several dedicated D-SMPS evaluation board (ex: STEVAL-ISA147V2) help accelerate application development

STM32F334 BLOCK DIAGRAM



STM32F334 devices greatly simplify digital control of complex power-supply topologies used in data servers and telecom infrastructure, as well as in wireless charging points, lighting, welding, industrial power supplies and all DSMPS.

COMPLEX WAVEFORM BUILDING AND MULTI-EVENT HANDLER (FROM HRTIM) - EXAMPLE

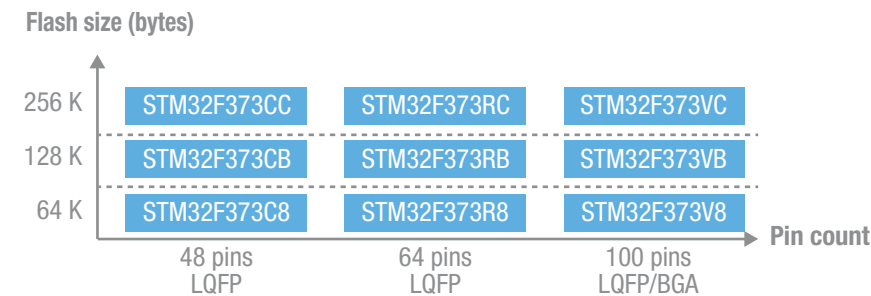


High precision line

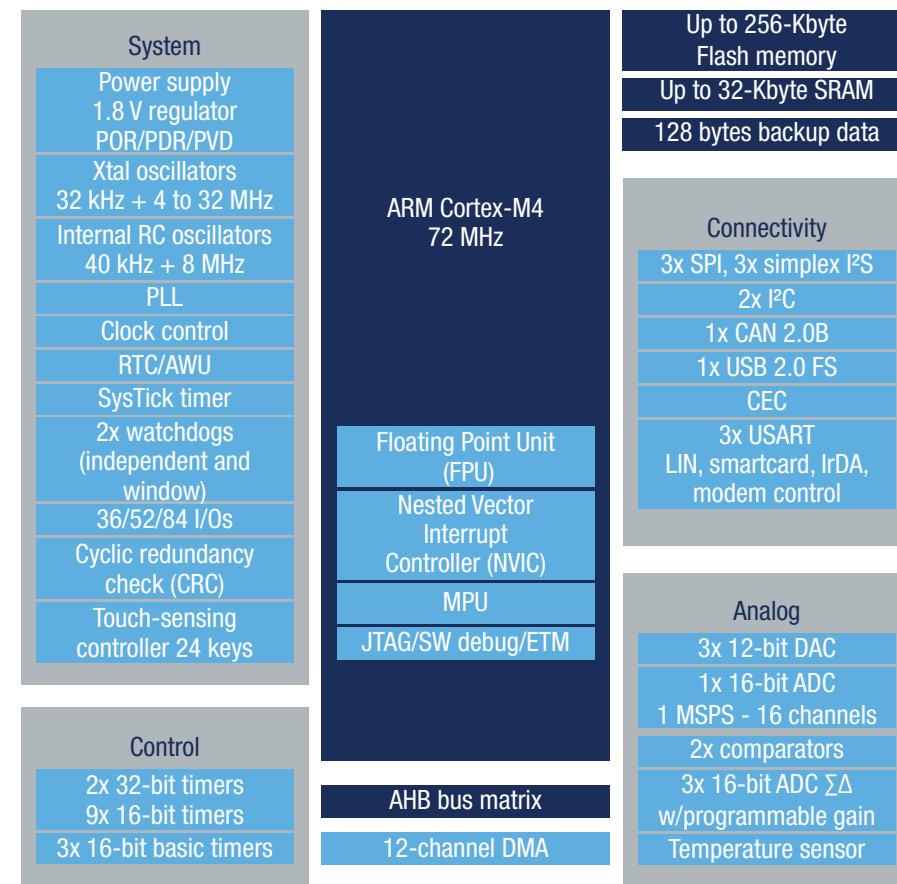
True 16-bit sigma delta ADC integration

The Cortex®-M4 based STM32F373 product line integrates 16-bit sigma-delta ADCs, making the devices a perfect fit for all kinds of sensor applications requiring high-precision measurements together with more demanding signal processing.

STM32F373 PORTFOLIO



STM32F373 BLOCK DIAGRAM



APPLICATION TARGET

- Portable medical equipment
- Entry-level consumer audio equipment
- Sensor hub for biometric sensors
- Portable fitness
- Gaming
- Metering equipment

Each STM32F373 device is equipped with three 16-bit sigma-delta ADCs with the following characteristics:

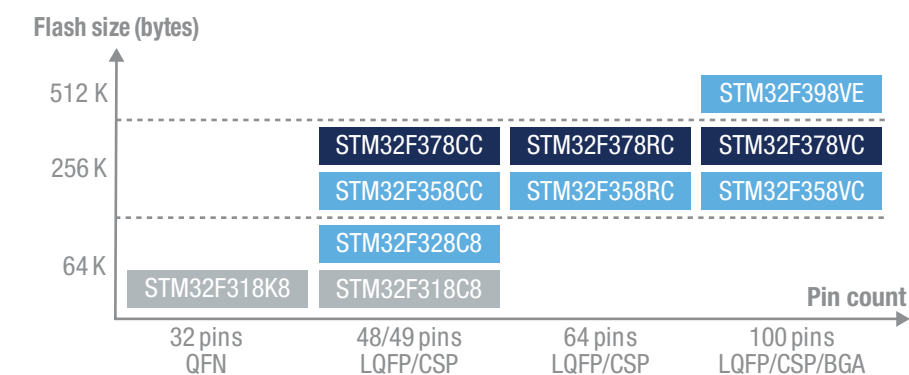
- 16-bit output signed code
- 7 gain levels: ½, 1, 2, 4, 8, 16, and 32
- Differential or single ended mode:
 - Up to 11 differential input pairs or 21 single-ended combinations over three ΣΔ ADCs
 - Free input configuration as single ended or differential
- Up to 50 Ksps in Fast mode on one channel (per ΣΔ ADC)
- Independent power supply and V_{REF}
- Offset error < 1 LSB after calibration
- 3 different low power modes:
 - Slow: 600 μA (max.), Standby: 200 μA, Power down: 10 μA (max.)

Low voltage line

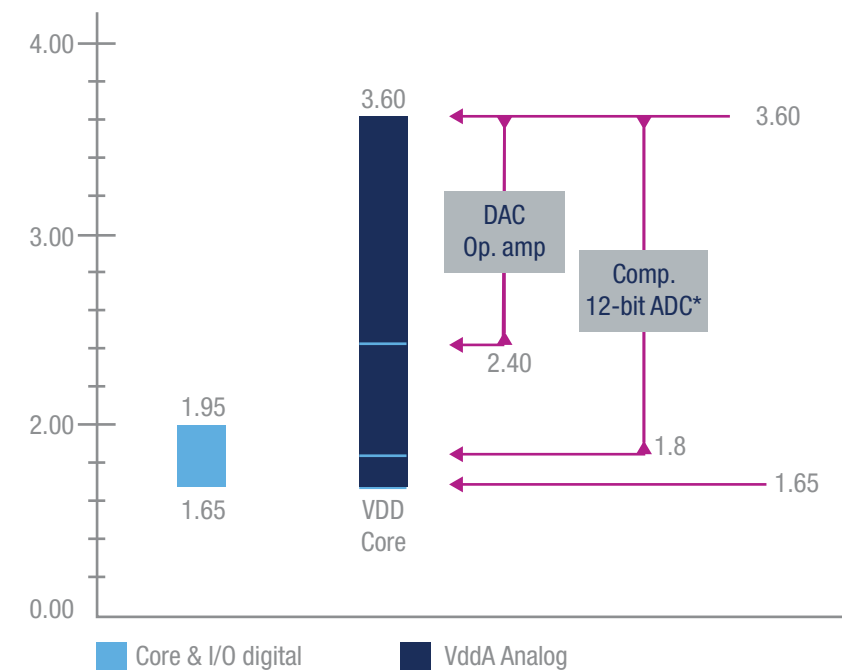
The STM32F3x8 low voltage 1.8 V line

The STM32F3x8 line operates at 1.8 V ± 8%. It is well suited for use in portable consumer applications such as smartphones, accessories and media devices. Designers can take advantage of the same features as the STM32F3 series with no compromise or degradation in processing performance when operating at a lower voltage. The combination of 1.8 V digital supply voltage and an independent analog domain is an advantage in heterogeneous system architectures, leading to simplified system design and connected cost savings. The STM32F3x8 devices are ideal low-voltage companion microcontrollers, allowing to maintain a wide analog dynamic range.

STM32F3x8 PORTFOLIO



VOLTAGE RANGE (CORE, I/Os AND VDDA)



* Except STM32F378

Simple interface with a 1.8 V application processor, ensuring maximum resolution (3.6 V) on ADC, DAC and op amp thanks to dual-voltage domains on the STM32F3.

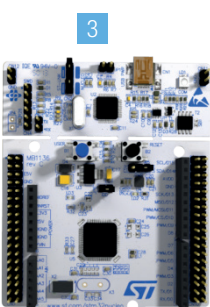
STM32F3 Ecosystem

Hardware tools

Various types of development boards let you get started with STM32F3 products.

The STM32 Nucleo boards provide an affordable and flexible way for users to try out new ideas and build prototypes with a wide choice of specialized expansion boards. The Discovery kits let developers quickly explore key features of STM32F3 products, while the evaluation boards highlight all MCU functions. All these development boards include an integrated debugger/programmer as well as ready-to-use software examples helping developers to promptly get started.

STM32 Nucleo



Discovery kit



Evaluation board



Flexible prototyping

Key feature prototyping

Full feature evaluation

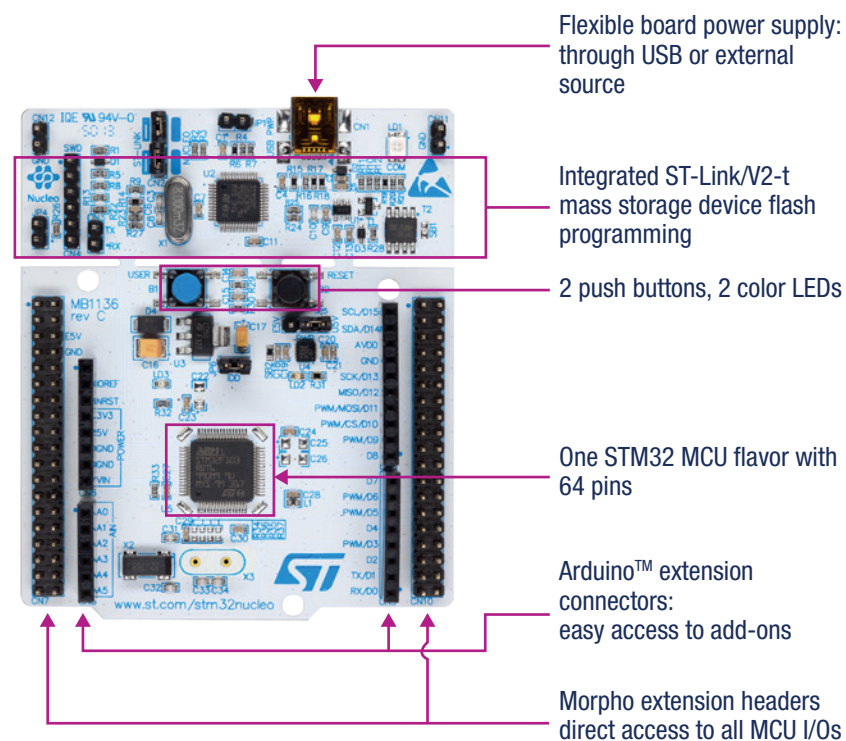
Number of hardware tools

www.st.com/stm32evaltools

STM32 NUCLEO

- Open platform with one STM32 MCU and integrated debugger/programmer.
- At least one board per main series STM32F334 and F303/302.
- 2 types of connectors for unlimited expansion possibilities.
- Support multiple IDEs and mbed online tools.
- \$10.32 recommended resale price.

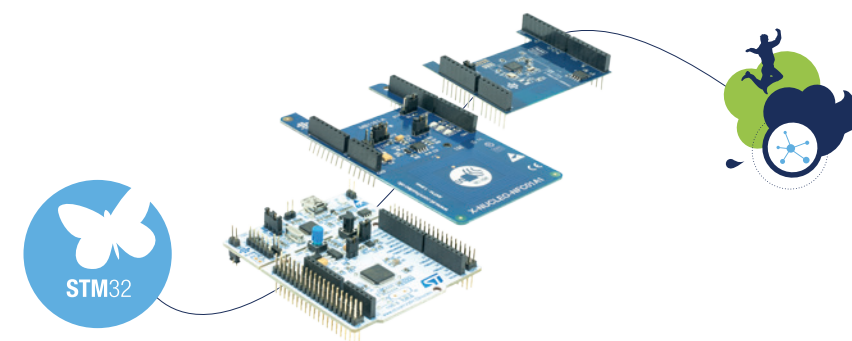
www.st.com/stm32nucleo



STM32 NUCLEO EXPANSION BOARDS

- The expansion boards let you add specialized functions (sense, connectivity...) with companion chips through Arduino™ or Morpho connectors.
- The portability of associated software components enables you to target several STM32 MCUs.

www.st.com/x-nucleo

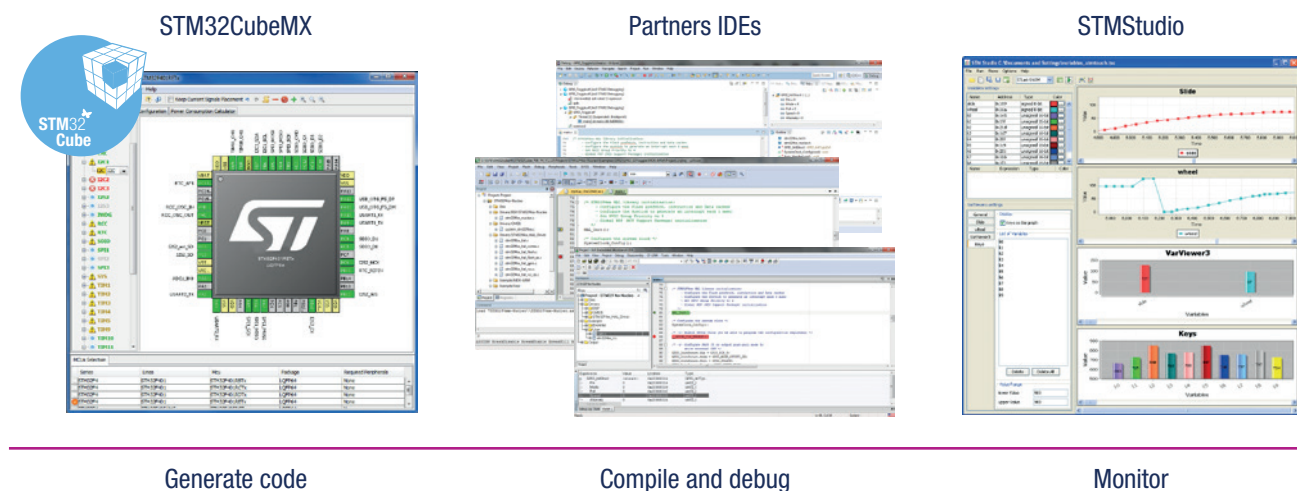


Software development tools offer

ST suggests a 3-step approach for standard development in C: configuration and generation, compile and debug, and then monitoring.

- Configure the microcontroller using the STM32CubeMX tool.
 - Optionally generate code depending on user choices, if STM32CubeF3 embedded software is used.
- Develop the application, compile and debug using integrated development environments (IDEs) from our partners: IAR, ARM/Keil, **Ac6***, Atollic, Altium, **CooCox***, Emprog, Hitex, iSystem, Keolabs/Raisonance, Rowley or Segger.
- Monitor the application while it is running without affecting application behavior (non-intrusive) with STMStudio www.st.com/stmstudio

* Free IDE

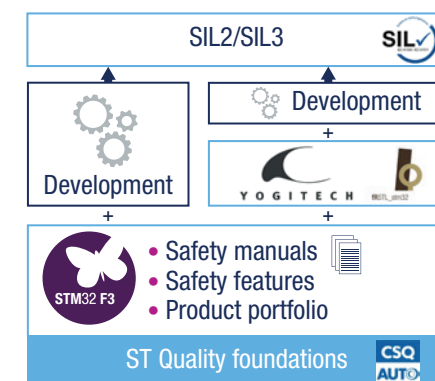


ACHIEVING SIL2/3 WITH STM32F3

Quickly achieve IEC 61508 Safety Integrity Level (SIL) certification with STM32F3 Functional Safety Package developed in partnership with Yogitech:

- STM32F3 Safety Manual: a user guide including detailed list of safety requirements and examples
- fRSTL_stm32f3 library: a set of ready to use, verified and application independent Software Test Libraries

www.st.com/stm32safety



STM32 MOTOR CONTROL ECOSYSTEM

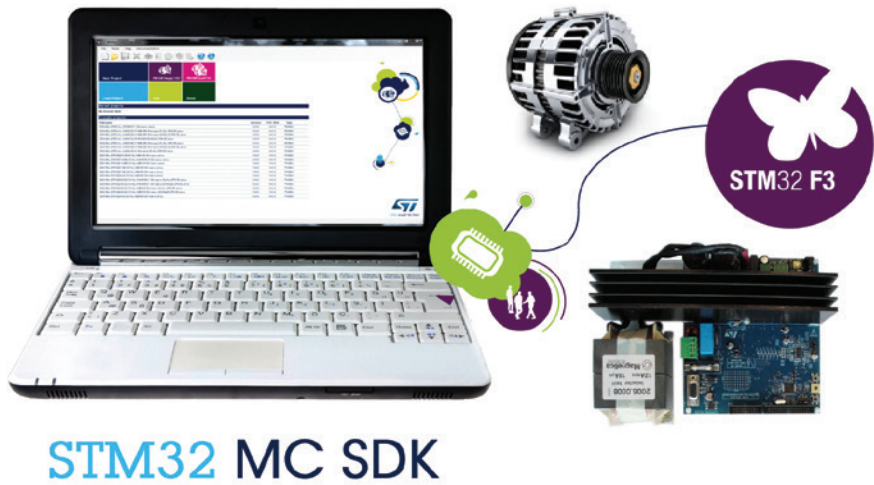
ST's STM32 MCU family offers the performance of the industry-standard Cortex®-M core with the service of vector control or field-oriented control (FOC) algorithms, widely used in high-performance drives. The STM32 PMSM FOC software development kit (SDK), (STSW-STM32100), which includes the permanent-magnet synchronous motor (PMSM) FOC firmware library and ST MC Workbench (graphical user interface to set the MC library parameters), lets users evaluate STM32 performance and develop a complete application for single or multi 3 phase permanent-magnet motor drive systems (sensored or sensorless).

The STM32 PMSM FOC SDK is part of ST's motor control ecosystem which offers a wide range of hardware and software solutions for various motor control applications, like toys, home appliances, factory automation.... From release 4.0, the STM32 PMSM FOC SDK includes the following features (among others):

- Sensorless motor control algorithm for STM32F3 and STM32F4 (ST patent pending) based on the High Frequency Injection (HFI) method
 - The HFI algorithm allows precise rotor angle detection in field-oriented control (FOC). It enables very low or zero speed operation for compressor applications (Air Con, Fridge) with reliable and efficient motor start-up, and for washing machines where it increases the efficiency of each washing cycle (low speed, full torque)
- "Maximum torque per ampere" (MTPA) that optimizes the motor torque for each load and increases efficiency
- "Feed-forward" that improves current control at high speeds
- And new additional features as easy motor start-up and one touch tuning (Plug'n Spin) will be available in Q2/2015

ST MC Workbench is PC software which reduces design effort and time when configuring the STM32 PMSM FOC firmware library. Using its GUI, the user generates all the parameter header files needed to configure the library according to application needs and can in real-time monitor and change certain variables of the algorithm.

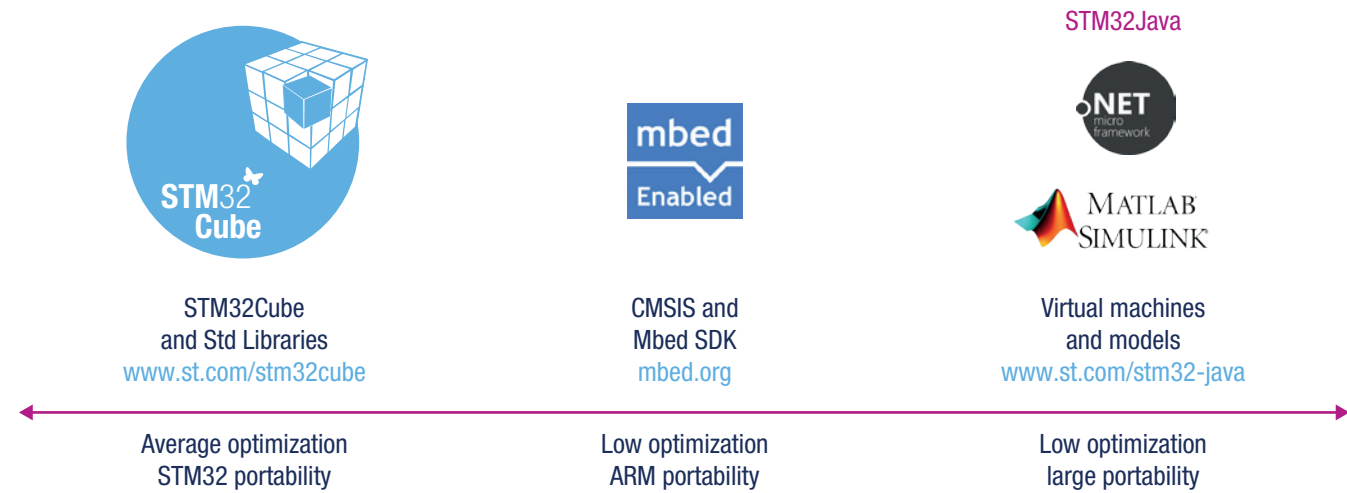
A wide range of HW boards, from standalone control boards and power boards up to a complete Motor Control Kit, are available to evaluate ST solutions for Motor Control applications.



Recommendations for choosing embedded software

When choosing between a strategy for code optimization or portability, here are some recommendations:

- Standard Peripheral Library offers a good tradeoff for users willing to remain within the STM32 F0 series in the future, with a portability level ensured among all STM32 F3 MCUs
- STM32CubeF3 embedded software is the correct choice for users who may want to easily port their application to another STM32 MCUs. In addition, this option also benefits from the full features of the STM32CubeMX tool on the PC, enabling access to code generation based on the user configuration and STM32CubeF3 embedded software



Focus on model development with MATLAB/Simulink

MATLAB and Simulink users can also benefit from the STM32 F3 series with their favorite environment.

A simple 3-step approach is possible using MATLAB and Simulink:

1. Create an algorithm model and simulate it on the host.
2. Generate Processor-in-the-Loop (PIL) code and verify it. This step uses MATLAB/Simulink to generate code optimized for the Cortex®-M4 devices using the DSP instruction set.
3. And finally, let everything run on the STM32F3 MCU using the peripheral blockset provided by ST, enabling the use of real STM32F3 peripherals such as the ADC, DAC, Timers, etc.

(More at www.st.com/stm32matlab)

Model development helps reduce development time and specification errors usually found with other methods.



Collaterals

www.st.com is a valuable source of information and support with a documentation repository, forums, video and social media that help provide solutions for any issues or challenges that you may encounter. The existing community around ARM Cortex cores is already big enough that developers will likely find existing solutions or examples ready to be imported.

Please download our mobile version of the ST MCU Finder which makes MCU selection easy. It is available for Apple, Windows and Android mobile platforms.

st.com



ST MCU finder
www.st.com/stmcfinder



Various social media



ST forums on microcontrollers



facebook.com/stm32



twitter.com/@ST_World



youtube.com/STonlineMedia

ARM [Mbed.org](https://mbed.org)

Information

MCU selection

Communities and social media

STM32F3 shortcuts

www.st.com/stm32f3

www.st.com/stm32evaltools

www.st.com/stm32discovery

www.st.com/stm32nucleo

www.st.com/stm32embeddedsoftware

www.st.com/stm32cube

mbed.org

www.st.com/stm32matlab

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