# **Debug Test Report UL, Transaction Security**

Debug Testing of

Product: MAX32550-SCR-EVKIT

Reference Standard: EMVCo Type Approval, Terminal Level 1 Test cases,

Version 2.1, January 2009

Evaluation Report Serial No: 1MAX-RP1100581686JD01A-UTS

This Test Report is Issued Under The Authority Of Scott D'Adamo, Operations Manager:	
Checked by: Tim Chapman  Tupu  Tested by: Nsima Udoh	Issued to: Maxim Integrated Products Inc. Company ZI Athelia IV, Le Forum BatQuartier Roumagoua, La Ciotat, Cedex, 13600, France
Issue date: 17 September 2014	Report Version: 1.0
Test start date: 12 September 2014	Test end date: 12 September 2014

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The results in this report apply only to the sample(s) tested and do not constitute an endorsement of the whole manufacturer's production.

UL is accredited by EMVCo to perform EMV Terminal Type Approval testing.



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# (II)

#### **Version history**

Version	Date	Status	Author
1.0	17/09/2014	Finalised	NU

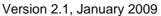
#### **Change history**

Version	Date	Changes
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# 1 Details of Test

# 1.1 Device Manufacturer

Manufacturer Address	Maxim Integrated Products Inc. Company
	ZI Athelia IV,
	Le Forum BatQuartier Roumagoua,
	La Ciotat,
	Cedex,
	13600,
	France
Contact name	Yann Loisel
Contact tel.	N/A
Contact fax	N/A
Contact e-mail	yann.loisel@maximintegrated.com

# 1.2 Location of Test

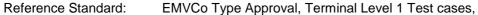
Laboratory Address	UL, Transaction Security Pavilion A, Ashwood Park, Basingstoke, Hampshire,
0,0	RG23 8BG
Contact name	United Kingdom Timothy Chapman
Contact Harne	тіпошу спартіап
Contact tel.	+44 (0)1256 31 2117 (Direct Dial)
Contact fax	+44 (0)1256 31 2001
Contact e-mail	timothy.chapman@ul.com

# 1.3 Test Environment

Testing start date:	12 September 2014	
Testing end date:	12 September 2014	
Environmental data	Temperature (°C)	Humidity (%)
Environmental data	20 ± 3	20-80
Nominal supply voltage (Vac)	240	

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# 2 Details of equipment under test

# 2.1 Final Device Build Status

The following is the build status for which compliance has been demonstrated by test.

Vendor name	Maxim Integrated Products Inc. Company
Type name or number	MAX32550-SCR-EVKIT
Product type	EMV 2000 level 1

# 2.2 Identification of Samples Tested

The following summary may be used to identify the samples referenced in the test summary.

## 2.2.1 IFM Sample #1

Brand name	MAX32550-SCR-EVKIT
Type name or number	MAX32550-SCR-EVKIT
Product type	EMV 2000 level 1
Test sample Serial No:	Max32550 (003)
IFM Unique Identifier:	N/A
IFM Unique Hardware Identifier:	N/A
IFM Unique Software Identifier:	N/A
Modifications from standard	None
Date of receipt of test samples	17 September 2014

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# 2.3 Description of Product

The Product is an IFM produced to conform to the EMV 2000 Level 1 Specification.

## 2.4 Generation of Conformance Test Plan

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In order to conform to the essential requirements of EMV 2000, Integrated Circuit Card Specification for Payment Systems Book 1, Version 4.2, June 2008. The following test plan has been carried out to meet the requirements of "EMVCo Integrated Circuit Card, MasterCard Implementation, Test Procedures - level 1, Terminal Type Approval EMV 4.0, Description of Tests, Version 4.0, Rev 3, April 2009"

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# 3 REFERENCE DOCUMENTS

Testing was performed according to the following reference documents and standards:

Title	Version and issue date
EMV 2000 Integrated Circuit Card Specification for Payment Systems, Book 1	Version 4.3, November 2011
EMV 2000 Integrated Circuit Card Specification for Payment Systems, Book 2	Version 4.3, November 2011
EMV 2000 Integrated Circuit Card Specification for Payment Systems, Book 3	Version 4.3, November 2011
EMV 2000 Integrated Circuit Card Specification for Payment Systems, Book 4	Version 4.3, November 2011
EMVCo Type Approval, Terminal Level 1, Test cases	Version 2.1, January 2009
EMVCo Type Approval, Terminal Level 1, Bulletin 93	2009
ISO/IEC 7811-1:1992 Identification cards - Recording technique - Part 1: Embossing	1992
ISO 7816-1: Identification cards - Integrated circuit(s) cards with contacts - Part 1: Physical characteristics	1987
ISO 7816-2: Identification cards - Integrated circuit(s) cards with contacts - Part 2: Dimensions and location of contacts	1988
ISO/IEC 7816-3: Identification cards - Integrated circuit(s) cards with contacts - Part 3: Electronic signals and transmission protocols	1989
ISO/IEC 7816-3: Identification cards - Integrated circuit(s) cards with contacts - Part 3, Amendment 1: Protocol type T=1, asynchronous half duplex block transmission protocol	1992
ISO/IEC 7816-3: Identification cards - Integrated circuit(s) cards with contacts - Part 3, Amendment 2: Protocol type selection (Draft International Standard	1994
ISO/IEC 7816-4: Identification cards - Integrated circuit(s) cards with contacts - Part 4, Inter-industry commands for interchange	1995
ISO/IEC 10373: Identification cards - Test methods	1993

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# 4 TEST EQUIPMENT

# 4.1 Test Platforms

Conformance testing was performed using test equipment calibrated in accordance with BABTS accreditation requirements. Calibration records are available for inspection at UL, Transaction Security if required:

#### 4.1.1 Test Platform (Contact Force)

Instrument	Model and/or Asset
Contact Force Test Tool	LC Indstrie Perrin Control system for contact effort on smartcard connectors N99111 (M1411)

# 4.1.2 Test Platform (Electrical)

Instrument	Model and/or Asset
Electrical Measurement Tool	Comprion IT <sup>3</sup> Test Platform EMV R 2.0 (M1618) APR v2.0 (M1619)
Test Script Versions	EMVCo Level 1 Electrical Tests v2.1 Test Platform EMV v2.0 EMV Simulator v2.0 Analogue Scope v2.5

# 4.1.3 Test Platform (Protocol)

Instrument	Model and/or Asset		
Protocol Measurements Tool	Intergri INQ v1.51.8 (	(PCN46NT)	
Micropross Card Simulator	Micropross Star 3150 (N1291) Micropross Star Universal Probe (A1662)		
Firmware (+ DLL) Information	Boot Syst Config Emulog L1 Player Version	1.03 1.13 70 Emulog_34_3.the L1Player_24_3.the	
Test Scripts Identification	EMV2000 L1 (Mastercard v3.0) Terminal Test Library v3.1.2		

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# 5 TEST RESULTS

# 5.1 Result Summary

The following table summarises the test results obtained. A definition of the result categories may be found at the end of the result tables.

Total test cases performed	782	
Pass	688	
Fail	0	2,7
N/A	25	
N/R	66	, (10
Inconclusive	N/A	\ \ \

# 5.2 Test Performed

The following tables reflect the requirements of the "EMVCo MasterCard Implementation". References to dates upon which the verdicts were obtained, as well as the verdicts of the applicable test cases, can be seen in the test logs available at UL, Transaction Security.

### 5.2.1 Test Case Descriptions

This reference is used to explain the notations used for tests and result files.

A terminal test case is described as follows: 1Cp.vwx.yz

Where the following letters mean:

1C = Test Case Level 1 Reference

Where p means one of the following:

A = Mechanical

B = Electrical

C= Card Session Tests

E = ATR

F = Protocol Tests

vwx = Three digits for Test Cases numbering (000 to 999)

yz = Two digits for Sub-test Cases numbering (00 to 99)

#### 5.2.2 Test Case Parameters Column

NT, HT & LT denotes tests performed at Nominal, High and Low Temperature respectively.

NV, HV & LV denotes tests performed at Nominal, High and Low Voltage respectively.

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#### 5.2.3 Level 1 Test Results

1CA.001.0x   Mandatory   Mandatory   Mandatory   PASS   CB.002.00   Mandatory   PASS   CB.001.0x   Mandatory   PASS   CB.002.00   EMV V Item 2   PASS   CB.003.0x   EMV V Item 2   PASS   CB.003.0x   EMV V Item 2   N/A   CB is electrically isolated from the IFM   IFM   IFM   CB.004.0x   NT   PASS   CB.004.0x   NT   PASS   CB.004.0x   LT   N/T   Not Tested Debug only   CB.004.0x   LT   N/T   Not Tested Debug only   CB.005.0x   NT   PASS   CB.005.0x   HT   N/T   Not Tested Debug only   CB.005.0x   LT   N/T   Not Tested Debug only   CB.006.0x   LT   N/T   Not Tested Debug only   CB.007.0x   LT   N/T   Not Tested Debug only   CB.007.0x   LT   N/T   Not Tested Debug only   CB.007.0x   LT   N/T   Not Tested Debug only   CB.008.0x   LT   N/T   Not Tested Debug only   CB.008.0x   LT   N/T   Not Tested Debug only   CB.008.0x   LT   N/T   Not Tested Debug only   CB.009.0x   LT   N/T   Not Tested Debug only   CB.001.0x   NT   PASS   CB.001.0x   NT   PASS   CB.001.0x   LT   N/T   Not Tested Debug only   CB.00	Test Case	Parameters	Result	Comment
TCB.001.0x	1CA.001.0x	Mandatory	PASS	
TCB.002.00	1CA.002.00	Mandatory	PASS	
TCB.003.0x	1CB.001.0x	Mandatory	PASS	
IFM	1CB.002.00	EMV V Item 2	PASS	70)
TCB.004.0x	1CB.003.0x	EMV V Item 2	N/A	
TCB.004.0x	1CB.004.0x	NT	PASS	
1	1CB.004.0x	HT	N/T	Not Tested - Debug only
1	1CB.004.0x	LT	N/T	Not Tested - Debug only
1	1CB.005.0x	NT	PASS	
1	1CB.005.0x	HT	N/T	Not Tested - Debug only
1CB.006.0x         HT         N/T         Not Tested - Debug only           1CB.006.0x         LT         N/T         Not Tested - Debug only           1CB.007.0x         NT         PASS           1CB.007.0x         HT         N/T         Not Tested - Debug only           1CB.008.0x         LT         N/T         Not Tested - Debug only           1CB.008.0x         HT         N/T         Not Tested - Debug only           1CB.008.0x         LT         N/T         Not Tested - Debug only           1CB.009.0x         HT         N/T         Not Tested - Debug only           1CB.009.0x         HT         N/T         Not Tested - Debug only           1CB.010.0x         NT         PASS           1CB.010.0x         NT         PASS           1CB.010.0x         HT         N/T         Not Tested - Debug only           1CB.010.0x         HT         N/T         Not Tested - Debug only           1CB.011.0x         NT         PASS           1CB.011.0x         HT         N/T         Not Tested - Debug only           1CB.012.0x         HT         N/T         Not Tested - Debug only           1CB.013.0x         HT         N/T         Not Tested - Debug only	1CB.005.0x	LT	N/T	Not Tested - Debug only
1CB.006.0x         LT         N/T         Not Tested - Debug only           1CB.007.0x         NT         PASS           1CB.007.0x         HT         N/T         Not Tested - Debug only           1CB.008.0x         LT         N/T         Not Tested - Debug only           1CB.008.0x         HT         N/T         Not Tested - Debug only           1CB.008.0x         LT         N/T         Not Tested - Debug only           1CB.009.0x         LT         N/T         Not Tested - Debug only           1CB.009.0x         HT         N/T         Not Tested - Debug only           1CB.009.0x         LT         N/T         Not Tested - Debug only           1CB.010.0x         NT         PASS           1CB.010.0x         NT         PASS           1CB.010.0x         HT         N/T         Not Tested - Debug only           1CB.011.0x         NT         PASS           1CB.011.0x         HT         N/T         Not Tested - Debug only           1CB.012.0x         NT         PASS           1CB.013.0x         NT         PASS           1CB.013.0x         NT         NT         Not Tested - Debug only           1CB.013.0x         HT         N/T         No	1CB.006.0x	NT	PASS	
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1CB.007.0x         HT         N/T         Not Tested - Debug only           1CB.007.0x         LT         N/T         Not Tested - Debug only           1CB.008.0x         NT         PASS           1CB.008.0x         LT         N/T         Not Tested - Debug only           1CB.009.0x         LT         N/T         Not Tested - Debug only           1CB.009.0x         HT         N/T         Not Tested - Debug only           1CB.009.0x         LT         N/T         Not Tested - Debug only           1CB.010.0x         NT         PASS           1CB.010.0x         NT         PASS           1CB.010.0x         HT         N/T         Not Tested - Debug only           1CB.011.0x         NT         PASS           1CB.011.0x         HT         N/T         Not Tested - Debug only           1CB.011.0x         HT         N/T         Not Tested - Debug only           1CB.012.0x         NT         PASS         Not Tested - Debug only           1CB.013.0x         HT         N/T         Not Tested - Debug only           1CB.013.0x         HT         N/T         Not Tested - Debug only           1CB.013.0x         HT         N/T         Not Tested - Debug only	1CB.006.0x	LT	N/T	Not Tested - Debug only
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1CB.008.0x         NT         PASS           1CB.008.0x         HT         N/T         Not Tested - Debug only           1CB.009.0x         NT         PASS           1CB.009.0x         HT         N/T         Not Tested - Debug only           1CB.009.0x         LT         N/T         Not Tested - Debug only           1CB.010.0x         NT         PASS           1CB.010.0x         HT         N/T         Not Tested - Debug only           1CB.010.0x         LT         N/T         Not Tested - Debug only           1CB.011.0x         NT         PASS           1CB.011.0x         NT         PASS           1CB.011.0x         HT         N/T         Not Tested - Debug only           1CB.012.0x         NT         PASS           1CB.012.0x         NT         PASS           1CB.012.0x         HT         N/T         Not Tested - Debug only           1CB.013.0x         NT         PASS           1CB.013.0x         NT         PASS           1CB.014.0x         NT         N/T         Not Tested - Debug only           1CB.014.0x         NT         NT         Not Tested - Debug only           1CB.014.0x         NT         NT	1CB.007.0x	HT	N/T	Not Tested - Debug only
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1CB.009.0x         LT         N/T         Not Tested - Debug only           1CB.010.0x         NT         PASS           1CB.010.0x         HT         N/T         Not Tested - Debug only           1CB.011.0x         LT         N/T         Not Tested - Debug only           1CB.011.0x         NT         PASS         Not Tested - Debug only           1CB.011.0x         LT         N/T         Not Tested - Debug only           1CB.012.0x         NT         PASS           1CB.012.0x         HT         N/T         Not Tested - Debug only           1CB.013.0x         NT         PASS           1CB.013.0x         NT         PASS           1CB.013.0x         HT         N/T         Not Tested - Debug only           1CB.014.0x         NT         PASS           1CB.014.0x         HT         N/T         Not Tested - Debug only           1CB.014.0x         LT         N/T         Not Tested - Debug only           1CB.015.0x         NT         PASS           1CB.015.0x         HT         N/T         Not Tested - Debug only	1CB.009.0x	NT	PASS	
1CB.010.0x         NT         PASS           1CB.010.0x         HT         N/T         Not Tested - Debug only           1CB.010.0x         LT         N/T         Not Tested - Debug only           1CB.011.0x         NT         PASS           1CB.011.0x         LT         N/T         Not Tested - Debug only           1CB.012.0x         NT         PASS           1CB.012.0x         HT         N/T         Not Tested - Debug only           1CB.013.0x         NT         PASS           1CB.013.0x         HT         N/T         Not Tested - Debug only           1CB.013.0x         HT         N/T         Not Tested - Debug only           1CB.014.0x         NT         PASS           1CB.014.0x         HT         N/T         Not Tested - Debug only           1CB.014.0x         LT         N/T         Not Tested - Debug only           1CB.015.0x         NT         PASS           1CB.015.0x         HT         N/T         Not Tested - Debug only	1CB.009.0x	HT	N/T	Not Tested - Debug only
1CB.010.0x         HT         N/T         Not Tested - Debug only           1CB.010.0x         LT         N/T         Not Tested - Debug only           1CB.011.0x         NT         PASS           1CB.011.0x         LT         N/T         Not Tested - Debug only           1CB.012.0x         NT         PASS           1CB.012.0x         HT         N/T         Not Tested - Debug only           1CB.012.0x         LT         N/T         Not Tested - Debug only           1CB.013.0x         NT         PASS           1CB.013.0x         HT         N/T         Not Tested - Debug only           1CB.014.0x         NT         PASS           1CB.014.0x         HT         N/T         Not Tested - Debug only           1CB.014.0x         LT         N/T         Not Tested - Debug only           1CB.015.0x         NT         PASS           1CB.015.0x         HT         N/T         Not Tested - Debug only	1CB.009.0x	ĹT	N/T	Not Tested - Debug only
1CB.010.0x         LT         N/T         Not Tested - Debug only           1CB.011.0x         NT         PASS           1CB.011.0x         HT         N/T         Not Tested - Debug only           1CB.012.0x         LT         N/T         Not Tested - Debug only           1CB.012.0x         HT         N/T         Not Tested - Debug only           1CB.012.0x         LT         N/T         Not Tested - Debug only           1CB.013.0x         NT         PASS           1CB.013.0x         HT         N/T         Not Tested - Debug only           1CB.013.0x         LT         N/T         Not Tested - Debug only           1CB.014.0x         NT         PASS           1CB.014.0x         HT         N/T         Not Tested - Debug only           1CB.014.0x         LT         N/T         Not Tested - Debug only           1CB.015.0x         NT         PASS           1CB.015.0x         HT         N/T         Not Tested - Debug only	1CB.010.0x	NT	PASS	
1CB.011.0x         NT         PASS           1CB.011.0x         HT         N/T         Not Tested - Debug only           1CB.011.0x         LT         N/T         Not Tested - Debug only           1CB.012.0x         NT         PASS           1CB.012.0x         LT         N/T         Not Tested - Debug only           1CB.013.0x         NT         PASS           1CB.013.0x         HT         N/T         Not Tested - Debug only           1CB.013.0x         LT         N/T         Not Tested - Debug only           1CB.014.0x         NT         PASS           1CB.014.0x         HT         N/T         Not Tested - Debug only           1CB.014.0x         LT         N/T         Not Tested - Debug only           1CB.015.0x         NT         PASS           1CB.015.0x         HT         N/T         Not Tested - Debug only	1CB.010.0x	HT	N/T	Not Tested - Debug only
1CB.011.0x         HT         N/T         Not Tested - Debug only           1CB.011.0x         LT         N/T         Not Tested - Debug only           1CB.012.0x         NT         PASS           1CB.012.0x         HT         N/T         Not Tested - Debug only           1CB.013.0x         LT         N/T         Not Tested - Debug only           1CB.013.0x         HT         N/T         Not Tested - Debug only           1CB.013.0x         LT         N/T         Not Tested - Debug only           1CB.014.0x         NT         PASS           1CB.014.0x         HT         N/T         Not Tested - Debug only           1CB.014.0x         LT         N/T         Not Tested - Debug only           1CB.015.0x         NT         PASS           1CB.015.0x         HT         N/T         Not Tested - Debug only	1CB.010.0x	LT	N/T	Not Tested - Debug only
1CB.011.0x         LT         N/T         Not Tested - Debug only           1CB.012.0x         NT         PASS           1CB.012.0x         HT         N/T         Not Tested - Debug only           1CB.012.0x         LT         N/T         Not Tested - Debug only           1CB.013.0x         NT         PASS           1CB.013.0x         LT         N/T         Not Tested - Debug only           1CB.013.0x         LT         N/T         Not Tested - Debug only           1CB.014.0x         NT         PASS           1CB.014.0x         LT         N/T         Not Tested - Debug only           1CB.015.0x         NT         PASS           1CB.015.0x         HT         N/T         Not Tested - Debug only	1CB.011.0x	NT	PASS	
1CB.012.0x         NT         PASS           1CB.012.0x         HT         N/T         Not Tested - Debug only           1CB.012.0x         LT         N/T         Not Tested - Debug only           1CB.013.0x         NT         PASS           1CB.013.0x         HT         N/T         Not Tested - Debug only           1CB.013.0x         LT         N/T         Not Tested - Debug only           1CB.014.0x         NT         PASS           1CB.014.0x         HT         N/T         Not Tested - Debug only           1CB.015.0x         NT         PASS           1CB.015.0x         HT         N/T         Not Tested - Debug only	1CB.011.0x	HT	N/T	Not Tested - Debug only
1CB.012.0x         HT         N/T         Not Tested - Debug only           1CB.012.0x         LT         N/T         Not Tested - Debug only           1CB.013.0x         NT         PASS           1CB.013.0x         HT         N/T         Not Tested - Debug only           1CB.013.0x         LT         N/T         Not Tested - Debug only           1CB.014.0x         NT         PASS           1CB.014.0x         HT         N/T         Not Tested - Debug only           1CB.015.0x         NT         PASS           1CB.015.0x         HT         N/T         Not Tested - Debug only	1CB.011.0x	LT	N/T	Not Tested - Debug only
1CB.012.0x         LT         N/T         Not Tested - Debug only           1CB.013.0x         NT         PASS           1CB.013.0x         HT         N/T         Not Tested - Debug only           1CB.013.0x         LT         N/T         Not Tested - Debug only           1CB.014.0x         NT         PASS           1CB.014.0x         HT         N/T         Not Tested - Debug only           1CB.015.0x         NT         PASS           1CB.015.0x         HT         N/T         Not Tested - Debug only	1CB.012.0x	NT	PASS	
1CB.013.0x         NT         PASS           1CB.013.0x         HT         N/T         Not Tested - Debug only           1CB.013.0x         LT         N/T         Not Tested - Debug only           1CB.014.0x         NT         PASS           1CB.014.0x         HT         N/T         Not Tested - Debug only           1CB.014.0x         LT         N/T         Not Tested - Debug only           1CB.015.0x         NT         PASS           1CB.015.0x         HT         N/T         Not Tested - Debug only	1CB.012.0x	HT	N/T	Not Tested - Debug only
1CB.013.0x         HT         N/T         Not Tested - Debug only           1CB.013.0x         LT         N/T         Not Tested - Debug only           1CB.014.0x         NT         PASS           1CB.014.0x         HT         N/T         Not Tested - Debug only           1CB.014.0x         LT         N/T         Not Tested - Debug only           1CB.015.0x         NT         PASS           1CB.015.0x         HT         N/T         Not Tested - Debug only	1CB.012.0x	LT	N/T	Not Tested - Debug only
1CB.013.0x         LT         N/T         Not Tested - Debug only           1CB.014.0x         NT         PASS           1CB.014.0x         HT         N/T         Not Tested - Debug only           1CB.014.0x         LT         N/T         Not Tested - Debug only           1CB.015.0x         NT         PASS           1CB.015.0x         HT         N/T         Not Tested - Debug only	1CB.013.0x	NT	PASS	
1CB.014.0x         NT         PASS           1CB.014.0x         HT         N/T         Not Tested - Debug only           1CB.014.0x         LT         N/T         Not Tested - Debug only           1CB.015.0x         NT         PASS           1CB.015.0x         HT         N/T         Not Tested - Debug only	1CB.013.0x	HT	N/T	Not Tested - Debug only
1CB.014.0x         NT         PASS           1CB.014.0x         HT         N/T         Not Tested - Debug only           1CB.014.0x         LT         N/T         Not Tested - Debug only           1CB.015.0x         NT         PASS           1CB.015.0x         HT         N/T         Not Tested - Debug only	1CB.013.0x	LT	N/T	Not Tested - Debug only
1CB.014.0x         HT         N/T         Not Tested - Debug only           1CB.014.0x         LT         N/T         Not Tested - Debug only           1CB.015.0x         NT         PASS           1CB.015.0x         HT         N/T         Not Tested - Debug only		NT		
1CB.014.0x         LT         N/T         Not Tested - Debug only           1CB.015.0x         NT         PASS           1CB.015.0x         HT         N/T         Not Tested - Debug only		HT	N/T	Not Tested - Debug only
1CB.015.0x         NT         PASS           1CB.015.0x         HT         N/T         Not Tested - Debug only	1CB.014.0x	LT		
1CB.015.0x HT Not Tested - Debug only				
				Not Tested - Debug only
	1CB.015.0x		N/T	

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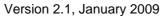


Test Case	Parameters	Result	Comment
1CB.016.0x	NT	PASS	
1CB.016.0x	HT	N/T	Not Tested - Debug only
1CB.016.0x	LT	N/T	Not Tested - Debug only
1CB.017.0x	NTNV	N/T	Not Tested - Debug only
1CB.017.0x	HTNV	N/T	Not Tested - Debug only
1CB.017.0x	LTNV	N/T	Not Tested - Debug only
1CB.017.0x	NTLV	N/T	Not Tested - Debug only
1CB.017.0x	HTLV	N/T	Not Tested - Debug only
1CB.017.0x	LTLV	N/T	Not Tested - Debug only
1CB.017.0x	NTHV	N/T	Not Tested - Debug only
1CB.017.0x	HTHV	N/T	Not Tested - Debug only
1CB.017.0x	LTHV	N/T	Not Tested - Debug only
1CB.018.0x	NTNV	N/T	Not Tested - Debug only
1CB.018.0x	HTNV	N/T	Not Tested - Debug only
1CB.018.0x	LTNV	N/T	Not Tested - Debug only
1CB.018.0x	NTLV	N/T	Not Tested - Debug only
1CB.018.0x	HTLV	N/T	Not Tested - Debug only
1CB.018.0x	LTLV	N/T	Not Tested - Debug only
1CB.018.0x	NTHV	N/T	Not Tested - Debug only
1CB.018.0x	HTHV	N/T	Not Tested - Debug only
1CB.018.0x	LTHV	N/T	Not Tested - Debug only
1CC.001.0x	NT	PASS	
1CC.001.0x	HT	N/T	Not Tested - Debug only
1CC.001.0x	LT.	N/T	Not Tested - Debug only
1CC.002.0x	NT	PASS	
1CC.002.0x	НТ	N/T	Not Tested - Debug only
1CC.002.0x	LT	N/T	Not Tested - Debug only
1CC.003.0x	Mandatory	PASS	
1CC.004.0x	Mandatory	PASS	
1CE.001.00	Mandatory	PASS	
1CE.002.00	Mandatory	PASS	
1CE.003.00	Mandatory	PASS	
1CE.004.00	Mandatory	PASS	
1CE.005.00	Mandatory	PASS	
1CE.005.01	Mandatory	PASS	
1CE.005.02	Mandatory	PASS	
1CE.005.03	Mandatory	PASS	
1CE.005.10	Mandatory	PASS	
1CE.005.11	Mandatory	PASS	
1CE.005.12	Mandatory	PASS	
1CE.005.13	Mandatory	PASS	
1CE.005.20	Mandatory	PASS	
1CE.005.21	Mandatory	PASS	

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Test Case	Parameters	Result	Comment
1CE.005.22	Mandatory	PASS	
1CE.005.23	Mandatory	PASS	
1CE.005.30	Mandatory	PASS	•
1CE.005.31	Mandatory	PASS	
1CE.005.32	Mandatory	PASS	\ (\)\(\)
1CE.005.33	Mandatory	PASS	/\ )
1CE.005.40	Mandatory	PASS	
1CE.005.41	Mandatory	PASS	
1CE.005.42	Mandatory	PASS	00)
1CE.005.43	Mandatory	PASS	5.0
1CE.005.50	Mandatory	PASS	
1CE.005.51	Mandatory	PASS	
1CE.005.52	Mandatory	PASS	,
1CE.005.53	Mandatory	PASS	
1CE.006.00	Mandatory	PASS	
1CE.006.01	Mandatory	PASS	
1CE.006.02	Mandatory	PASS	
1CE.006.03	Mandatory	PASS	
1CE.006.10	Mandatory	PASS	
1CE.006.11	Mandatory	PASS	
1CE.006.12	Mandatory	PASS	
1CE.006.13	Mandatory	PASS	
1CE.006.20	Mandatory	PASS	
1CE.006.21	Mandatory	PASS	
1CE.006.22	Mandatory	PASS	
1CE.006.23	Mandatory	PASS	
1CE.007.00	Mandatory	PASS	
1CE.007.01	Mandatory	PASS	
1CE.007.10	EMV VII Item 7	PASS	
1CE.007.11	EMV VII Item 7	PASS	
1CE.007.20	EMV VII Item 7	PASS	
1CE.007.21	EMV VII Item 7	PASS	
1CE.008.00	Mandatory	PASS	
1CE.008.01	Mandatory	PASS	
1CE.008.10	EMV VII Item 7	PASS	
1CE.008.11	EMV VII Item 7	PASS	
1CE.008.20	EMV VII Item 7	PASS	
1CE.008.21	EMV VII Item 7	PASS	
1CE.009.00	Mandatory	PASS	
1CE.009.01	Mandatory	PASS	
1CE.009.02	Mandatory	PASS	
1CE.009.03	Mandatory	PASS	
1CE.009.04	Mandatory	PASS	

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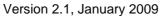


Test Case	Parameters	Result	Comment
1CE.010.00	Mandatory	PASS	
1CE.010.01	Mandatory	PASS	
1CE.010.02	Mandatory	PASS	
1CE.011.00	Mandatory	PASS	
1CE.011.01	Mandatory	PASS	
1CE.011.02	Mandatory	PASS	
1CE.011.03	Mandatory	PASS	
1CE.011.04	Mandatory	PASS	
1CE.012.00	Mandatory	PASS	
1CE.012.01	Mandatory	PASS	50'
1CE.012.02	Mandatory	PASS	
1CE.013.00	Mandatory	PASS	$\sim$
1CE.014.00	Mandatory	PASS	)
1CE.015.00	Mandatory	PASS	
1CE.016.00	Mandatory	PASS	
1CE.017.00	Mandatory	PASS	
1CE.017.01	Mandatory	PASS	
1CE.017.02	Mandatory	PASS	
1CE.017.03	Mandatory	PASS	
1CE.017.04	Mandatory	PASS	
1CE.018.00	Mandatory	PASS	
1CE.018.01	Mandatory	PASS	
1CE.018.02	Mandatory	PASS	
1CE.018.03	Mandatory	PASS	
1CE.018.04	Mandatory	PASS	
1CE.018.05	Mandatory	PASS	
1CE.018.06	Mandatory	PASS	
1CE.019.00	Mandatory	PASS	
1CE.019.01	Mandatory	PASS	
1CE.020.00	Mandatory	PASS	
1CE.020.01	Mandatory	PASS	
1CE.021.00	Mandatory	PASS	
1CE.021.01	Mandatory	PASS	
1CE.021.02	Mandatory	PASS	
1CE.022.00	Mandatory	PASS	
1CE.022.01	Mandatory	PASS	
1CE.022.02	Mandatory	PASS	
1CE.023.00	Mandatory	PASS	
1CE.024.00	Mandatory	PASS	
1CE.024.01	Mandatory	PASS	
1CE.024.02	Mandatory	PASS	
1CE.024.03	Mandatory	PASS	
1CE.025.00	Mandatory	PASS	

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Test Case	Parameters	Result	Comment
1CE.025.01	Mandatory	PASS	
1CE.026.00	Mandatory	PASS	
1CE.026.01	Mandatory	PASS	
1CE.026.02	Mandatory	PASS	a lX
1CE.027.00	Mandatory	PASS	401
1CE.028.00	Mandatory	PASS	
1CE.029.00	Mandatory	PASS	
1CE.030.00	Mandatory	PASS	
1CE.031.00	Mandatory	PASS	00)
1CE.032.00	Mandatory	PASS	50
1CE.033.00	Mandatory	PASS	
1CE.033.01	Mandatory	PASS	
1CE.033.02	Mandatory	PASS	,
1CE.034.00	Mandatory	PASS	
1CE.034.01	Mandatory	PASS	
1CE.034.02	Mandatory	PASS	
1CE.035.00	Mandatory	PASS	
1CE.036.00	Mandatory	PASS	
1CE.037.00	Mandatory	PASS	
1CE.037.01	Mandatory	PASS	
1CE.037.02	Mandatory	PASS	
1CE.037.03	Mandatory	PASS	
1CE.050.00-1	Option 1	N/R	Not implemented by the INQ test tool
1CE.050.00-2	Option 2	PASS	
1CE.051.00-1	Option 1	N/R	Not implemented by the INQ test tool
1CE.051.00-2	Option 2	PASS	
1CE.052.00-1	Option 1	PASS	
1CE.052.00-2	Option 2	PASS	
1CE.053.00-1	Option 1	PASS	
1CE.053.00-2	Option 2	PASS	
1CE.054.00-1	Option 1	N/R	Not implemented by the INQ test tool
1CE.054.00-2	Option 2	PASS	
1CE.055.00-1	Option 1	N/R	Not implemented by the INQ test tool
1CE.055.00-2	Option 2	PASS	
1CE.056.00	Mandatory	PASS	
1CE.056.01	Mandatory	PASS	
1CE.057.00	Mandatory	PASS	
1CE.057.01	Mandatory	PASS	
1CE.058.00	Mandatory	PASS	
1CE.058.01	Mandatory	PASS	

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Test Case	Parameters	Result	Comment
1CE.058.02	Mandatory	PASS	
1CE.058.03	Mandatory	PASS	
1CE.059.00	Mandatory	PASS	•
1CE.059.01	Mandatory	PASS	
1CE.059.02	Mandatory	PASS	40/
1CE.059.03	Mandatory	PASS	
1CE.060.00	EMV Ref VII Item 5	N/A	Terminal Continues Card Session
1CE.060.01	EMV Ref VII Item 5	N/A	Terminal Continues Card Session
1CE.061.00	EMV Ref VII Item 5	N/A	Terminal Continues Card Session
1CE.061.01	EMV Ref VII Item 5	N/A	Terminal Continues Card Session
1CE.062.00	EMV Ref IX Item 3	PASS	
1CE.062.01	EMV Ref IX Item 3	PASS	
1CE.062.02	EMV Ref IX Item 3, Post Processing on PASS	PASS	
1CE.062.03	EMV Ref IX Item 3, Post Processing on PASS	PASS	
1CE.063.00	EMV Ref IX Item 3	PASS	
1CE.063.01	EMV Ref IX Item 3	PASS	
1CE.064.00	Mandatory	PASS	
1CE.064.01	Mandatory	PASS	
1CE.064.02	Mandatory	PASS	
1CE.064.03	Mandatory	PASS	
1CE.065.00	Mandatory	PASS	
1CE.065.01	Mandatory	PASS	
1CE.066.00	Mandatory	PASS	
1CE.066.01	Mandatory	PASS	
1CE.067.00	Mandatory	PASS	
1CE.068.00	Mandatory	PASS	
1CE.069.00	Mandatory	PASS	
1CE.070.00	Mandatory	PASS	
1CE.071.00	Mandatory	PASS	
1CE.071.01	EMV Ref IX Item 16	N/A	Terminal supports TC2 value 0B
1CE.071.02	EMV Ref IX Item 16	N/A	Terminal supports TC2 value 01
1CE.071.03	EMV Ref IX Item 16	N/A	Terminal supports TC2 value 09
1CE.072.00	Mandatory	PASS	
1CE.072.01	EMV Ref IX Item 16	N/A	Terminal supports TC2 value 0B
1CE.072.02	EMV Ref IX Item 16	N/A	Terminal supports TC2 value 01
1CE.072.03	EMV Ref IX Item 16	N/A	Terminal supports TC2 value 09
1CE.073.00	Mandatory	PASS	
1CE.073.01	Mandatory	PASS	
1CE.073.02	Mandatory	PASS	

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Test Case	Parameters	Result	Comment
1CE.074.00	Mandatory	PASS	
1CE.074.01	Mandatory	PASS	
1CE.074.02	Mandatory	PASS	
1CE.075.00	Mandatory	PASS	a lX
1CE.075.01	Mandatory	PASS	A ()\
1CE.075.02	Mandatory	PASS	
1CE.076.00	Mandatory	PASS	0
1CE.076.01	Mandatory	PASS	
1CE.076.02	Mandatory	PASS	70)
1CE.077.00	Mandatory	PASS	50
1CE.077.01	Mandatory	PASS	
1CE.077.02	Mandatory	PASS	
1CE.077.03	Mandatory	PASS	,
1CE.078.00	Mandatory	PASS	
1CE.078.01	Mandatory	PASS	
1CE.078.02	Mandatory	PASS	
1CE.078.03	Mandatory	PASS	
1CE.079.00	Mandatory	PASS	
1CE.079.01	Mandatory	PASS	
1CE.080.00	Mandatory	PASS	
1CE.080.01	Mandatory	PASS	
1CE.081.00	Mandatory	PASS	
1CE.081.01	Mandatory	PASS	
1CE.082.00	Mandatory	PASS	
1CE.082.01	Mandatory	PASS	
1CF.001.00	Mandatory	PASS	
1CF.002.00	Mandatory	PASS	
1CF.003.00	Mandatory	PASS	
1CF.004.00	Mandatory	PASS	
1CF.005.00	Mandatory	PASS	
1CF.005.01	Mandatory	PASS	
1CF.005.02	Mandatory	PASS	
1CF.006.00	Mandatory	PASS	
1CF.006.01	Mandatory	PASS	
1CF.006.02	Mandatory	PASS	
1CF.007.00	Mandatory	PASS	
1CF.008.00	Mandatory	PASS	
1CF.009.00	Mandatory	PASS	
1CF.010.00	Mandatory	PASS	
1CF.011.00	Mandatory	PASS	
1CF.011.01	Mandatory	PASS	
1CF.012.00	Mandatory	PASS	
1CF.012.01	Mandatory	PASS	

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Test Case	Parameters	Result	Comment
1CF.013.00	Mandatory	PASS	
1CF.013.01	Mandatory	PASS	
1CF.013.02	Mandatory	PASS	
1CF.014.00	Mandatory	PASS	a lX
1CF.014.01	Mandatory	PASS	
1CF.015.00	Mandatory	PASS	/( )
1CF.015.01	Mandatory	PASS	
1CF.016.00	Mandatory	PASS	
1CF.016.01	Mandatory	PASS	
1CF.016.02	Mandatory	PASS	3 7
1CF.016.03	Mandatory	PASS	
1CF.016.04	Mandatory	PASS	
1CF.016.05	Mandatory	PASS	,
1CF.016.06	Optional	PASS	
1CF.016.07	Optional	PASS	
1CF.016.08	Optional	PASS	
1CF.016.09	Optional	PASS	
1CF.016.10	Optional	PASS	
1CF.016.11	Optional	PASS	
1CF.016.12	Optional	PASS	
1CF.016.13	Optional	PASS	
1CF.016.14	Optional	PASS	
1CF.016.15	Mandatory	PASS	
1CF.016.16	Mandatory	PASS	
1CF.016.17	Optional	PASS	
1CF.016.18	Optional	PASS	
1CF.016.19	Optional	PASS	
1CF.016.20	Optional	PASS	
1CF.016.21	Optional	PASS	
1CF.016.22	Optional	PASS	
1CF.016.23	Optional	PASS	
1CF.016.24	Optional	PASS	
1CF.016.25	Optional	PASS	
1CF.016.26	Optional	PASS	
1CF.016.27	Optional	PASS	
1CF.016.28	Optional	PASS	
1CF.030.00-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.030.01-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.030.02-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.030.00-2	Option 2	PASS	
1CF.030.01-2	Option 2	PASS	

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Test Case	Parameters	Result	Comment
1CF.030.02-2	Option 2	PASS	
1CF.031.00-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.031.01-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.031.02-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.031.00-2	Option 2	PASS	
1CF.031.01-2	Option 2	PASS	
1CF.031.02-2	Option 2	PASS	02)
1CF.032.00	Mandatory	PASS	
1CF.032.01	Mandatory	PASS	
1CF.033.00	Mandatory	PASS	
1CF.033.01	Mandatory	PASS	,
1CF.034.00	Mandatory	PASS	
1CF.035.00	Mandatory	PASS	
1CF.035.01	Mandatory	PASS	
1CF.035.02	Mandatory	PASS	
1CF.036.00	Mandatory	PASS	
1CF.037.00-1	Option 1	PASS	
1CF.037.01-1	Option 1	PASS	
1CF.037.02-1	Option 1	PASS	
1CF.037.03-1	Option 1	PASS	
1CF.037.04-1	Option 1	PASS	
1CF.037.00-2	Option 2	N/R	Not implemented by the INQ test tool
1CF.038.00	Mandatory	PASS	
1CF.039.00	Mandatory	PASS	
1CF.039.01	Mandatory	PASS	
1CF.039.02	Mandatory	PASS	
1CF.051.00	Mandatory	PASS	
1CF.052.00	Mandatory	PASS	
1CF.052.01	Mandatory	PASS	
1CF.052.02	Mandatory	PASS	
1CF.052.03	Mandatory	PASS	
1CF.052.04	Mandatory	PASS	
1CF.053.00	Mandatory	PASS	
1CF.053.01	Mandatory	PASS	
1CF.053.02	Mandatory	PASS	
1CF.053.03	Mandatory	PASS	
1CF.053.04	Mandatory	PASS	
1CF.053.10	Mandatory	PASS	
1CF.053.11	Mandatory	PASS	
1CF.053.12	Mandatory	PASS	

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Test Case	Parameters	Result	Comment
1CF.053.13	Mandatory	PASS	
1CF.053.14	Mandatory	PASS	
1CF.053.20	Mandatory	PASS	
1CF.053.21	Mandatory	PASS	a lX
1CF.053.22	Mandatory	PASS	A ()
1CF.053.23	Mandatory	PASS	/\ )
1CF.053.24	Mandatory	PASS	
1CF.054.00	Mandatory	PASS	
1CF.055.00	Mandatory	PASS	00)
1CF.056.00	Mandatory	PASS	5.0
1CF.057.00	Mandatory	PASS	
1CF.057.01	Mandatory	PASS	
1CF.057.02	Mandatory	PASS	,
1CF.058.00	Mandatory	PASS	
1CF.059.00	Mandatory	PASS	
1CF.060.00	Mandatory	PASS	
1CF.061.00	Mandatory	PASS	
1CF.062.00	Mandatory	PASS	
1CF.063.00	Mandatory	PASS	
1CF.063.01	Mandatory	PASS	
1CF.063.02	Optional	PASS	
1CF.063.03	Optional	PASS	
1CF.063.04	Optional	PASS	
1CF.063.05	Optional	PASS	
1CF.063.06	Optional	PASS	
1CF.063.07	Optional	PASS	
1CF.063.08	Optional	PASS	
1CF.064.00	Mandatory	PASS	
1CF.064.01	Optional	PASS	
1CF.064.02	Optional	PASS	
1CF.064.03	Optional	PASS	
1CF.080.00-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.080.01-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.080.02-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.080.03-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.080.04-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.080.10-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.080.11-1	Option 1	N/R	Not implemented by the INQ test tool

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Test Case	Parameters	Result	Comment
1CF.080.12-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.080.13-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.080.14-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.080.00-2	Option 2	PASS	
1CF.080.01-2	Option 2	PASS	
1CF.080.02-2	Option 2	PASS	
1CF.080.03-2	Option 2	PASS	
	Option 2	PASS	
1CF.080.10-2	Option 2	PASS	
1CF.080.11-2	Option 2	PASS	
	Option 2	PASS	)
	Option 2	PASS	
	Option 2	PASS	
	Option 1	N/R	Not implemented by the INQ test tool
1CF.081.01-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.081.02-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.081.03-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.081.04-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.081.10-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.081.11-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.081.12-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.081.13-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.081.14-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.081.20-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.081.21-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.081.22-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.081.23-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.081.24-1	Option 1	N/R	Not implemented by the INQ test tool
1CF.081.00-2	Option 2	PASS	
	Option 2	PASS	
	Option 2	PASS	

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Test Case	Parameters	Result	Comment
1CF.081.03-2	Option 2	PASS	
1CF.081.04-2	Option 2	PASS	
1CF.081.10-2	Option 2	PASS	
1CF.081.11-2	Option 2	PASS	
1CF.081.12-2	Option 2	PASS	ON
	•		19)
1CF.081.13-2	Option 2	PASS	
1CF.081.14-2	Option 2	PASS	
1CF.081.20-2	Option 2	PASS	
1CF.081.21-2	Option 2	PASS	
1CF.081.22-2	Option 2	PASS	
1CF.081.23-2	Option 2	PASS	
1CF.081.24-2	Option 2	PASS	
1CF.081.00-	Option 1	N/R	Not implemented by the INQ test
1(bis)	Spilon !		tool
1CF.081.01-	Option 1	N/R	Not implemented by the INQ test
1(bis)	•		tool
1CF.081.02-	Option 1	N/R	Not implemented by the INQ test
1(bis)	$\sim$ 0	,	tool
1CF.081.03-	Option 1	N/R	Not implemented by the INQ test
1(bis)		11/D	tool
1CF.081.04-	Option 1	N/R	Not implemented by the INQ test
1(bis) 1CF.081.10-	Option 1	N/R	tool  Not implemented by the INQ test
1(bis)	Option 1	IN/IX	tool
1CF.081.11-	Option 1	N/R	Not implemented by the INQ test
1(bis)	Spilon	1011	tool
1CF.081.12-	Option 1	N/R	Not implemented by the INQ test
1(bis)			tool
1CF.081.13-	Option 1	N/R	Not implemented by the INQ test
1(bis)	<b>&gt;</b>		tool
1CF.081.14-	Option 1	N/R	Not implemented by the INQ test
1(bis)	Outin 4	N/D	tool
1CF.081.20-	Option 1	N/R	Not implemented by the INQ test tool
1(bis) 1CF.081.21-	Option 1	N/R	Not implemented by the INQ test
1(bis)	Option 1	IN/IX	tool
1CF.081.22-	Option 1	N/R	Not implemented by the INQ test
1(bis)	- F		tool
1CF.081.23-	Option 1	N/R	Not implemented by the INQ test
1(bis)			tool
1CF.081.24-	Option 1	N/R	Not implemented by the INQ test
1(bis)			tool
1CF.081.00-	Option 2	PASS	
2(bis)	Outin 0	DA 00	
1CF.081.01-	Option 2	PASS	
2(bis) 1CF.081.02-	Option 2	PASS	
2(bis)	Οριίστ Ζ	1 700	
در013)	<u> </u>		<u> </u>

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Test Case	Parameters	Result	Comment
1CF.081.03- 2(bis)	Option 2	PASS	
1CF.081.04- 2(bis)	Option 2	PASS	٨.
1CF.081.10- 2(bis)	Option 2	PASS	40/1
1CF.081.11- 2(bis)	Option 2	PASS	
1CF.081.12- 2(bis)	Option 2	PASS	OV.
1CF.081.13- 2(bis)	Option 2	PASS	3
1CF.081.14- 2(bis)	Option 2	PASS	O
1CF.081.20- 2(bis)	Option 2	PASS	)
1CF.081.21- 2(bis)	Option 2	PASS	
1CF.081.22- 2(bis)	Option 2	PASS	
1CF.081.23- 2(bis)	Option 2	PASS	
1CF.081.24- 2(bis)	Option 2	PASS	
1CF.082.00	Mandatory	PASS	
1CF.082.01	Mandatory	PASS	
1CF.082.02	Mandatory	PASS	
1CF.083.00-1	Option 1	PASS	
1CF.083.01-1	Option 1	PASS	
1CF.083.02-1	Option 1	PASS	
1CF.083.03-1	Option 1	PASS	
1CF.083.04-1	Option 1	PASS	
1CF.083.10-1	Option 1	PASS	
1CF.083,11-1	Option 1	PASS	
1CF.083.12-1	Option 1	PASS	
1CF.083.13-1	Option 1	PASS	
1CF.083.14-1	Option 1	PASS	
1CF.083.20-1	Option 1	PASS	
1CF.083.21-1	Option 1	PASS	
1CF.083.22-1	Option 1	PASS	
1CF.083.23-1	Option 1	PASS	
1CF.083.24-1	Option 1	PASS	
1CF.083.00-2	Option 2	N/A	Terminal supports Deactivation
1CF.083.01-2	Option 2	N/A	Terminal supports Deactivation
1CF.083.02-2	Option 2	N/A	Terminal supports Deactivation
1CF.083.03-2	Option 2	N/A	Terminal supports Deactivation
1CF.083.04-2	Option 2	N/A	Terminal supports Deactivation
107.003.04-2	Οριίθη Ζ	IN/ <i>F</i> A	reminal supports Deactivation

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Test Case	Parameters	Result	Comment
1CF.083.10-2	Option 2	N/A	Terminal supports Deactivation
1CF.083.11-2	Option 2	N/A	Terminal supports Deactivation
1CF.083.12-2	Option 2	N/A	Terminal supports Deactivation
1CF.083.13-2	Option 2	N/A	Terminal supports Deactivation
1CF.083.14-2	Option 2	N/A	Terminal supports Deactivation
1CF.083.20-2	Option 2	N/A	Terminal supports Deactivation
1CF.083.21-2	Option 2	N/A	Terminal supports Deactivation
1CF.083.22-2	Option 2	N/A	Terminal supports Deactivation
1CF.083.23-2	Option 2	N/A	Terminal supports Deactivation
1CF.083.24-2	Option 2	N/A	Terminal supports Deactivation
1CF.084.00-1	Option 1	PASS	
1CF.084.01-1	Option 1	PASS	
1CF.084.02-1	Option 1	PASS	)
1CF.084.00-2	Option 2	N/R	Not implemented by the INQ test
	·		tool
1CF.085.00	Optional, EMV Ref XI Item	PASS	
105.005.01	1.1	7100	
1CF.085.01	Optional	PASS	
1CF.085.02	Optional	PASS	
1CF.085.03	Optional	PASS	
1CF.085.04	Optional	PASS	
1CF.085.05	Optional	PASS	
1CF.085.06	Optional	PASS	
1CF.085.07	Optional	PASS	
1CF.085.08	Optional	PASS	
1CF.085.09	Optional	PASS	
1CF.085.10	Optional	PASS	
1CF.085.11	Optional	PASS	
1CF.085.12	Optional	PASS	
1CF.085.13	Optional	PASS	
1CF.086.00-1	EMV Ref XI-2 Item 4, Option 1	PASS	
1CF.086.01-1	EMV Ref XI-2 Item 4, Option 1	PASS	
1CF.086.02-1	EMV Ref XI-2 Item 4,	PASS	
	Option 1		
1CF.086.00-2	EMV Ref XI-2 Item 4,	N/R	Not implemented by the INQ test
1CE 097 00	Option 2	DACC	tool
1CF.087.00	EMV Ref XI-2 Item 4, Mandatory	PASS	
1CF.087.01	EMV Ref XI-2 Item 4,	PASS	
	Mandatory		
1CF.087.02	EMV Ref XI-2 Item 4,	PASS	
105.00=	Mandatory	D.100	
1CF.087.03	EMV Ref XI-2 Item 4,	PASS	
1CF.087.04	Mandatory EMV Ref XI-2 Item 4,	PASS	
101.007.04	Mandatory	1 700	

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Test Case	Parameters	Result	Comment
1CF.087.05	EMV Ref XI-2 Item 4, Mandatory	PASS	
1CF.087.06	EMV Ref XI-2 Item 4, Mandatory	PASS	٨.
1CF.087.08	EMV Ref XI-2 Item 4, Mandatory	PASS	10/1
1CF.087.09	EMV Ref XI-2 Item 4, Mandatory	PASS	
1CF.087.10	EMV Ref XI-2 Item 4, Mandatory	PASS	-0/
1CF.087.11	EMV Ref XI-2 Item 4, Mandatory	PASS	
1CF.087.12	EMV Ref XI-2 Item 4, Mandatory	PASS	O
1CF.087.13	EMV Ref XI-2 Item 4, Mandatory	PASS	)
1CF.088.00	Mandatory	PASS	
1CF.088.01	Mandatory	PASS	
1CF.088.10	Mandatory	PASS	
1CF.088.11	Mandatory	PASS	
1CF.088.20	Mandatory	PASS	
1CF.088.21	Mandatory	PASS	
1CF.089.00	Mandatory	PASS	
1CF.089.01	EMV Ref XI Item 1.1	PASS	
1CF.089.02	Mandatory	PASS	
1CF.089.03	Mandatory	PASS	
1CF.089.04	Mandatory	PASS	
1CF.089.05	Mandatory	PASS	
1CF.089.06	Mandatory	PASS	
1CF.089.07	Mandatory	PASS	
1CF.089.08	Mandatory	PASS	
1CF.089.09	Mandatory	PASS	
1CF.089.10	Mandatory	PASS	
1CF.089.11	Mandatory	PASS	
1CF.089.12	Mandatory	PASS	
1CF.089.13	Mandatory	PASS	
1CF.090.00	EMV Ref XI Item 1.1	PASS	
1CF.090.01	Mandatory	PASS	
1CF.090.02	Mandatory	PASS	
	,		
1CF.090.03 1CF.090.04	Mandatory Mandatory	PASS PASS	
1CF.090.05	Mandatory	PASS	
1CF.090.06	Mandatory	PASS	
1CF.090.08	Mandatory	PASS	
1CF.090.09	Mandatory	PASS	
1CF.090.10	Mandatory	PASS	

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Test Case	Parameters	Result	Comment
1CF.090.11	Mandatory	PASS	
1CF.090.12	Mandatory	PASS	
1CF.090.13	Mandatory	PASS	
1CF.091.00-1	Optional, Option 1	PASS	a lX
1CF.091.01-1	Optional, Option 1	PASS	
1CF.091.02-1	Optional, Option 1	PASS	/\ )
1CF.091.00-2	Optional, Option 2	N/R	Not implemented by the INQ test tool
1CF.092.00	EMV Ref XI Item 1.1	PASS	
1CF.092.01	Mandatory	PASS	
1CF.092.02	Mandatory	PASS	
1CF.092.03	Mandatory	PASS	$\circ$
1CF.092.04	Mandatory	PASS	)
1CF.092.05	Mandatory	PASS	
1CF.092.06	Mandatory	PASS	
1CF.092.07	Mandatory	PASS	
1CF.092.08	Mandatory	PASS	
1CF.092.09	Mandatory	PASS	
1CF.092.10	Mandatory	PASS	
1CF.092.11	Mandatory	PASS	
1CF.092.12	Mandatory	PASS	
1CF.092.13	Mandatory	PASS	
1CF.093.00-1	Optional, Option 1	PASS	
1CF.093.00-2	Optional, Option 2	N/R	Not implemented by the INQ test tool
1CF.094.00	Optional	PASS	
1CF.095.00-1	Option 1	PASS	
1CF.095.01-1	Option 1	PASS	
1CF.095.02-1	Option 1	PASS	
1CF.095.00-2	Option 2	N/R	Not implemented by the INQ test tool
1CF.096.00	Optional, EMV Ref XI Item 1.1	PASS	
1CF.096.01	Optional	PASS	
1CF.096.02	Optional	PASS	
1CF.096.03	Optional	PASS	
1CF.096.04	Optional	PASS	
1CF.096.05	Optional	PASS	
1CF.096.06	Optional	PASS	
1CF.096.07	Optional	PASS	
1CF.096.08	Optional	PASS	
1CF.096.09	Optional	PASS	
1CF.096.10	Optional	PASS	
1CF.096.11	Optional	PASS	

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Issue Date: 17 September 2014



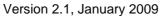
v.02092013a

Test Case	Parameters	Result	Comment
1CF.097.00-1	Optional, Option 1	PASS	
1CF.097.01-1	Optional, Option 1	PASS	
1CF.097.02-1	Optional, Option 1	PASS	
1CF.097.00-2	Optional, Option 2	N/R	Not implemented by the INQ test tool
1CF.098.00	Optional, EMV Ref XI Item 1.1	PASS	
1CF.098.01	Optional	PASS	
1CF.098.02	Optional	PASS	
1CF.098.03	Optional	PASS	
1CF.098.04	Optional	PASS	
1CF.098.05	Optional	PASS	
1CF.098.06	Optional	PASS	
1CF.098.07	Optional	PASS	
1CF.098.08	Optional	PASS	
1CF.098.09	Optional	PASS	
1CF.098.10	Optional	PASS	
1CF.098.11	Optional	PASS	
1CF.099.00	Mandatory	PASS	
1CF.100.00	Mandatory	PASS	
1CF.101.00	EMV Ref XI-2 Item 4, Mandatory	PASS	
1CF.102.00-1	Optional, Option 1	PASS	
1CF.102.01-1	Optional, Option 1	PASS	
1CF.102.02-1	Optional, Option 1	PASS	
1CF.102.00-2	Optional, Option 2	N/R	Not implemented by the INQ test tool
1CF.103.00	Optional, EMV Ref IX Item 1.1	PASS	
1CF.103.01	Optional	PASS	
1CF.103.02	Optional	PASS	
1CF.103.03	Optional	PASS	
1CF.103.04	Optional	PASS	
1CF.103.05	Optional	PASS	
1CF.103.06	Optional	PASS	
1CF.103.07	Optional	PASS	
1CF.103.08	Optional	PASS	
1CF.103.09	Optional	PASS	
1CF.103.10	Optional	PASS	
1CF.103.11	Optional	PASS	
1CF.103.12	Optional	PASS	
1CF.103.13	Optional	PASS	
1CF.104.00	Mandatory	PASS	
1CF.105.00-1	Option 1	PASS	
1CF.105.01-1	Option 1	PASS	

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Test Case	Parameters	Result	Comment
1CF.105.02-1	Option 1	PASS	
1CF.105.00-2	Option 2	N/R	Not implemented by the INQ test tool
1CF.106.00	EMV Ref XI Item 1.1	PASS	
1CF.106.01	Mandatory	PASS	.0
1CF.106.02	Mandatory	PASS	7(2)
1CF.106.03	Mandatory	PASS	
1CF.106.04	Mandatory	PASS	
1CF.106.05	Mandatory	PASS	201
1CF.106.06	Mandatory	PASS	
1CF.106.07	Mandatory	PASS	
1CF.106.08	Mandatory	PASS	$\mathcal{O}$
1CF.106.09	Mandatory	PASS	
1CF.106.10	Mandatory	PASS	
1CF.106.11	Mandatory	PASS	
1CF.107.00	Mandatory	PASS	
1CF.108.00-1	Optional, Option 1	PASS	
1CF.108.01-1	Optional, Option 1	PASS	
1CF.108.10-1	Optional, Option 1	PASS	
1CF.108.11-1	Optional, Option 1	PASS	
1CF.108.20-1	Optional, Option 1	PASS	
1CF.108.21-1	Optional, Option 1	PASS	
1CF.108.00-2	Optional, Option 2	N/R	Not implemented by the INQ test tool
1CF.108.01-2	Optional, Option 2	N/R	Not implemented by the INQ test tool
1CF.109.00	Optional, EMV Ref XI Item 1.1	PASS	
1CF.109.01	Optional	PASS	
1CF.109.02	Optional	PASS	
1CF.109.03	Optional	PASS	
1CF.109.04	Optional	PASS	
1CF.109.05	Optional	PASS	
1CF.109.06	Optional	PASS	
1CF.109.07	Optional	PASS	
1CF.109.08	Optional	PASS	
1CF.109.09	Optional	PASS	
1CF.109.10	Optional	PASS	
1CF.109.11	Optional	PASS	
1CF.109.12	Optional	PASS	
1CF.109.13	Optional	PASS	
1CF.110.00	Mandatory	PASS	
1CF.110.01	Mandatory	PASS	
1CF.110.02	Mandatory	PASS	

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1CF.110.03         Mandatory         PASS           1CF.110.04         Mandatory         PASS           1CF.110.05         Mandatory         PASS           1CF.110.06         Mandatory         PASS	
1CF.110.05 Mandatory PASS	
1CF.110.05 Mandatory PASS	^
· · · · · · · · · · · · · · · · · · ·	
	- 1X
1CF.110.08 Mandatory PASS	.0
1CF.110.09 Mandatory PASS	<del>/(Y)</del>
1CF.110.10 Mandatory PASS	
1CF.110.11 Mandatory PASS	
1CF.110.12 Mandatory PASS	
1CF.110.13 Mandatory PASS	
1CF.111.00 Mandatory PASS	
1CF.112.00-1 Option 1 PASS	
1CF.112.01-1	
1CF.112.02-1 Option 1 PASS	
·	ented by the INQ test
tool	•
1CF.113.00 EMV Ref XI Item 1.1 PASS	
1CF.113.01 Mandatory PASS	
1CF.113.02 Mandatory PASS	
1CF.113.03 Mandatory PASS	
1CF.113.04 Mandatory PASS	
1CF.113.05 Mandatory PASS	
1CF.113.06 Mandatory PASS	
1CF.113.07 Mandatory PASS	
1CF.113.08 Mandatory PASS	
1CF.113.09 Mandatory PASS	
1CF.113.10 Mandatory PASS	
1CF.113.11 Mandatory PASS	
1CF.114.00-1 EMV Ref XI-2 Item 4, PASS	
Option 1	
1CF.114.01-1 EMV Ref XI-2 Item 4, PASS Option 1	
1CF.114.02-1 EMV Ref XI-2 Item 4, PASS	
Option 1  1CF.114.00-2 EMV Ref XI-2 Item 4, N/R Not implement	ented by the INQ test
Option 2	ented by the mid test
1CF.115.00 EMV Ref XI-2 Item 4, EMV PASS	
Ref XI Item 1.1	
1CF.115.01 EMV Ref XI-2 Item 4, PASS Mandatory	
1CF.115.02 EMV Ref XI-2 Item 4, PASS	
Mandatory  1CF.115.03 EMV Ref XI-2 Item 4, PASS	
Mandatory	
1CF.115.04 EMV Ref XI-2 Item 4, PASS	
Mandatory	

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Test Case	Parameters	Result	Comment
1CF.115.05	EMV Ref XI-2 Item 4, Mandatory	PASS	
1CF.115.06	EMV Ref XI-2 Item 4, Mandatory	PASS	Λ.
1CF.115.07	EMV Ref XI-2 Item 4, Mandatory	PASS	40/1
1CF.115.08	EMV Ref XI-2 Item 4, Mandatory	PASS	
1CF.115.09	EMV Ref XI-2 Item 4, Mandatory	PASS	OV.
1CF.115.10	EMV Ref XI-2 Item 4, Mandatory	PASS	77
1CF.115.11	EMV Ref XI-2 Item 4, Mandatory	PASS	O
1CF.116.00	EMV Ref XI-2 Item 4, Mandatory	PASS	
1CF.117.00	EMV Ref XI Item 2.11	PASS	
1CF.117.01	EMV Ref XI Item 2.11	PASS	
1CF.117.02	EMV Ref XI Item 2.11	PASS	
1CF.118.00	Mandatory	PASS	
1CF.119.00-1	Option 1	PASS	
1CF.119.01-1	Option 1	PASS	
1CF.119.02-1	Option 1	PASS	
1CF.119.00-2	Option 2	N/R	Not implemented by the INQ test tool
1CF.120.00	Optional, EMV Ref XI Item 1.1	PASS	
1CF.120.01	Optional	PASS	
1CF.120.02	Optional	PASS	
1CF.120.03	Optional	PASS	
1CF.120.04	Optional	PASS	
1CF.120.05	Optional	PASS	
1CF.120.06	Optional	PASS	
1CF.120.07	Optional	PASS	
1CF.120.08	Optional	PASS	
1CF.120.09	Optional	PASS	
1CF.120.10	Optional	PASS	
1CF.120.11	Optional	PASS	
1CF.120.12	Optional	PASS	
1CF.120.13	Optional	PASS	
1CF.121.00-1	Optional, Option 1	PASS	
1CF.121.01-1	Optional, Option 1	PASS	
1CF.121.02-1	Optional, Option 1	PASS	
1CF.121.00-2	Optional, Option 2	N/R	Not implemented by the INQ test tool
1CF.122.00	EMV Ref XI Item 1.1	PASS	
1CF.122.01	Mandatory	PASS	

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Test Case	Parameters	Result	Comment
1CF.122.02	Mandatory	PASS	
1CF.122.03	Mandatory	PASS	
1CF.122.04	Mandatory	PASS	<u> </u>
1CF.122.05	Mandatory	PASS	
1CF.122.06	Mandatory	PASS	·
1CF.122.07	Mandatory	PASS	<b>100</b>
1CF.122.07	Mandatory	PASS	
	,		
1CF.122.09	Mandatory	PASS	
1CF.122.10	Mandatory	PASS	(2)
1CF.122.11	Mandatory	PASS	
1CF.122.12	Mandatory	PASS	
1CF.122.13	Mandatory	PASS	
1CF.124.00	EMV Ref XI-2 Item 4,	PASS	,
	Mandatory	$\bigcirc$	
1CF.124.01	EMV Ref XI-2 Item 4,	PASS	
105 101 00	Mandatory	71.00	
1CF.124.02	EMV Ref XI-2 Item 4,	PASS	
1CF.124.03	Mandatory EMV Ref XI-2 Item 4.	PASS	
101.124.03	Mandatory	FAGG	
1CF.124.04	EMV Ref XI-2 Item 4,	PASS	
	Mandatory		
1CF.124.10	EMV Ref XI-2 Item 4,	PASS	
	Mandatory		
1CF.124.11	EMV Ref XI-2 Item 4,	PASS	
105 101 10	Mandatory	2100	
1CF.124.12	EMV Ref XI-2 Item 4,	PASS	
1CF.124.13	Mandatory EMV Ref XI-2 Item 4,	PASS	
TCF.124.13	Mandatory	PASS	
1CF.124.14	EMV Ref XI-2 Item 4,	PASS	
101112111	Mandatory	17100	
1CF.124.20	EMV Ref XI-2 Item 4,	PASS	
<b>X</b> / \	Mandatory		
1CF.124.21	EMV Ref XI-2 Item 4,	PASS	
105 101 00	Mandatory	D.4.00	
1CF.124.22	EMV Ref XI-2 Item 4,	PASS	
1CF.124.23	Mandatory EMV Ref XI-2 Item 4,	PASS	
101.124.23	Mandatory	1 700	
1CF.124.24	EMV Ref XI-2 Item 4,	PASS	
	Mandatory		
1CF.126.00-1	EMV Ref XI-2 Item 4,	PASS	
	Option 1		
1CF.126.01-1	EMV Ref XI-2 Item 4,	PASS	
105 165 55 1	Option 1	D. 00	
1CF.126.02-1	EMV Ref XI-2 Item 4,	PASS	
	Option 1		

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Test Case	Parameters	Result	Comment
1CF.126.00-2	EMV Ref XI-2 Item 4, Option 2	N/R	Not implemented by the INQ test tool
1CF.151.00	Optional	PASS	
1CF.152.00	Mandatory	PASS	
1CF.152.01	Mandatory	PASS	. 0
1CF.153.00	Mandatory	PASS	7(9)
1CF.154.00	Mandatory	PASS	
1CF.155.00	Mandatory	PASS	
1CF.155.01	Mandatory	PASS	201
1CF.155.02	Mandatory	PASS	
1CF.155.03	Mandatory	PASS	
1CF.156.00	Mandatory	PASS	9
1CF.157.00	Mandatory	PASS	)
1CF.158.00	Mandatory	PASS	
1CF.158.01	Mandatory	PASS	
1CF.159.00	Mandatory	PASS	
1CF.159.01	Mandatory	PASS	
1CF.160.00	Mandatory	PASS	
1CF.160.01	Mandatory	PASS	
1CF.160.02	Mandatory	PASS	
1CF.160.03	Mandatory	PASS	
1CF.161.00	Mandatory	PASS	
1CF.161.01	Mandatory	PASS	
1CF.161.02	Mandatory	PASS	
1CF.161.03	Mandatory	PASS	
1CF.162.00	Mandatory	PASS	
1CF.162.01	Mandatory	PASS	
1CF.162.02	Mandatory	PASS	

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# 5.3 Key to Result Codes

The following codes are used in the table of results:

Code	Meaning
Pass	Test result shows that the requirements of the relevant test case have been satisfied.
Fail	Test result shows that the requirements of the relevant test case have not been satisfied. A Failed test result will have a detailed explanation in section "5.5. Special Notes" as to why the test case does not meet the requirements.
N/A	Test not applicable.
N/T	Not tested
Other	Test result shows that the requirements of the relevant test case have not been validated. Section "5.5. Special Notes" will contain a detailed analysis.

Note: Any failed test results shall be commented on in section "5.4. Special Notes". This section will contain detailed analysis of the failed test logs and other information about any exception tests performed and modifications that may be required for the purpose of executing the test cases.

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Reference Standard: EMVCo Type Approval, Terminal Level 1 Test cases,

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#### **Special Notes** 5.4

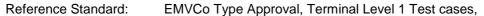
All tests where run on nominal temperatures and voltages

All tests requiring to be run at different temperatures and voltages are marked "N/T" in the

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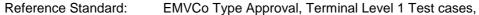
# **ANNEX B. - IMAGE OF IFM**

The following image was taken of the device during Debug



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