



SYNOPSYS®

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ALLIANCES

CERTIFICATE OF PARTICIPATION

This Certificate is Presented to

Mr. Dhairya Nandkumar Pandit

for Participating in the **Level 1** training of the India Semiconductor Workforce Development Program (ISWDP), in **Student/Industry** Category, held from October 1, 2024.

The Level 1 covered the basics of Semiconductor Device Technology, Technology CAD and TCAD Based Technology Design Workflow with emphasis on Basic 2D Device Creation, Device Simulation and Analysis.

Prof. Mayank Shrivastava
Program Director
Indian Institute of Science
(IISc)

23 October 2024

Date

Dr. Patrick Haspel
Senior Director
Synopsys Academic & Research Alliances
(SARA)