```
#include "LCD IO Init.h"
 3
 4
     void LCD IO Init()
 5
       RCC->APB2ENR |= RCC APB2ENR IOPCEN | RCC APB2ENR IOPBEN | RCC APB2ENR IOPAEN;
 6
 7
 8
       GPIOC->CRL |= GPIO_CRL_MODE7 | GPIO_CRL_MODE6 | GPIO_CRL_MODE5 | GPIO_CRL_MODE4 | GPIO_CRL_MODE3 |
     GPIO_CRL_MODE2 | GPIO_CRL_MODE1 | GPIO_CRL_MODE0 ;
 9
       GPIOC->CRL &= ~GPIO_CRL_CNF7 & ~GPIO_CRL_CNF6 & ~GPIO_CRL_CNF5 & ~GPIO_CRL_CNF4 & ~GPIO_CRL_CNF3 &
     ~GPIO CRL CNF2 & ~GPIO CRL CNF1 & ~GPIO CRL CNF0 ;
10
       GPIOB->CRL |= GPIO CRL MODE5 | GPIO CRL MODE1 | GPIO CRL MODE0 ;
11
       GPIOB->CRL &= ~GPIO CRL CNF5 & ~GPIO CRL CNF1 & ~GPIO CRL CNF0 ;
12
13
14
       commandToLCD(LCD 8B2L, 0);
       commandToLCD(LCD 8B2L, 0);
15
       commandToLCD(LCD 8B2L, 0);
16
17
       commandToLCD(LCD 8B2L, 0);
18
       commandToLCD(LCD DCB, 0);
19
       commandToLCD(LCD CLR, 0);
20
       commandToLCD(LCD MCR, 0);
21
     }
22
23
24
    * Name: commandToLCD
     * Type: PUBLIC
25
     ^{\star} Parameters: a single byte of command information for the LCD controller
26
     * Returns: nothing
27
28
     * Description: This function generates control timing and data signals to send one command byte to the
     LCD
29
     */
30
     void commandToLCD(uint8 t data, int CORD)
31
32
       if (CORD == 0)
33
       {
34
         GPIOB->BSRR = LCD CM ENA; //RS low, E high
35
         // GPIOC\rightarrow ODR = data; //BAD: may affect upper bits on port C
         GPIOC->ODR &= 0xFF00; //GOOD: clears the low bits without affecting high bits
36
37
         GPIOC->ODR |= data; //GOOD: only affects lowest 8 bits of Port C
38
         delay(80000);
39
         GPIOB->BSRR = LCD CM DIS; //RS low, E low
40
         delay(800000);
41
42
       //send to LDC screen aka display
       else if (CORD == 1)
43
44
45
         GPIOB->BSRR = LCD DM ENA; //RS low, E high
46
         // GPIOC->ODR = data; //BAD: may affect upper bits on port C
47
         GPIOC->ODR &= 0xFF00; //GOOD: clears the low bits without affecting high bits
         GPIOC->ODR |= data; //GOOD: only affects lowest 8 bits of Port C
48
49
         delay(80000);
50
         GPIOB->BSRR = LCD DM DIS; //RS low, E low
51
         delay(800000);
52
     }
53
54
5.5
56
57
58
```