HALF ADDER

AIM:

To write the verilog program for half adder circuit using the software.

SOFTWARE REQUIRED:

- ➢ Amd vivoda 2023.2
- ➢ Simulation tools

PROGRAM:

```
module half(a,b,s,c);

input a,b;

output s,c;

assign s=a^b;

assign c=a&b;

endmodule
```

OUTPUT: