

## ENCODER

**AIM:**

To write the verilog program for 4:2 encoder circuit using the software.

## SOFTWARE REQUIRED:

- Amd vivoda 2023.2
- Simulation tools

**PROGRAM:**

```
module encoder(a,d);
input [3:0]a;
output [1:0]d;
or g1(d[0],a[3],a[2]);
or g2(d[1],a[3],a[1]);
endmodule
```

### OUTPUT:



