HALF SUBTRACTOR

AIM:

To write the verilog program for half subtractor circuit using the software.

SOFTWARE REQUIRED:

- ➢ Amd vivoda 2023.2
- ➢ Simulation tools

PROGRAM:

```
module halfsubtractor(a,b,d,bor);

input a,b;

output d,bor;

assign d=a^b;

assign bor=~a&b;

endmodule
```

OUTPUT: