

## DEMULTIPLEXER

### AIM:

To write the verilog program for demultiplexer circuit using the software.

### SOFTWARE REQUIRED:

- Amd vivoda 2023.2
- Simulation tools

### PROGRAM:

```
module demux(a,b,d,y);  
input a,b,d;  
output [3:0]y;  
assign y[0]=(~a)&(~b)&d;  
assign y[1]=(~a)&(b)&d;  
assign y[2]=(a)&(~b)&d;  
assign y[3]=(a)&(b)&d;  
endmodule
```

OUTPUT:

