

D FLIPFLOP

AIM:

To write the verilog program for Dflipflop circuit using the software.

SOFTWARE REQUIRED:

- Amd vivoda 2023.2
- Simulation tools

PROGRAM:

```
module Dff(D,clk,rst,Q);  
input D,clk,rst;  
output reg Q;  
always @(posedge clk)  
begin  
    if(rst==1)  
        Q=1'b0;  
    else  
        Q=D;  
    end  
end  
endmodule
```

OUTPUT:

