DEPARTMENT
OF
COMPUTER SCIENCE AND ENGINEERING

RECORD

FOR

ANALYSIS AND DESIGN OF ALGORITHMS LABORATORY

BCSL404

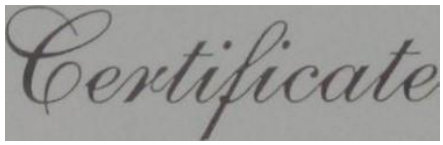
NAME:

USN:

STUDENT'S SIGNATURE **COURSE COORDINATOR SIGNATURE**

SRI VENKATESHWARA COLLEGE OF ENGINEERING

(Affiliated to VTU, Belgaum & Approved by AICTE. New Delhi)
An ISO 9001 :2008 Certified Institution
Vidyanagar, Bengaluru International Airport Road. Bengaluru - 562 157



This is to certify that Mr./Miss……..bearing USN…….has satisfactorily completed the course of Experiments in **Analysis And Design Of Algorithms Laboratory – BCSL404** as prescribed by the Visvesvaraya Technological University at the college during Academic Year: 2023-24.

Date …… **Signature of Course Teacher**

Signature of Head of the Department

[Dr. HEMA M S]

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11.		Design and implement C/C++ Program to sort a given set of n integer elements using Merge Sort method and compute its time complexity. Run the program for varied values of n> 5000, and record the time taken to sort. Plot a graph of the time taken versus n. The elements can be read from a file or can be generated using the random number generator	
12.		Design and implement C/C++ Program for N Queen's problem using Backtracking.	