

Gopala Dhar

27/37 Manish Nagar,
Four Bungalows, Andheri W,
Mumbai (Maharashtra, INDIA) - 400053
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CAREER OBJECTIVE

As an *Electronics Engineer*, in my Second Year I have developed an urge for practical knowledge, be it working on algorithms or interfacing Sensors.

I Believe that **Automation**, is the future and would like to pursue a Career in Automation with implementation of *Machine Learning* and *Neural Networks* along with *Computer Vision*.

I foresee its Implementation to solve Real Word issues and I would be gratified if I could help the World become a better Place.

ACADEMIC DETAILS

Examination	University/Board	Institute	Year	CGPA/%
Undergraduate (4th Sem, Electronics)	<i>Autonomous</i> (Mumbai University)	Sardar Patel Institute of Technology, Mumbai	2018	Pursuing
Undergraduate (3rd Sem, Electronics)	<i>Autonomous</i> (Mumbai University)	Sardar Patel Institute of Technology, Mumbai	2017	10
Undergraduate (1st Year, Electronics)	<i>Mumbai University</i>	Sardar Patel Institute of Technology, Mumbai	2017	9.04
Higher Secondary	CBSE	Delhi Public School, Jammu	2016	91.2
Matriculation	CBSE	Delhi Public School, Jammu	2014	10

PROJECTS AND COMPETITIONS

1. eYRC (2017)
2. Robocon (2016, 2017)
3. Land Mine Detection and Mapping (IICDC 2017)

TRAINING AND INTERNSHIP

- College Certified Course in **LabVIEW**
- College Certified Course in **Verilog**

RESEARCH PUBLICATIONS

1. BDR problem solving through SIMULINK and MATLAB has been taken up and outcome of results will be submitted for publication during this year

TECHNICAL SKILLS

- Possess hands on experience of Micro-controllers and Micro Computers such as Arduino, Raspberry Pi etc
- Indepth working knowledge of Image Processing through Open-CV
- Interfacing experience of a Host of Sensors including IR Arrays, Lidar Sensor, Ultrasonic Module etc
- Well versed with Pneumatics and DC motors including BLDC, Steppers and Servos
- Ability to design basic Circuits and Drivers

- Familiar with Languages: C, Python, VHDL, Verilog

SOFT SKILLS

1. Very good ability to work in a Team
2. Possess excellent Communication Skills
3. Problem Solving and Time Management attitude

EXTRA- CURRICULAR ACTIVITIES

- Member of Fire and Security Association of India
- Active Participant of Versova Beach Clean Drive
- Avid Badminton Player

CO- CURRICULAR ACTIVITIES

1. Member of the College Robocon Team From The Past Two Years
2. Organized Technical workshop for Juniors

PERSONAL DETAILS

Father's Name: Dr. Anil Dhar
Mother's Name: Dr. Asha Dhar
Sex: Male
Date of Birth: 29th October,1998
Nationality: Indian
Marital Status: Unmarried

REFERENCE

Dr. S.S. Rathod,
Ph.D. (IIT, Roorkee),
Dean Academics,
Sardar Patel Institute of Technology, Mumbai
Email: surendra_rathod@spit.ac.in
Mobile: 9920228275

DECLARATION

The aforesaid information is correct to the best of my knowledge and Belief.

Date: April 18, 2018

Gopala Dhar