**Student reg.no: 19BCE0298**

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**Course Code: CSE1003**

**Course Name: Digital Logic and Design**

**Due Date: 28-10-2021**

**Date of Submission: 28-10-2021**

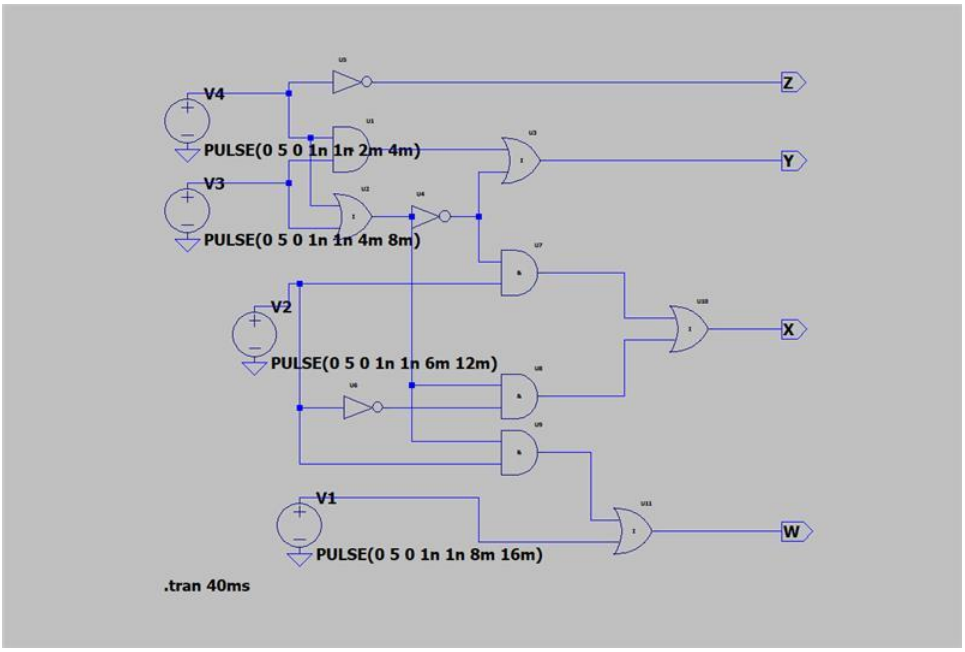
**Slot & Semester: L5+L6  FALL-2021-2022**

**Faculty: Dr. N.Sangeetha**

1. Construct the circuits of the below code converters using LT spice

**BCD to Excess 3**

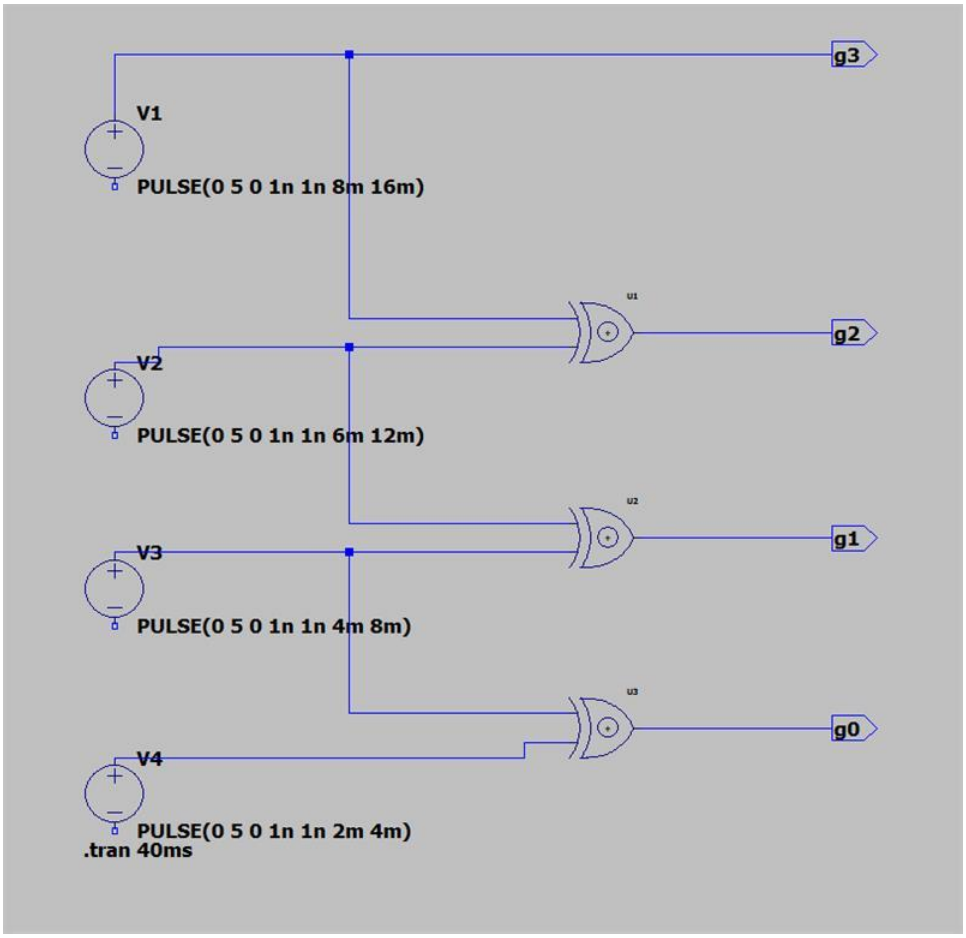
Simulation Circuit



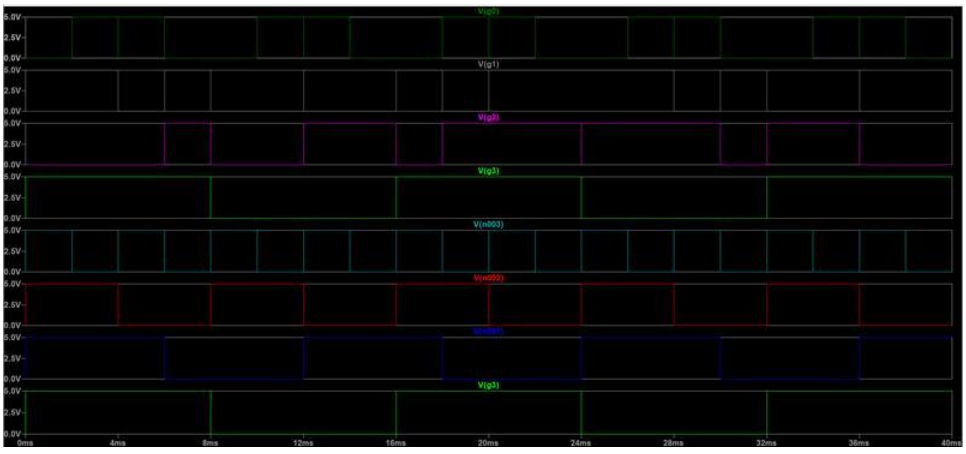

Output waveform

**Binary to gray**

Simulation Circuit

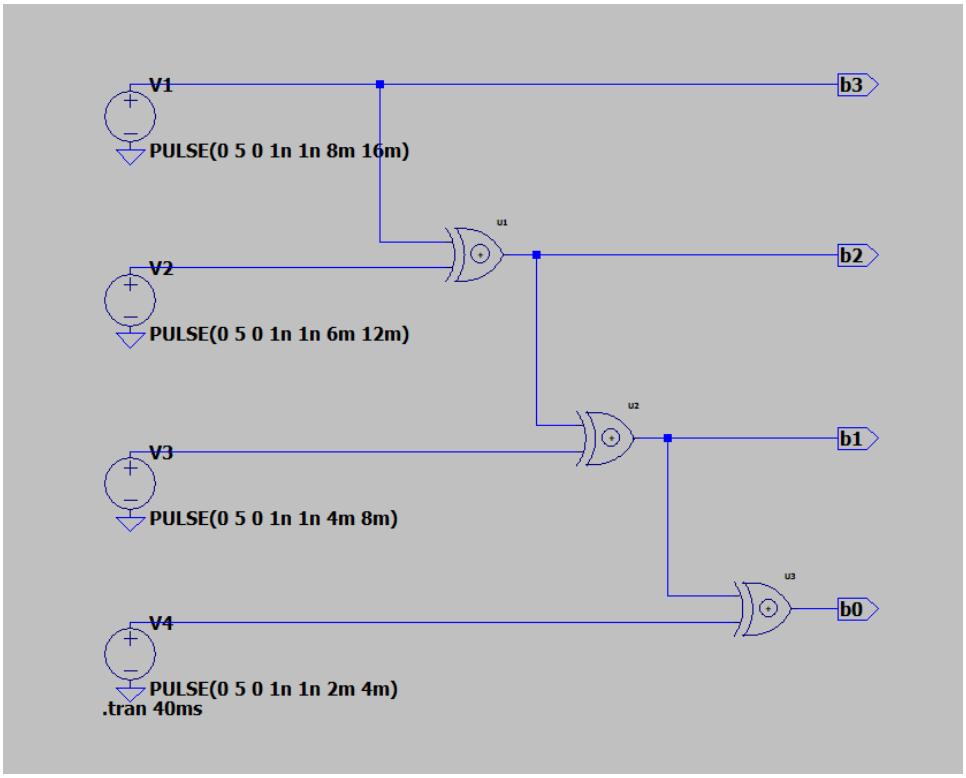



Output waveform

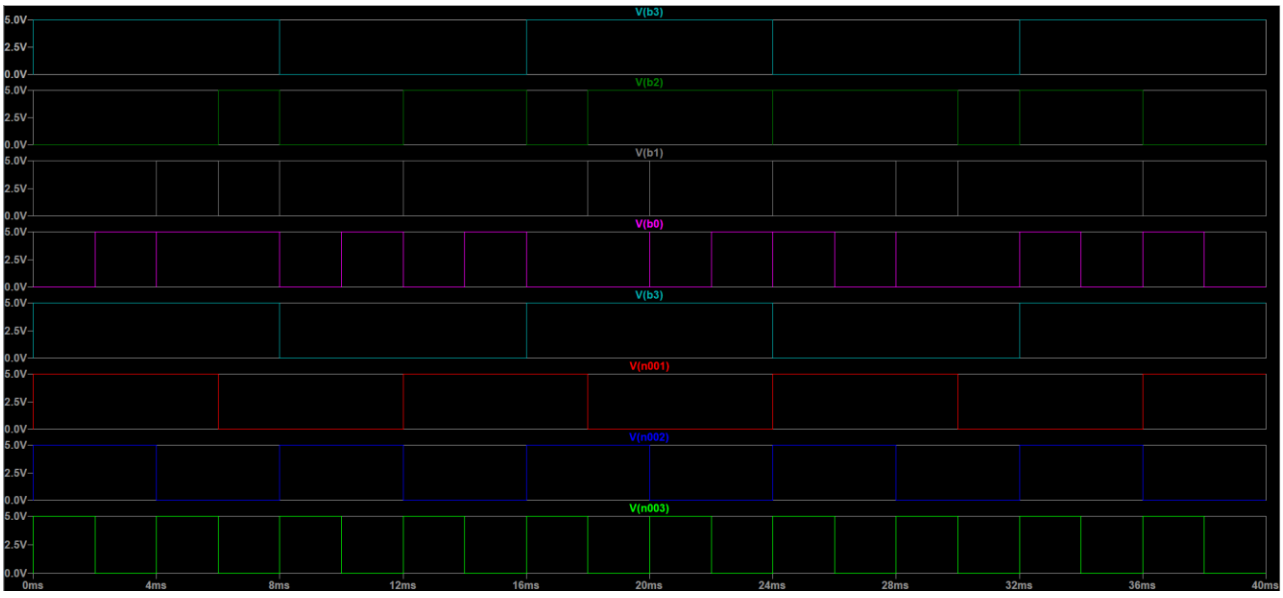


## Gray to Binary

Simulation Circuit

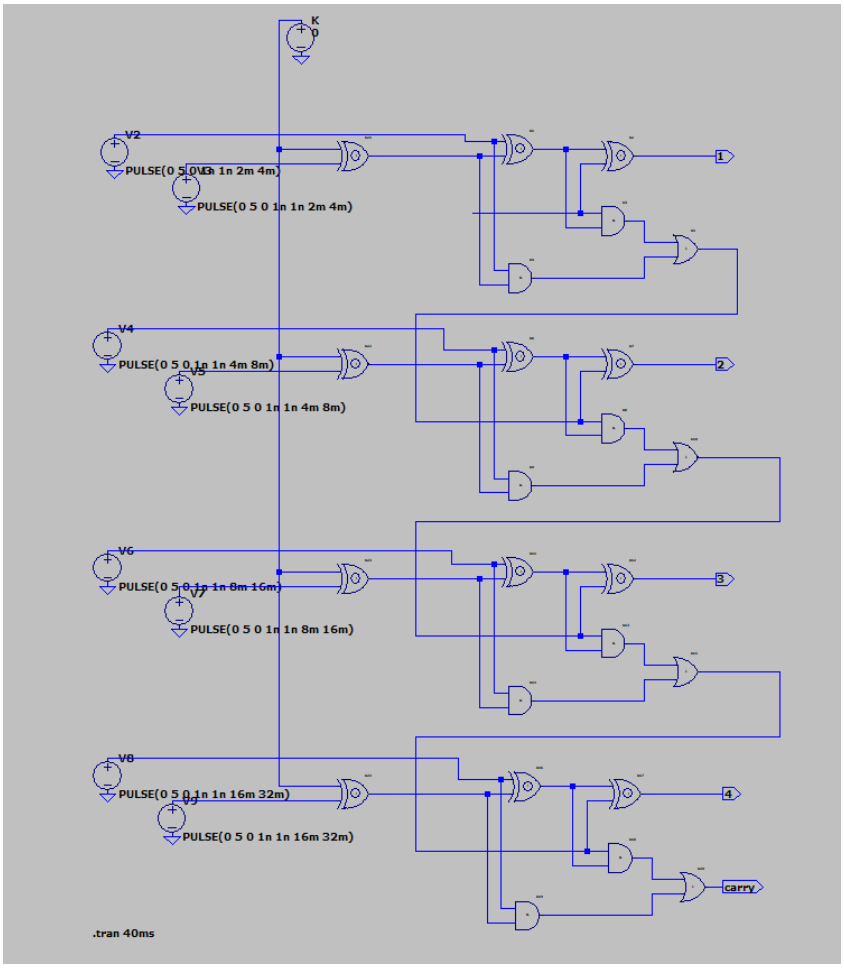


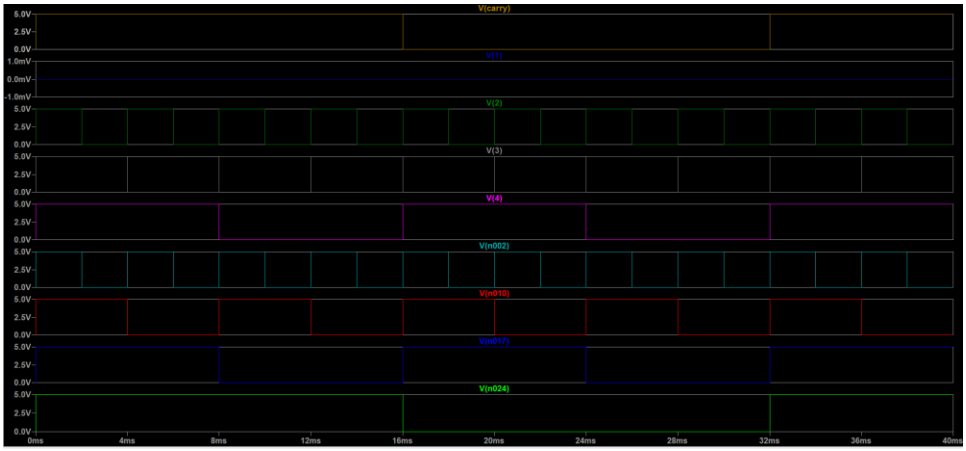

Output waveform



2. Construct 4 bit binary parallel adder using LT spice

Simulation Circuit




Output waveform