

GANPAT UNIVERSITY

U.V.PATEL COLLEGE OF ENGINEERING

5CEIT501: COMPUTER ARCHITECTURE & ORGANIZATION

FIRST INTERNAL EXAM SYLLABUS –SEPTEMBER 2022

Sr.No	Chapter Name	Topic
1	Memory Organization:	<ul style="list-style-type: none">● Memory Sub System,● Memory Hierarchy,● Main Memory,● Cache Memory,● Cache Mapping,<ol style="list-style-type: none">1. Associative cache mapping2. Direct Mapped cache3. Set Associative cache● Auxiliary memory

Important Link:

Set- associative memory

<https://www.youtube.com/watch?v=6dOsTTjzrU>

Direct mapped Cache memory

<https://www.youtube.com/watch?v=QcAaP5V2Gpc>

Theory of Cache Mapping Technique

1.<https://www.youtube.com/watch?v=kdwHTUSUdJM&list=PLLDc70psjvq5hIT0kfr1sirNuees0NIbG&index=19>.

2.

<https://www.youtube.com/watch?v=CU04MDfa6ys&list=PLLDC70psjvq5hIT0kfr1sirNuees0NIbG&index=20>

For Examples refer below mention link

Associative cache:

https://www.youtube.com/watch?v=yH_AHVAaBM4&t=16s

Set Associative cache:

<https://www.youtube.com/watch?v=j5PUJlIPPVE&t=1073s>

Direct mapped cache:

<https://www.youtube.com/watch?v=Trd5XxEk2jU&t=6s>