{addu, subu, ori, lw, sw, beq, lui, nop}。

Addu: 000000 rs rt rd 00000 100001

Subu: 000000 rs rt rd 00000 100011

Ori: 001101 rs rt immediate

Lw: 100011 base rt offset

Sw: 101011 base rt offset

Beq: 000100 rs rt offset

Lui: 001111 00000 rt immediate

Nop: 000000 0\*26

addu，subu 可以不支持溢出。

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
|  | [31:26] | [25:21] | [20:16] | [15:11] | [10:6] | [5:0] |
| Addu | 000000 | rs | rt | rd | 00000 | 100001 |
| Subu | 000000 | rs | rt | rd | 00000 | 100011 |
| Ori | 001101 | rs | rt | imm | imm | imm |
| Lw | 100011 | base | rt | off | off | off |
| Sw | 101011 | base | rt | off | off | off |
| Beq | 000100 | rs | rt | off | off | off |
| Lui | 001111 | 00000 | rt | imm | imm | imm |
| Nop | 000000 | 00000 | 00000 | 00000 | 00000 | 000000 |

R型指令（addu subu）：前六位均为0，后五位编码

I型指令（ori lw sw beq lui）：前六位编码

Nop 全零，可用特判

Splitter分位[31:26][25:21][20:16][15:11][10:6][5:0]