

# **Embedded Real-time Stixel Computation**



D. Hernandez-Juarez<sup>1</sup>, A. Espinosa<sup>1</sup>, J. C. Moure<sup>1</sup>, D. Vazquez<sup>2</sup>, A. M. Lopez<sup>2</sup>
Universitat Autònoma de Barcelona

<sup>1</sup>Computer Architecture & Operating Systems Dpt. — <sup>2</sup>Computer Vision Center





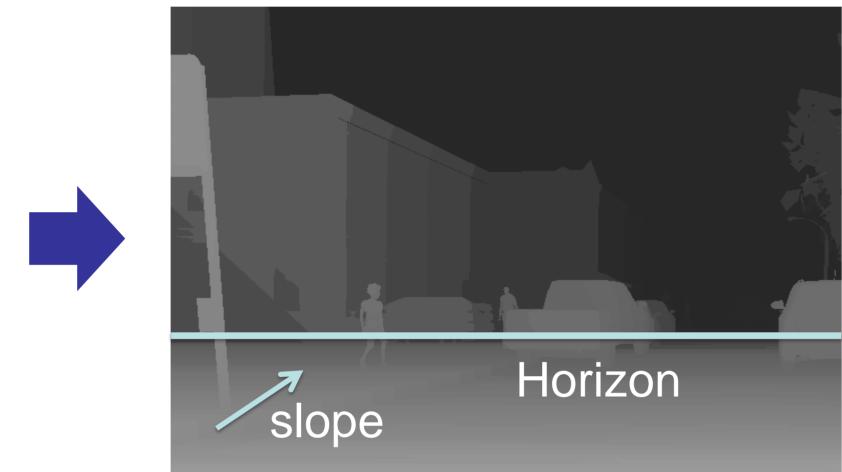
### Abstract

The Stixel World is a medium-level, compact representation of road scenes that abstracts millions of disparity pixels into hundreds or thousands of stixels. The goal of this work is to implement and evaluate a complete multi-stixel estimation pipeline on an embedded, energy efficient, GPU-accelerated device. This work presents a full GPU-accelerated implementation of stixel estimation that produces reliable results at 26 frames per second (real-time) on the Tegra X1 for disparity images of 1024 × 440 pixels and stixel widths of 5 pixels, and achieves more than 400 frames per second on a high-end Titan X GPU card.

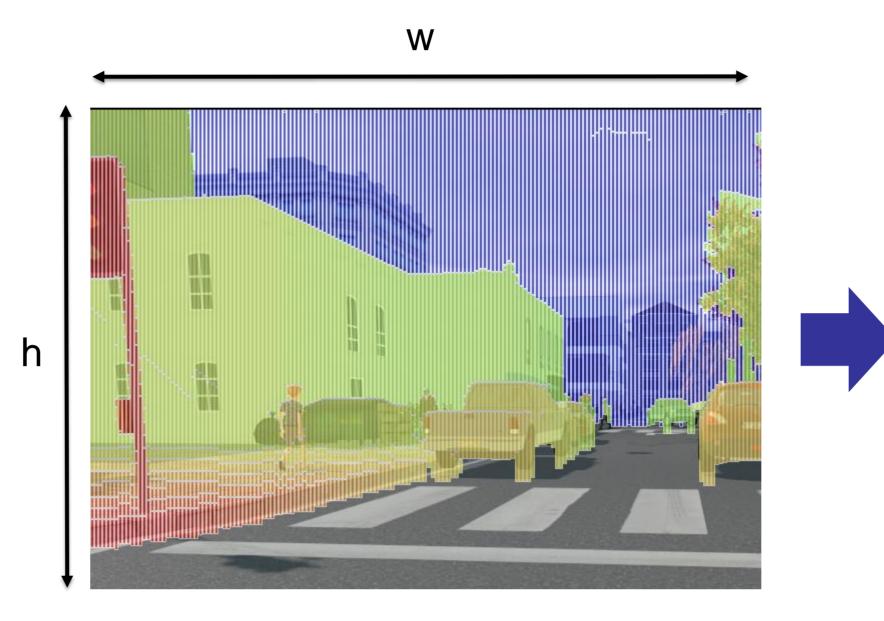
## Problem: Compact depth representation



Stereo Images



Stereo + Horizon Line + Road Slope



Stixels

# Stixels Overview

Sky

Object

**Object** 

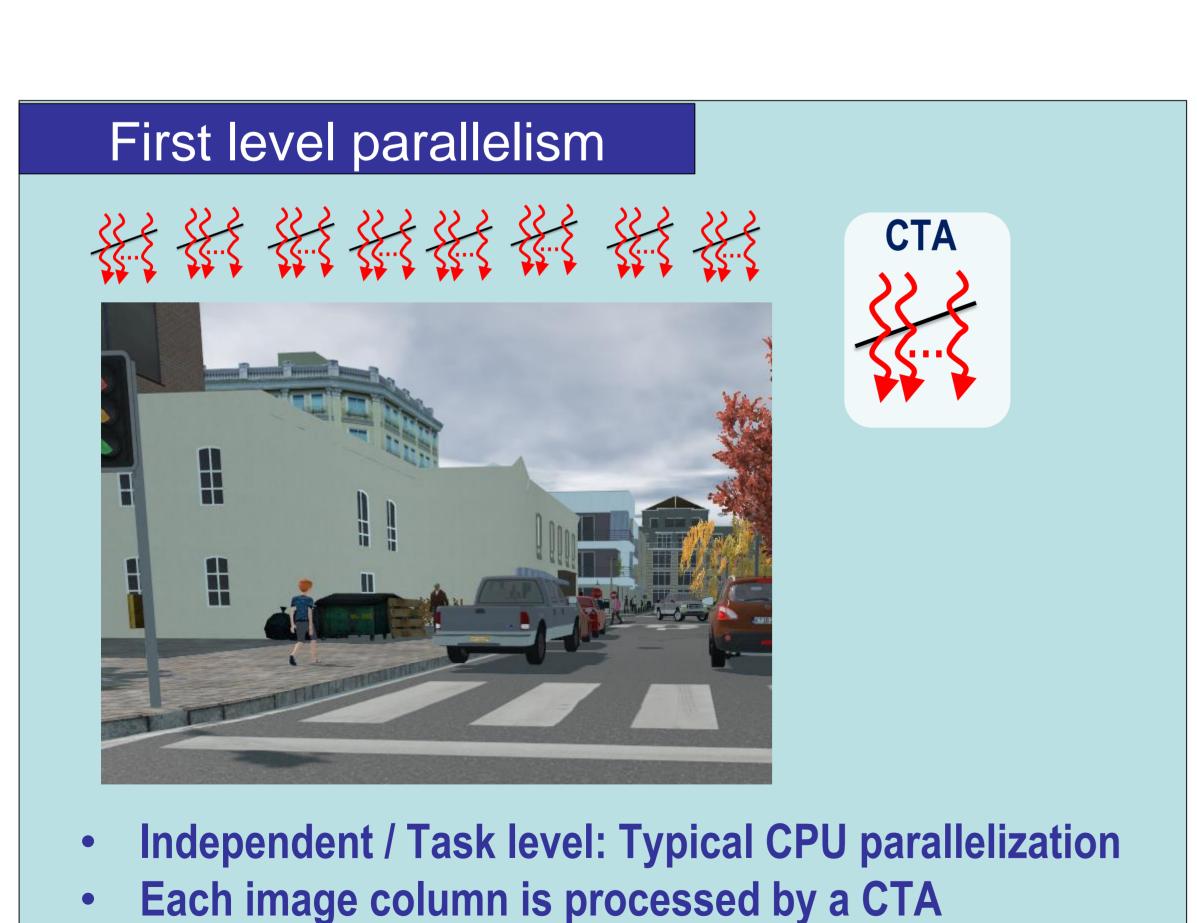
**Object** 

Ground

- Goal: Compact depth representation
  - Extensions: tracking, grouping, semantics
  - Dynamic Programming algorithm
  - High computational complexity O(w × h²)

Tegra X1

### **Parallelization**



# Second level parallelism C<sub>i</sub> Step 1 Step 2 Step 3 ..... Step h Extra parallelism level needed for efficient GPU use Sequentially perform h (image height) steps CTA threads collaborate sharing info each step Decreasing Parallelism: Each step uses one thread fewer

### Speed Up FPS / Watt CPU: Intel Core i7 980X **FPS NVIDIA Titan X** GPU: **CPU Muti-thread** 13.3 0.10 **Drive PX:** NVIDIA Tegra X1 **GPU Optimized** 413 31 1.65 **NVIDIA** Drive PX<sup>1</sup> 26 1.95 2.6 Image Size: 1024x440 <sup>1</sup> single-socket 1000,0 ■ 1280 x 240 ■ 1280 x 240 ■ 640 x 480 ■ 640 x 480 ■ 1280 x 480 ■ 1280 x 480

### **Conclusions:**

Tegra X1

Results

- Real-time performance for energy efficient GPU NVIDIA Tegra X1.

Titan X

- NVIDIA Tegra X1 has better energetic efficiency than high-end GPUs.

### References:

[1] D. Pfeiffer and U. Franke. Towards a global optimal multilayer stixel representation of dense 3D data. In British Machine Vision Conference, BMVC 2011, Dundee, UK, August 29 - September 2, 2011. Proceedings, pages 1–12, 2011. Acknowledgements:

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