

# DHIRAJ BORADE

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## OBJECTIVE:

Actively seeking Fall Co-op / Internship to develop myself into an excellent Computer Science professional through continuous learning and research in both technical and managerial aspects of business.

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## EDUCATION:

**Master of Science: Computer Science, GPA - 3.45**  
**University of Florida** - Gainesville, Florida, USA

Expected Graduation - May 2018

- Coursework in Distributed Operating Systems Principles, Computer Architecture Principles, Concurrent Programming, Analysis of Algorithms, Database Management Systems and Advance Data Structures.

**Bachelor of Engineering: Electronics Engineering, GPA - 3.33**  
**University of Mumbai** - Mumbai, Maharashtra, India

Graduation - May 2013

**Licentiate Diploma in Engineering: Electronics Engineering, GPA - 3.85**  
**Veermata Jijabai Technological Institute** - Mumbai, Maharashtra, India

Graduation - May 2010

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## EXPERIENCE:

**Data Science and Full Stack Development Intern**  
**Claimfound Inc.** - Gainesville, Florida, USA

May 2017 to Present

- Improved and maintained Full Stack of a Web Application i.e. Front End (HTML, CSS and AngularJS) and Back End (RESTful APIs using Python and Flask and PostgreSQL) in an Agile Software Development Environment.
- Developed learning algorithms in Python to improve web application performance, including matching individuals to claims and also design and operate data pathways that collect, clean, and connect data from internal and external sources.
- Improved the database query runtime for matching claims with user by 94% (140x faster), using database indexing and by improving the existing claim matching algorithm.
- Achieved concurrency in all web application tasks using Celery and RabbitMQ for better performance.
- Deployed production application's programmatic code to cloud servers via Amazon Web Services.
- Produced a Continuous Integration Continuous Development Framework (CICD).
- Technologies: PyTest, Celery, RabbitMQ, NumPy, Pandas, Docker, Git, Shell Scripting, PyCharm, Eclipse, WebStorm.

**Senior Design Engineer**  
**Larsen and Toubro Limited** - Mysore, Karnataka, India

July 2013 to June 2016

- Firmware/Software (Embedded C, C++ and JAVA) and Hardware (Mixed Signal Processing) for Single Phase Energy Meter on various platforms viz., Texas Instruments (TI), Renesas, Analog Devices (AD).
- Designed and Developed Low Cost Energy Meter from Proof of Concept to manufacturing in record time of 6 months to overcome market challenges of cost reduction and achieved sales of 10 Million pieces annually boosting the sales by 20%.
- Improved the energy measurement accuracy of the energy meter by 75% (1.5x better) by developing optimized algorithms.
- Developed Native Android and iOS application as an interface to generate Energy Bill by acquiring data from Energy Meter directly to Mobile Phone via Bluetooth and pushed onto the Cloud via AWS, to be collected by the Electricity Utility.
- Project Life-Cycle Management of Single Phase Energy Meter.
- Technologies: IAR Systems for TI and AD, Cubesuite+ for Renesas, CAD/CAM, PSpice simulator, Android Studio, Swift.

**Research and Development Intern**  
**APLAB Limited** - Mumbai, Maharashtra, India

November 2008 to November 2009

- Firmware (Embedded C) and Software (C++ and JAVA) for Single and Three Phase Online Uninterruptible Power Supply (UPS) Systems, Frequency Converters, Static Transfer Switch, Inverters and Isolation Transfer.

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## SKILLS SUMMARY:

- Languages: Embedded C, C++, C#, Java, JavaScript, Python, Swift, PHP, Bootstrap, Materialize CSS, Kotlin, Ruby, R, Matlab.
- Technologies: Visual Studio, IntelliJ IDEA, Eclipse, PyCharm, WebStorm, NoSQL, Docker, Git, MySQL, iPython.
- Frameworks and Libraries: Flask, Django, Spring, Hibernate, NumPy, Pandas, Matplotlib.

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## ACADEMIC PROJECTS:

- Data Intrinsic application (Database Management Systems) built upon a huge database which houses soccer data spanning over 10 years and having 100,000 records (HTML, CSS, AngularJS, Bootstrap, JAVA, RESTful APIs) - April 2017
- Implemented Huffman Tree using Priority Queue Structures for Huffman Encoder and Decoder (JAVA) - April 2017
- Implemented MIPS32 Simulator using Tomasulo Algorithm with Branch Prediction (JAVA) - December 2016
- Wireless Home Automation using Internet of Things (Embedded C, HTML, CSS, JQuery, EDGE, RESTful APIs) - December 2016
- Autonomous Security Robot to detect intruders and attack them using stun gun and capture the video and wireless transfer of video data. (Embedded C, Ladder Programming for PLC) - May 2013
- Implemented 8255A PPI (Programmable Peripheral Interface) on FPGA (VHDL, Advance VLSI Design) - May 2012

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## ACHIEVEMENTS:

- GROUND BREAKER INNOVATION AWARD during the Unit Level Awards 2014-15 of Larsen & Toubro Limited, India
- 2nd Prize in National Level Second PG Symposium for Computer Engineering (CPGCON) 2013 for paper presentation
- 1st Prize in National Level Debate Competition organized by SV Patel Institute of Technology, India (February 2009)