



Society for Computer Technology & Research's

Pune Institute of Computer Technology, Pune

Department of Computer Engineering

and

Board of Studies in Computer Engineering, University of Pune

in association with

ACM Pune Professional Chapter

organized

“C-PGCCON - 2013”

Second post graduate symposium for Computer Engineering

This is to certify that Mr. / M/s. Borade Dhiraj V.
of Fr. C RCE, Mumbai has attended c-PGCCON - 2013
and secured 2nd prize for presenting paper on 8255A PPI on FPGA using VHDL
on 29th - 30th March 2013 at Pune Institute of Computer Technology, Pune.

Mr. Yogesh Dandwate
Chairman, ACM

Prof. G. P. Potdar
Convener & HOD, Computer Engg.

Dr. Sarang Joshi
Professor (Computer Engineering)

Dr. S. A. Joshi
BOS Chairman, UoP

Dr. P. T. Kulkarni
Principal, PICT