

Using the Digital I/O on a BeagleBone Black

BY [ALL ABOUT CIRCUITS \(/EN/MAKER/PROFILES/E491DB21E4CB468190034A2F2F9901A0/\)](https://www.allaboutcircuits.com/profiles/E491DB21E4CB468190034A2F2F9901A0/)



Courtesy of [All About Circuits \(https://www.allaboutcircuits.com/projects/how-to-use-the-digital-i-o-on-a-beaglebone/\)](https://www.allaboutcircuits.com/projects/how-to-use-the-digital-i-o-on-a-beaglebone/)

A great way to get started with the powerful BeagleBone Black is to learn how to use the general purpose I/O. We'll go over the steps in this project.

Export Only

Linux, everything is a file, and the bash shell gives you an easy way to interface with files in

and a Beaglebone Black runs Linux, so it's not an exception to this rule, that even the GPIO on Beaglebones are files. The initial step when using the GPIO is to *export* your chosen pin as a GPIO. This step is pretty simple, and involves a file write using the 'echo' command:

```
$ echo 67 > /sys/class/gpio/export
```

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Although it looks like this command was pointless because nothing happens to your board, keep going! The Beaglebone is built around TI's Sitara processor. Like many other modern processors, the Sitara family has a slew of pins to interface with the outside world, and most of these pins can conveniently perform multiple functions. These functions range from extremely simple, such as the GPIO function we'll be setting up in this project, to extremely complex, such as a part of a PCIe bus or an SGMII network.

Note that you cannot perform all functions of a pin at once. These pins are *multiplexed* into a port on the processor's internal bus, meaning you must select which function you want a chosen pin to perform. The `echo` command writes the number 67 into the file '/sys/class/gpio/export'. This tells the system we want to use pin 67 on the board as the GPIO, and that the processor should propagate that setting throughout the system. Notice that once you've run this command, the directory /sys/class/gpio/ contains an extra folder marked as:

```
$ ls /sys/class/gpio
```

Copy Code

```
export gpio67 gpiochip0 gpiochip32 gpiochip64 gpiochip96 unexport
```

Shifting Directions

By 'echo'-ing 67 into that file, you instructed the system to export the settings for GPIO_67. The system responded by building the folder 'gpio67'. When examining the contents of this, you should see the following structure:



```
drwxr-xr-x 7 root root 0 Jan 1 00:00 ..
-rw-r--r-- 1 root root 4096 Jan 1 00:42 active_low
-rw-r--r-- 1 root root 4096 Jan 1 00:14 direction
-rw-r--r-- 1 root root 4096 Jan 1 00:43 edge
drwxr-xr-x 2 root root 0 Jan 1 00:42 power
lrwxrwxrwx 1 root root 0 Jan 1 00:41 subsystem -> ../../../../class/gpio
-rw-r--r-- 1 root root 4096 Jan 1 00:14 uevent
-rw-r--r-- 1 root root 4096 Jan 1 00:20 value
```

There are two files in the new folder, 'gpio67', that require attention: the first is the ``direction`` file. When you run the command ``$ cat /sys/class/gpio/gpio67/direction``, this output should be displayed:

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in

If you've dealt with any bare metal embedded processor (i.e. PIC, AVR, HCS08), the register called the data direction register should ring a bell, giving you permission to skip the next paragraph.

For those of you who would like a refresher, the *data direction register* dictates which way (either in or out) data can flow out of a GPIO port. Setting the data direction register up for a certain GPIO pin generally involves three steps:

- Finding the right register
- Finding the right bit within that register
- Writing an 'and' statement in C to set or clear that bit

This is not the case for the Beaglebone! When you exported GPIO_67, the Beaglebone created a handy little file to read the processor's data direction register, giving it back to us in an easily read format. Rather than a complex mess of hexadecimal, we get two simple values: 'in' or 'out'. You may have correctly guessed from the earlier 'cat' command that the default state of this register is 'in' - meaning that it has the ability to read in data on

that pin into the processor, but it is unable to affect the state of GPIO_67. Knowing that

let's now change that so we can see that pin's output in the real world! This can be done

by using the 'echo' command, and using 'cat' to verify it worked:



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```
$ echo out > /sys/class/gpio/gpio67/direction
```

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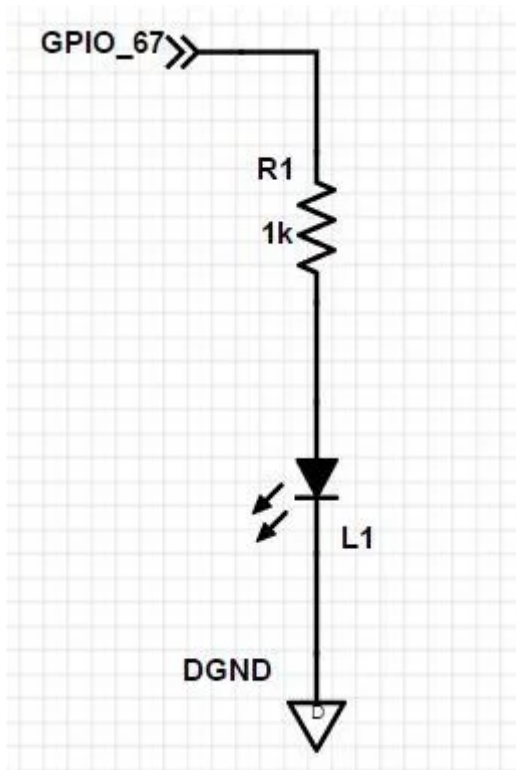
```
$ cat /sys/class/gpio/gpio67/direction
```

```
out
```

Just like that, we've changed this I/O's data direction from an input to an output. Now we can move on and really make it do something!

There Is a Light that Sometimes Goes Out

For this next step, you will need to build a simple circuit using a single 1 kOhm resistor and an LED (pick your favorite color!). One pin of the LED needs to connect to pin 2 of header P8 on the Beaglebone, and the other pin into any row of a solderless breadboard. Then, connect one of the resistor's pins to the same breadboard row that the LED is plugged into, and the other pin into GPIO_67 (found on pin 8 of header P).





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```
if ! -e /sys/class/gpio/gpio67/value
then
    echo 67 > /sys/class/gpio/export
    echo out > /sys/class/gpio/gpio67/direction
fi

while True
do
    echo 1 > /sys/class/gpio/gpio67/value
    usleep 500000
    echo 0 > /sys/class/gpio/gpio67/value
    usleep 500000
done
```

Note that this script will run until you cancel it. In order to cancel the script, press `Ctrl+C` to get back into your Linux terminal. You can copy it from the code excerpt above into your terminal and run it from git using [this repository](#) (https://github.com/shychicken/bittoggle).



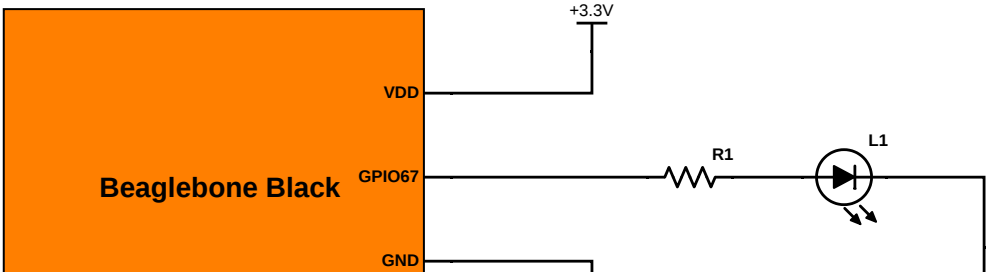
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And there you have it! An easy way to [toggle GPIOs on a Beaglebone Black](#) (<http://www.teachmemicro.com/beaglebone-black-blink-led-using-c/>).

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