

**Nirma University**  
Institute of Technology  
Semester End Examination (IR), December - 2019  
B. Tech. in Computer Engineering, Semester-VII  
IT794 Compiler Construction

Roll /  
Exam No.

Supervisor's Initial  
with Date

Time: 3 Hours

Max Marks: 100

- Instructions:
1. Attempt all the questions.
  2. Figures to right indicate full marks.
  3. Draw neat sketches wherever necessary.
  4. Assume suitable data wherever required.

**Q-1 Do as directed**

- A** Define ambiguous grammar. Show that the following grammar is ambiguous or not. [18]  
CO-1 [6]  
BL-2  $A \rightarrow AA \mid (A) \mid a$

- B** With a given semantic rule: ( $\epsilon$  is NULL) [6]  
CO-1  
BL-2

Production	Semantic Rules
$T \rightarrow F T'$	$T'.inh = F.val$ $T.val = T'.syn$
$T' \rightarrow * F T_1'$	$T_1'.inh = T'.inh * F.val$ $T'.syn = T_1'.syn$
$T' \rightarrow \epsilon$	$T'.syn = T'.inh$
$F \rightarrow digit$	$F.val = digit.lexval$

Draw annotated parse tree for the given expressions. 8\*9

- C** Show whether the following grammar is SLR or not. [6]  
CO-1  
BL-2  $S \rightarrow A+S$   
 $S \rightarrow A$   
 $A \rightarrow a$

**Q-2 Do as directed**

- A** Whether the given grammar is LL(1) or not. Prove your answer with proper parsing table and explanation. ( $\epsilon$  is NULL) [16]  
CO-2 [8]  
BL-3  $S \rightarrow iEtSS' \mid a$   
 $S' \rightarrow eS \mid \epsilon$   
 $E \rightarrow b$

**OR**

**A** Show that the following grammar is CLR(1) but not LALR(1). [8]

CO-2

BL-3

$S \rightarrow Aa/bAc/Bc/bBa$

$A \rightarrow d$

$B \rightarrow d$

Explain with proper state diagram and parsing table.

**B** Show Whether the following grammar is LALR(1) or not. [8]

CO-2

BL-3

$S \rightarrow AA$

$A \rightarrow aA$

$A \rightarrow b$

Explain with proper state diagram and parsing table.

**Q-3 Do as directed** [16]

**A** Define terms Activation Tree and Activation Record in static allocation of space with example. [4]

CO-3

BL-4

**OR**

**A** Explain the following code optimization techniques with example. [4]

CO-3

BL-4

a. Constant folding

b. Strength Reduction

**B** Write down the single static assignment for given below statements. [4]

CO-3

BL-4

$p = a \wedge b$

$q = p * c$

$p = q / d$

$p = e + p$

$q = p - q$

How many variables are required to convert into static single assignment?

**C** Construct the DAG for the given below expression [4]

CO-3

BL-4

$((x + y) - ((x + y) / (x - y))) + ((x + y) * (x - y))$

**D** Translate the arithmetic expression  $a*(b-c)+d/e$  into [4]

CO-3

BL-4

1. A syntax tree.

2. Quadruple.

## Section II

**Q-4 Do as directed** [16]

**A** Differentiate Synthesized attributes and inherent attributes with suitable example. [4]

CO-1

BL-2

**B** Explain the following issues in the design of a code generator? [4]

CO-1

BL-2

1. Register allocation

2. Instruction Selection

**C** Convert the following C code into 3 address code: [4]  
 CO-1 fact(x)  
 BL-2 {

```
    int f = 1;
    for (i = 2; i <= x; i++)
        f = f * i;
    return f;
}
```

**D** Differentiate the Data Flow Analysis and Control Flow Analysis with [4]  
 CO-1 suitable example.  
 BL-2

**OR**

**D** Describe the Code motion and Dead code Elimination optimization [4]  
 CO-1 technique with proper example  
 BL-2

**Q-5** [18]

**A** What do you mean by backpatching?. Show the true and false lists [6]  
 CO-2 for each expression? You may assume the address of the first  
 BL-3 instruction generated from 100.

a==b || (c==d && e==f)

**B** Copy propagation and Global common subexpressions optimization [6]  
 CO-2 techniques are useful for optimizing code, Justify with proper  
 BL-3 example.

**C** What is the relation between S-Attributed definition and L-attributed [6]  
 CO-2 definition? Write down Which of the following definition are S-  
 BL-3 attributed and L-attributed.

1.  $P \rightarrow QR \{ R.val = Q.val \}$
2.  $P \rightarrow QR \{ R.val = Q.val \text{ and } P.val = R.val \}$
3.  $P \rightarrow QR \{ Q.val = R.val \text{ and } P.val = R.val \}$

**Q-6 Do as directed** [16]

**A** Differentiate between type checking and type casting with suitable [8]  
 CO-3 example.  
 BL-4

**OR**

**A** Explain Global Register Allocation, Usage counts and Graph coloring [8]  
 CO-3 interference method to allocate registers.  
 BL-4

**B** True/False with Justification [8]  
 CO-3

1. Left recursive grammar can be LL(1).
2. Reduce/Reduce conflicts and Shift/Reduce Conflicts occur at the same time.
3. The languages that need heap allocation in the runtime environment are those that allow dynamic data structure.
4. Normally type checking is done during code optimization.