



Disassembly Logic Analyzer

main.c startup.s

```

73 for(i = count; i > 0; i--)
74 {
75     Delaytime = 116548;
76     while (Delaytime > 0)
77     {
78         Delaytime--;
79     }
80 }
81 // -UUU- Declare your debug dump arrays and indexes
82 // arrays here uint8_t
83 uint32_t DataBuffer[50]; uint32_t TimeBuffer[50];
84 void DebugInit(void)
85 { uint32_t p;
86   for (p = 0; p < 50; p++)
87   { TimeBuffer[p] = 0xFFFFFFFF;
88     DataBuffer[p] = 0xFF;
89   }

```

00001) & 0x1) >> 0  
00004) >> 2 & 0x4) >> 2

BreakEnable BreakKill BreakList BreakSet BreakAccess

Call Stack + Locals

Name	Location/Value	Type
main	0x00000A04	int f0

Call Stack + Locals

Memory 1

Simulation

t1: 12.63130091 sec

L:78 C:1