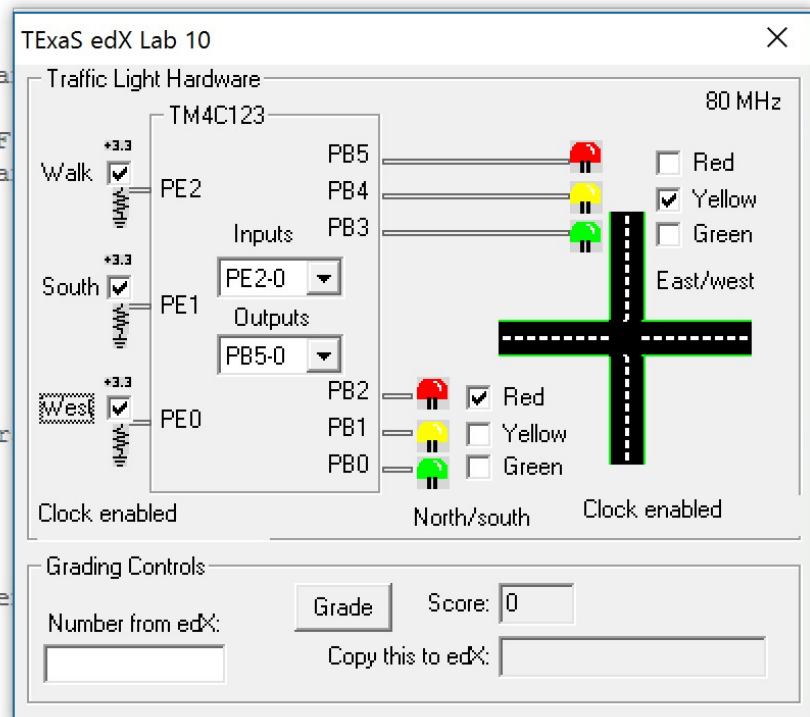


```
1 ;***** main.s *****
2 ; Program written by: put your names here: DHRUV SANDESARA
3 ; Date Created: 8/25/2013
4 ; Last Modified: 2/24/2015
5 ; Section 1-2pm TA: Youngchun Kim
6 ; Lab number: 5
7 ; Brief description of the program
8 ; A traffic light controller with 3 inputs a
9 ; Hardware connections
10 ;The "don't walk" and "walk" lights must be PE
11 ;Obviously, you will not connect both inputs a
12
13 ;Red south PA7 PB5 PE5
14 ;Yellow south PA6 PB4 PE4
15 ;Green south PA5 PB3 PE3
16 ;Red west PA4 PB2 PE2
17 ;Yellow west PA3 PB1 PE1
18 ;Green west PA2 PB0 PE0
19 ;Table 5.1. Possible ports to interface the tr
20
21 ;Walk sensor PA4 PD2 PE2
22 ;South sensor PA3 PD1 PE1
23 ;West sensor PA2 PD0 PE0
24 ;Table 5.2. Possible ports to interface the se
25 SYSCTL_RCGC2_R EQU 0x400FE108
26 SYSCTL_RCGCGPIO_R EQU 0x400FE608
27 GPIO_PORTA_DATA_R EQU 0x400043FC
28 GPIO_PORTA_DIR_R EQU 0x40004400
```

Thread
Privileged
MSP
0
0.00000000



Version with 32768 Byte Code Size Limit
Used: 1348 Bytes (4%)

able BreakEnable BreakKill BreakList BreakSet BreakAccess

Call Stack + Locals

Name Location/Value Type

Call Stack + Locals

Memory 1

Simulation

t1: 41.97593880 sec

L:2 C:59

CAP