



Registers

| Register | Value |
|-----------------|------------|
| Core | |
| R0 | 0x20000060 |
| R1 | 0x20000060 |
| R2 | 0x20000060 |
| R3 | 0x20000060 |
| R4 | 0x00000000 |
| R5 | 0x20000000 |
| R6 | 0x00000000 |
| R7 | 0x00000000 |
| R8 | 0x00000000 |
| R9 | 0x00000000 |
| R10 | 0x00000E28 |
| R11 | 0x00000000 |
| R12 | 0x20000040 |
| R13 (SP) | 0x20000460 |
| R14 (LR) | 0x000002F3 |
| R15 (PC) | 0x0000059C |
| xPSR | 0x21000000 |
| Banked | |
| System | |
| Internal | |
| Mode | Thread |
| Privilege | Privileged |

Disassembly

```
66: int main(void){
0x0000059C F5AD7D74 SUB      sp,SP,#0x3D0
67:          UART_Init();          //initialize UART
0x000005A0 F7FFFF5A RL.W      UART_Init (0x00000458)
```

Exam2asm.s Exam2.c startup.s **main.c** UART.c

```
61      UART_OutUDec(score);
62      UART_OutCRLF();
63  }
64
65  ////////////////////////////////////////////////// MAIN FUNCTION ///////////////////////////////////
66  int main(void){
67      UART_Init();          //initialize UART
68      UART_OutCRLF();
69      UART_OutString("Exam2 Practice Test 1!\n\n\r");
70      UART_OutString = 0x000004F4
71
72      int score = 0;
73      char* lenStrings[lenTests] = {"", "hi!", "@!#$%^&*()",
74                                     "This is a long string of
75
76      int lengths[lenTests] = {0, 3, 10, 11, 13, 62};
77      Sentence para[strTests] = {{"Alice was not here."}, {"
78                                  {"Winter Summer is coming."}
79                                  {"Oatmeal raisin cookies are
80                                  {"Vvickie iya mvakinva mvore
```

UART #1

```
Exam2 Practice Test 1!

Test of strLen
Yes! Score = 5
Yes! Score = 10
Yes! Score = 15
Yes! Score = 20
Yes! Score = 25
Yes! Score = 30

Test of fix
Yes! Score = 35
Yes! Score = 40
Yes! Score = 45
Yes! Score = 50
Yes! Score = 55
Yes! Score = 60
Yes! Score = 65

Test of Check
Yes! Score = 70
Yes! Score = 75
Yes! Score = 80
Yes! Score = 85
Yes! Score = 90
Yes! Score = 95
Yes! Score = 100

Final Score = 100
-----End of Test Cases-----
```

Command

*** Currently used: 3624 Bytes (11%)

```
WS 1, (*edit).remove[a]
WS 1, (*edit).words[i+a]
WS 1, (*edit).words[i]
WS 1, (*edit).words[temp]
WS 1, (*edit).words[temp+length]
```

ASSIGN BreakDisable BreakEnable BreakKill BreakList BreakSet BreakAccess COVERAGE DEFINE DIR Display

Call Stack + Locals

UART #1

Watch 1

Memory 1

Simulation

t1: 11.24150000 sec

L:66 C:1

CAP NUM

SCRL

OVR R/W