

# DHRUV VYAS

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## SUMMARY

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Experienced software engineer with over 5 years of diverse experience spanning software engineering, data science, and applied machine learning. Strong communicator recognized for fostering collaboration among interdisciplinary teams, delivering impactful solutions at the crossroads of technology and data-driven decision-making

## SKILLS

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**Programming:** Python, Java, Swift, SQL, C, C++  
**Tools/Libraries:** TensorFlow, PyTorch, Jupyter, Matplotlib, Pandas, Sci-kit, Numpy, Git, Docker  
**Cloud:** GCE, AWS

## RELEVANT EXPERIENCE

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**Postdoctoral Researcher** Jan 2023 - Present  
University of Iowa *Iowa City, IA*

- Leveraged cutting-edge technologies, such as smart glasses and watches, to collect 6000+ data points to drive significant advancements in the field of hearing healthcare
- Formulated a multi-modal machine learning model to improve diagnostics and treatments related to hearing healthcare

**Data and Operations Research Scientist Intern** May 2020 - Aug 2020  
Principal Global Investors *Remote*

- Designed the tools for standardizing ETL pipelines across the data science products
- Led the efforts in modernizing and establishing standardized data science pipelines across the team

**Graduate Research Assistant** Jan 2017 - Dec 2022  
University of Iowa *Iowa City, IA*

- Led a research initiative dedicated to creating algorithms that reduced the time needed for hearing aid personalization by 50%
- Designed and maintained smartphone applications and data collection infrastructure to enhance research pertaining to cochlear implants, accumulating data from over 1000 participants
- Constructed machine learning models for forecasting hearing outcomes using user surveys and hearing aid data

**Sr. Embedded Engineer** Jan 2014 - Jul 2016  
Archana Automation *Gujarat, India*

- Streamlined and expanded single-board computer-based HMI products
- Introduced data logger product line for industrial applications

## EDUCATION

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**Doctor of Philosophy, Computer Science** – University of Iowa 2016 - 2022  
**Master of Engineering, Embedded Systems** – BITS - Pilani 2011 - 2013  
**Bachelor of Engineering, Electronics Engineering** – Dharmsinh Desai University 2007 - 2011

## MISC PROJECTS

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**QEMU Raspberry PI Kernel:** Furnished tools for the compilation of QEMU Linux kernels ([Git repo](#))

**Raspberry Pi Libmodbus:** Modified existing libmodbus library to support half-duplex RS-485 communication ([Git repo](#))