

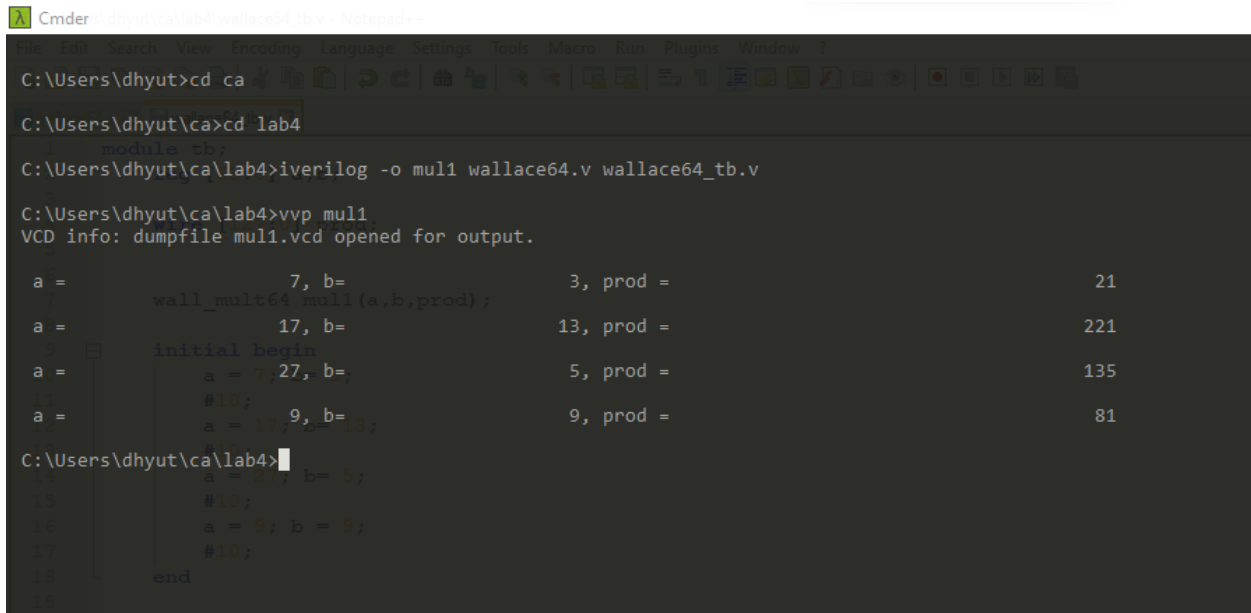
Computer Architecture Practice - COM307P

Lab - 4

64-Bit Wallace Tree Multiplier

Done by : N Sree Dhyuti - CED19I027

Output :



```
C:\Users\dhyut\ca\lab4>cd ca
C:\Users\dhyut\ca\lab4>cd lab4
C:\Users\dhyut\ca\lab4>iverilog -o mul1 wallace64.v wallace64_tb.v
C:\Users\dhyut\ca\lab4>vvp mul1
VCD info: dumpfile mul1.vcd opened for output.

a = 7, b= 3, prod = 21
wall_mult64 mul1(a,b,prod);
a = 17, b= 13, prod = 221
initial begin
a = 7, b= 5, prod = 35
#10;
a = 17, b= 13, prod = 221
#10;
a = 7, b= 5, prod = 35
#10;
a = 17, b= 13, prod = 221
end
```

THE END