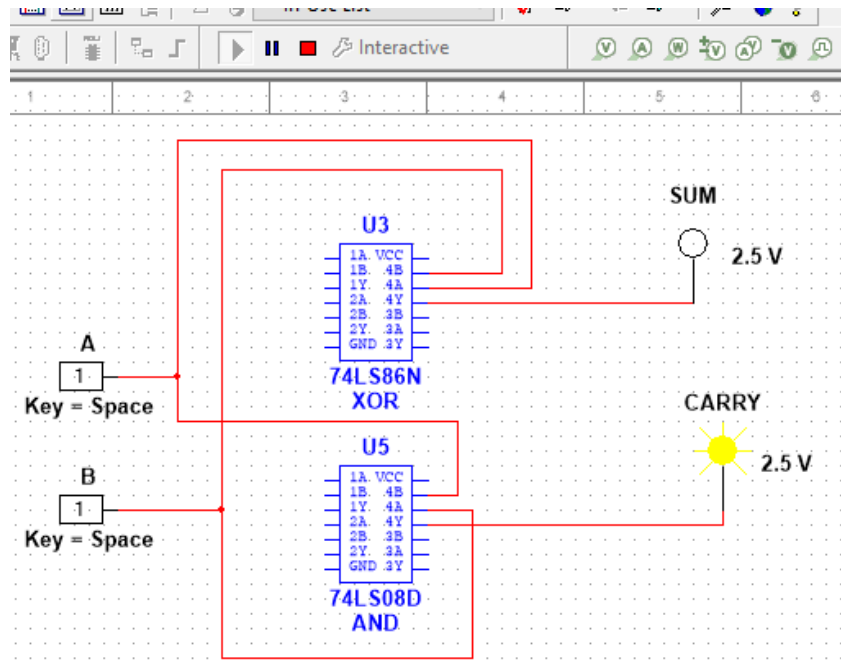
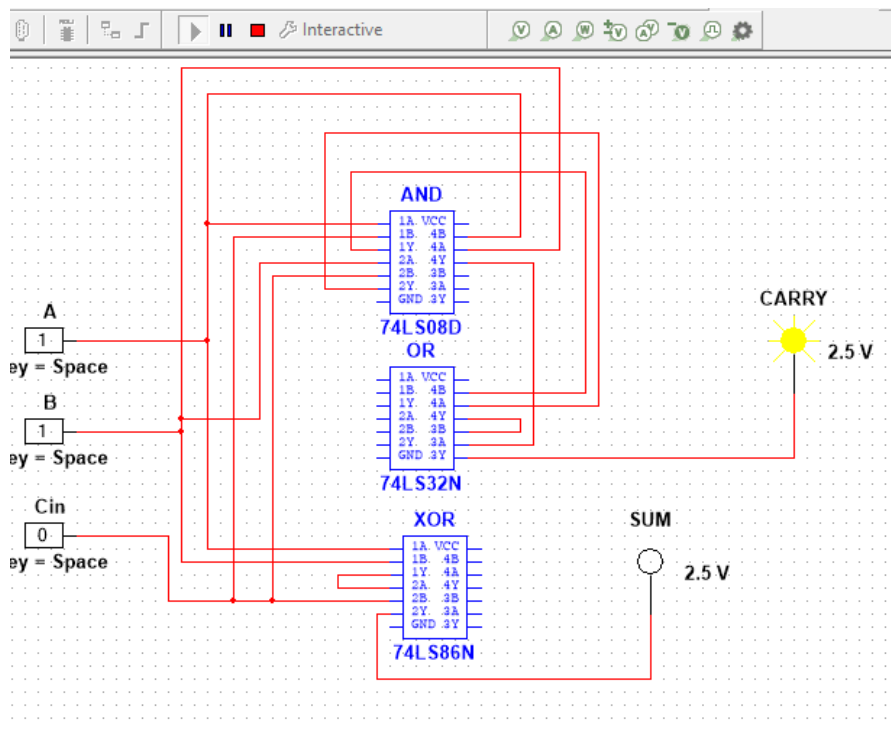


DACD LAB EXPERIMENT 5

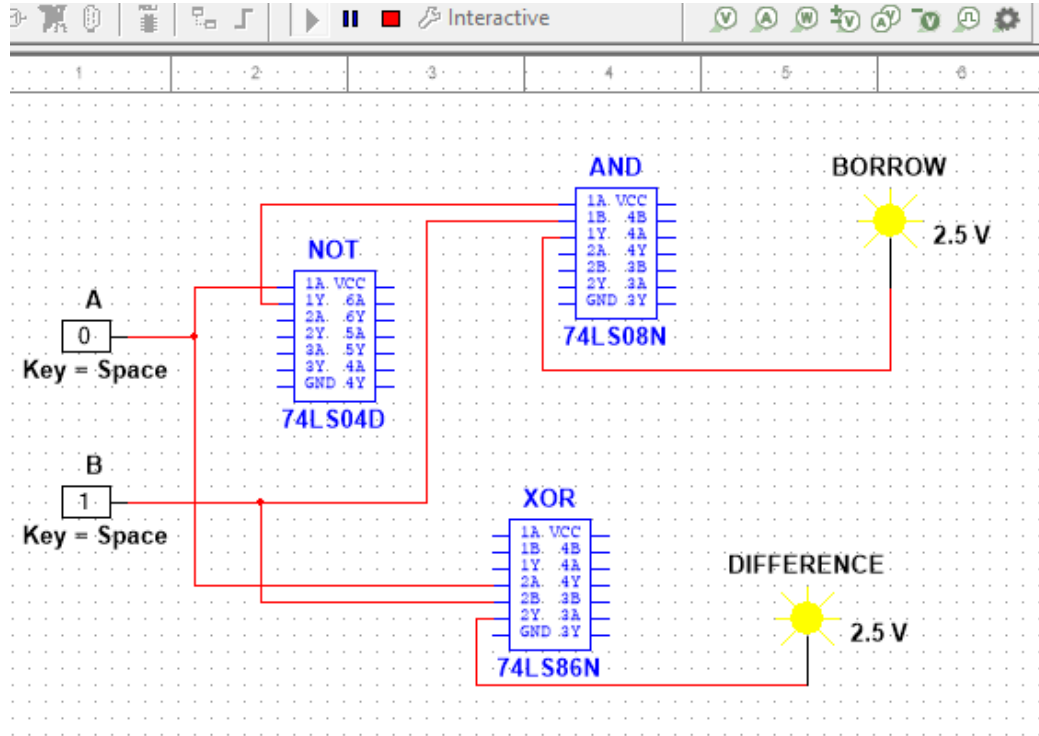
1) HALF ADDER



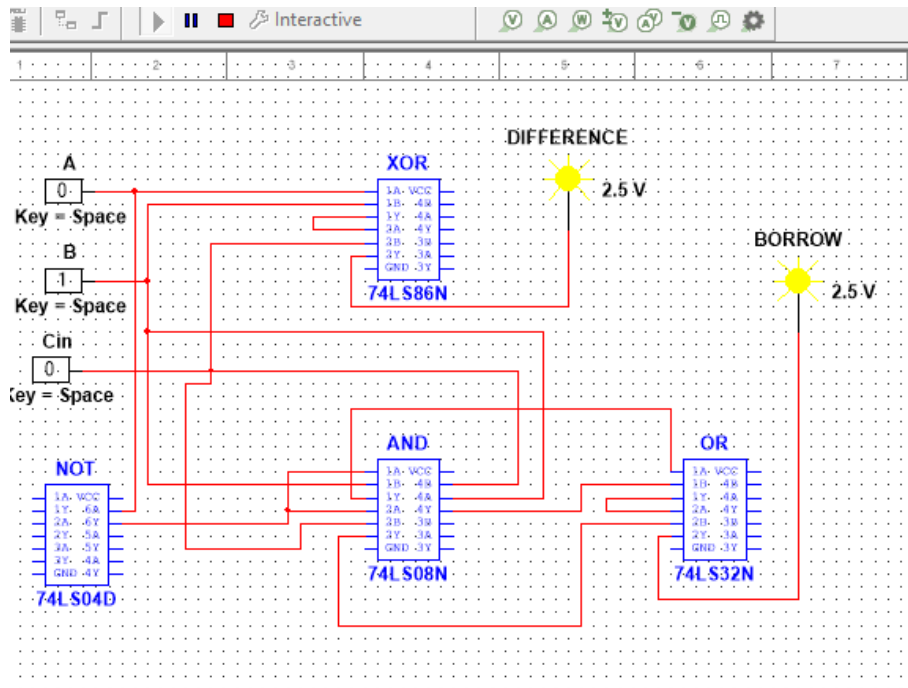
2) FULL ADDER



3) HALF SUBTRACTOR

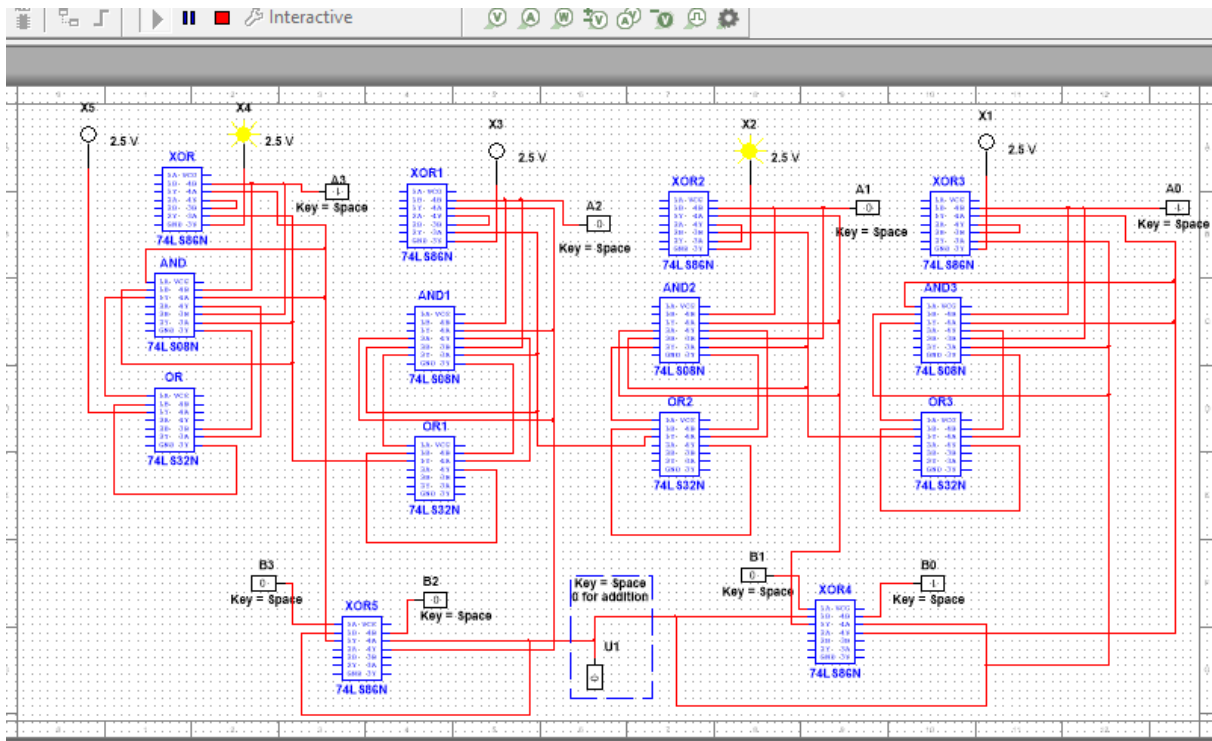


4) FULL SUBTRACTOR



5) 4 BIT ADDER CUM SUBTRACTOR

During addition, everything is as expected X5 is the end carry.



During subtraction, When end carry=1 => the answer is positive and carry bit can be ignored and the 4 bits constitute the answer When end carry=0=> the answer should be converted into 2s complement form and then added with a negative sign to the left. This is because, here, we are using Full Adder. So we need to do subtraction also using adder only. Hence we convert the binary number to be subtracted (B) using Xor IC And thus we again convert it back to normal form. When the answer is positive, no need to convert because, for positive numbers 2s complement is the same as the number itself.

