DACD LAB EXPERIMENT 8
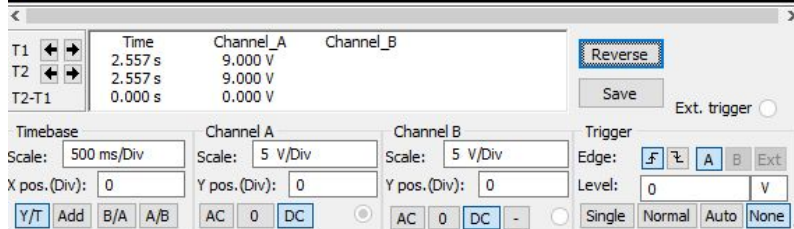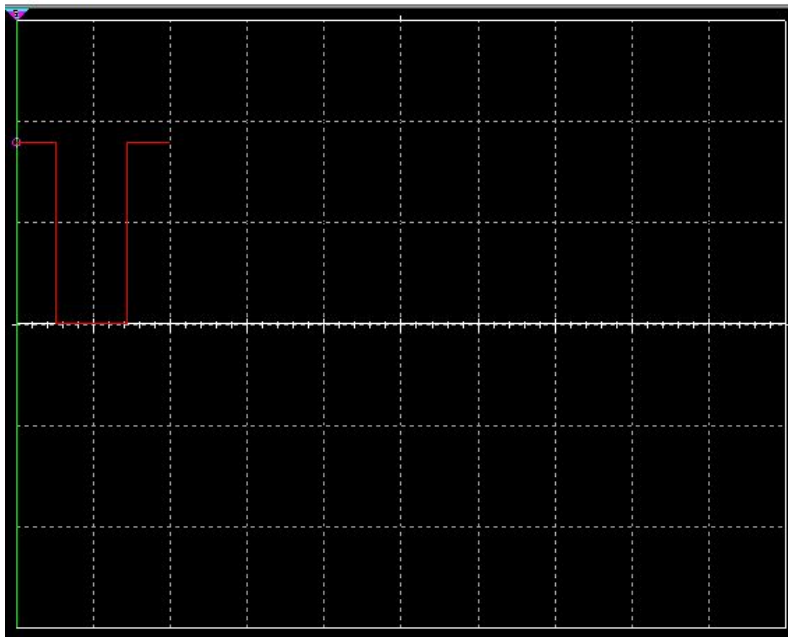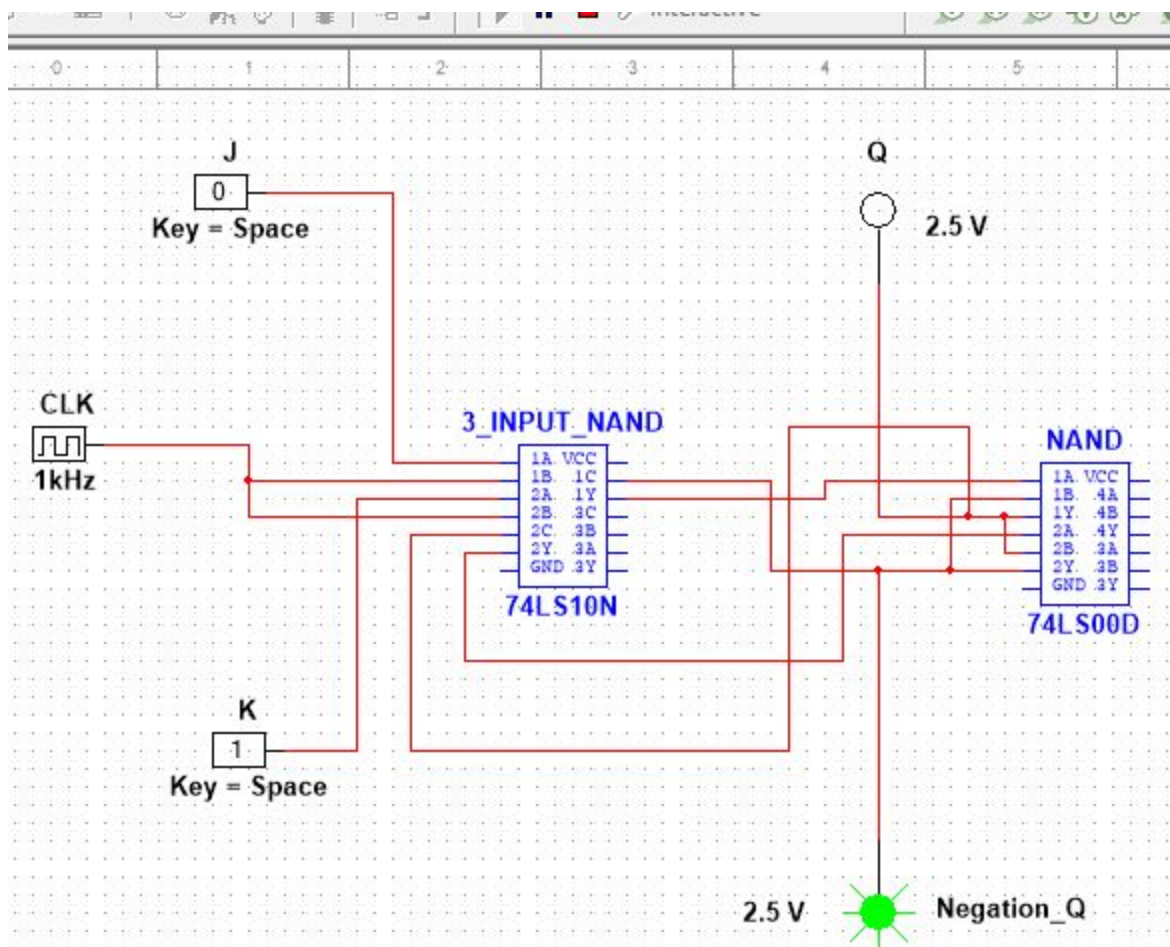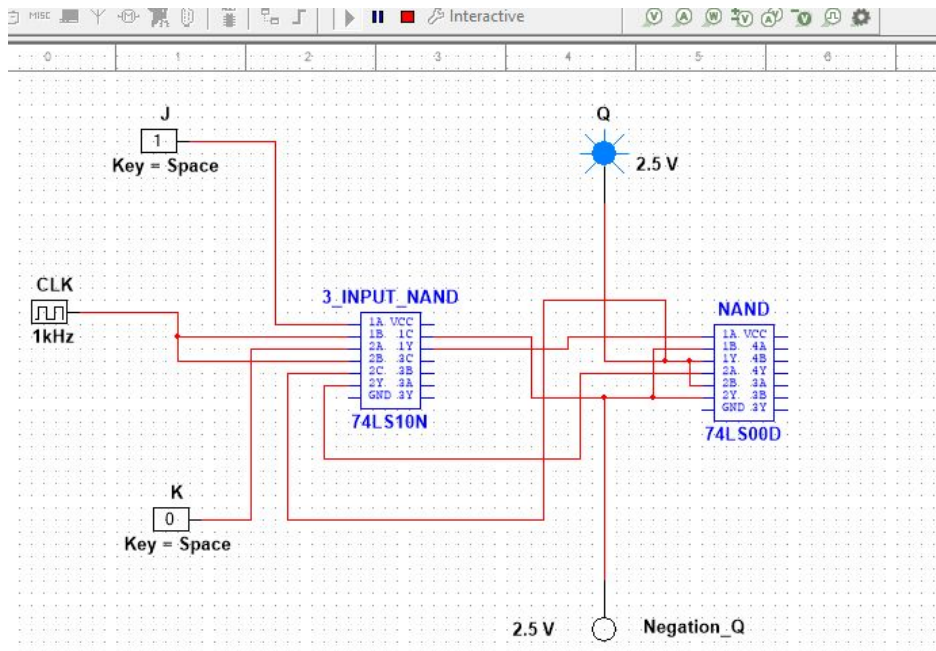
1) Design 1Hz Clock Waveform using 555 Timer IC.
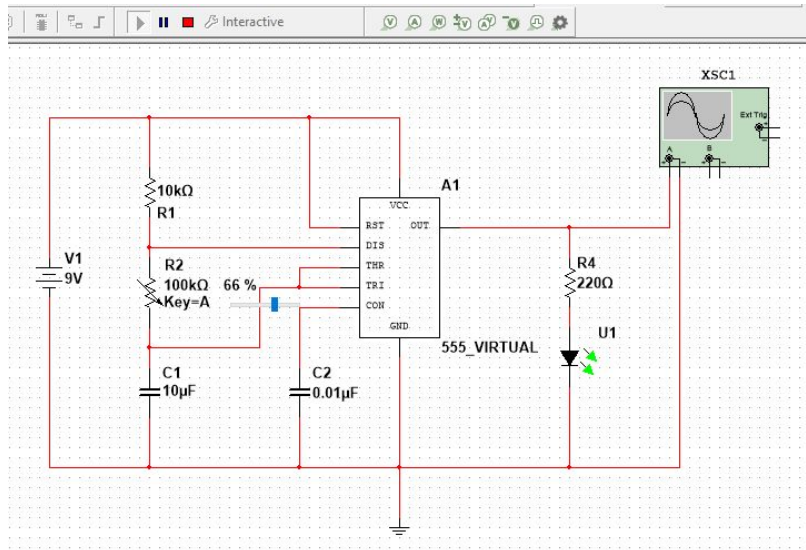
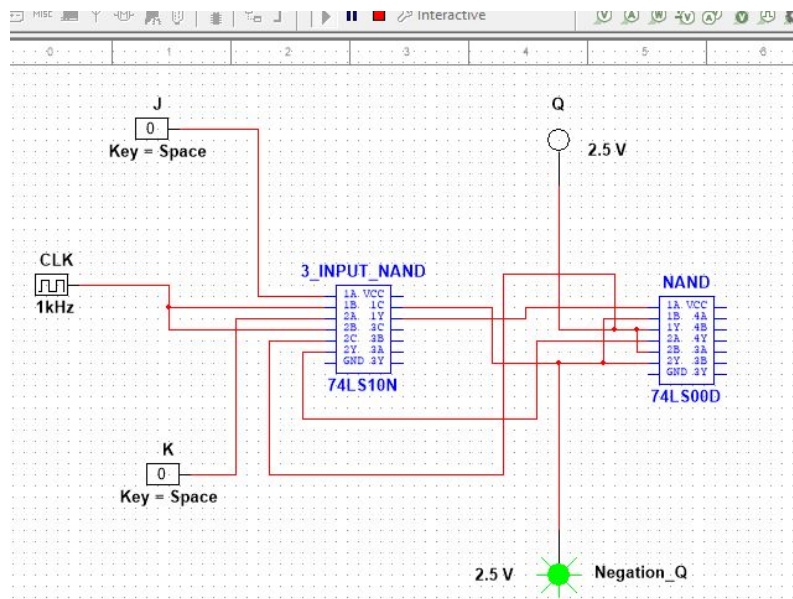2) Design JK FlipFlop using NAND or NOR gates ICs.

Below images show toggling

J
1
Key = Space

Q
2.5 V

CLK
1kHz

3_INPUT_NAND

1A.	VCC
1B.	1C
2A.	1Y
2B.	3C
2C.	3B
2Y.	3A
GND	3Y

74LS10N

NAND

1A.	VCC
1B.	4A
1Y.	4B
2A.	4Y
2B.	3A
2Y.	3B
GND	3Y

74LS00D

K
1
Key = Space

2.5 V　　Negation_Q