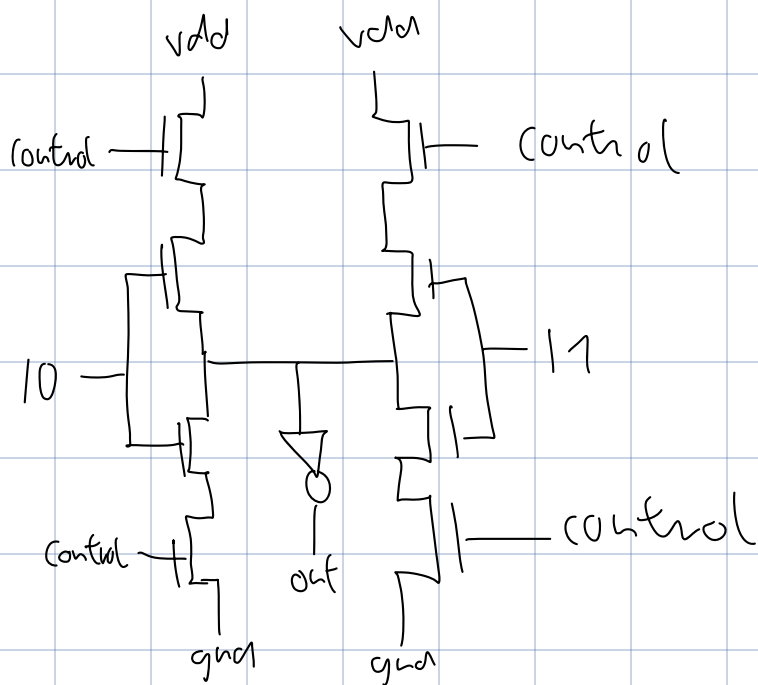
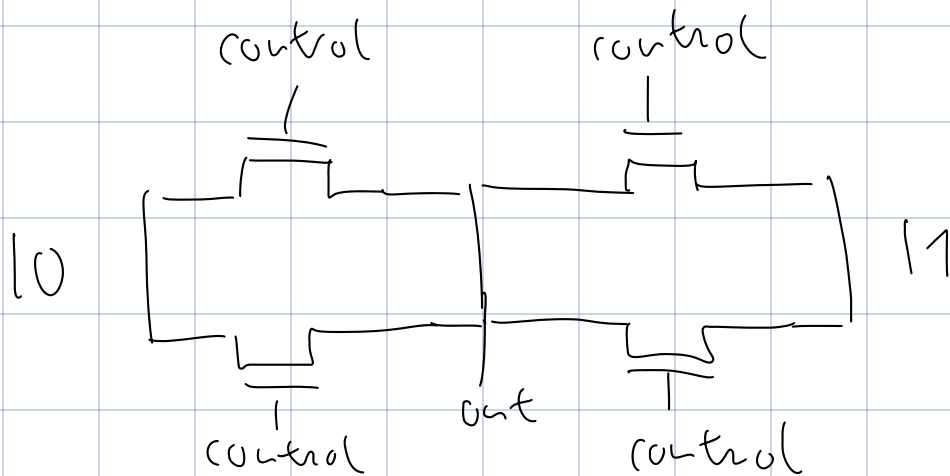


control signals are not worked out
transistors in upper half are PMOS

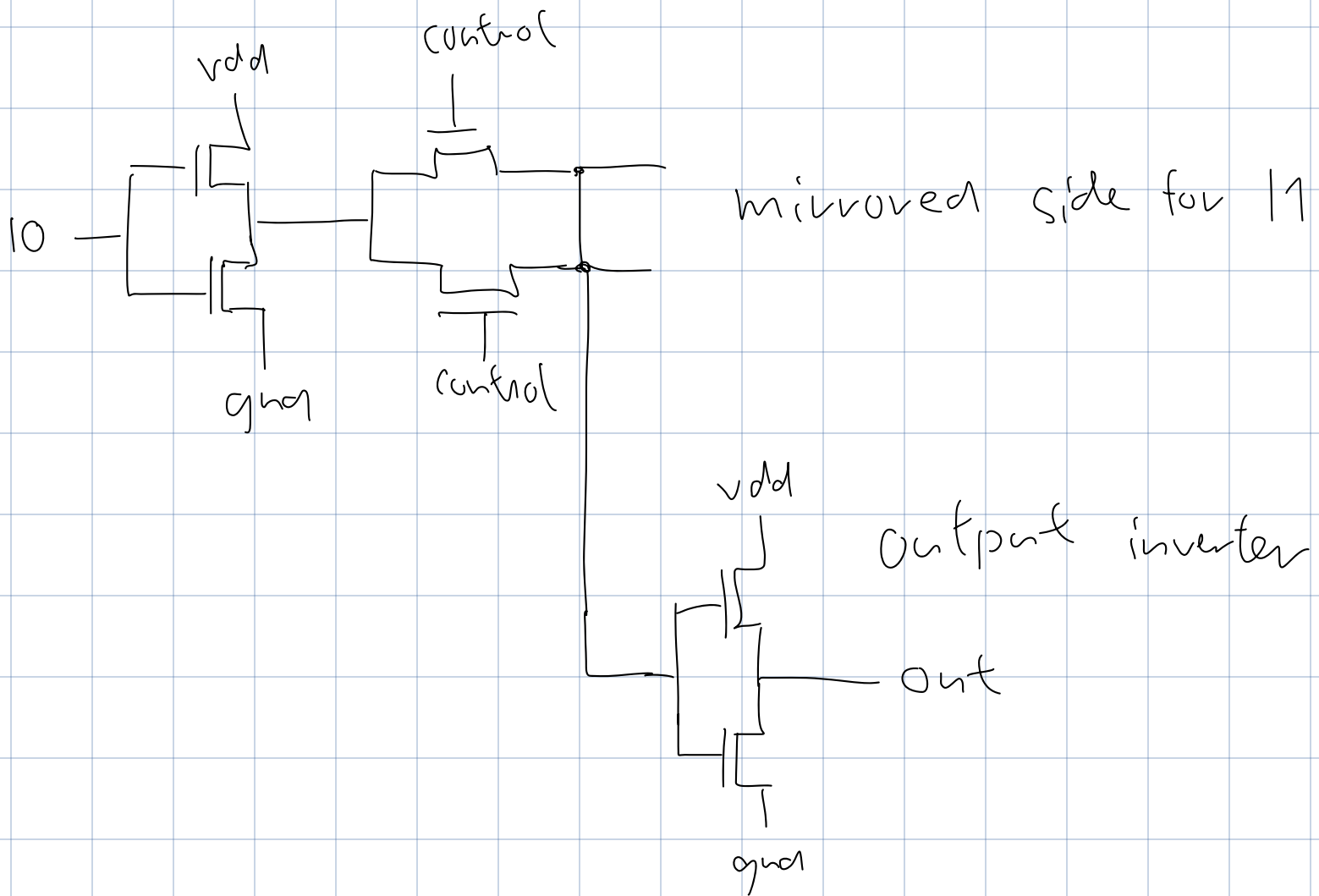
Dual-Gate Mux2



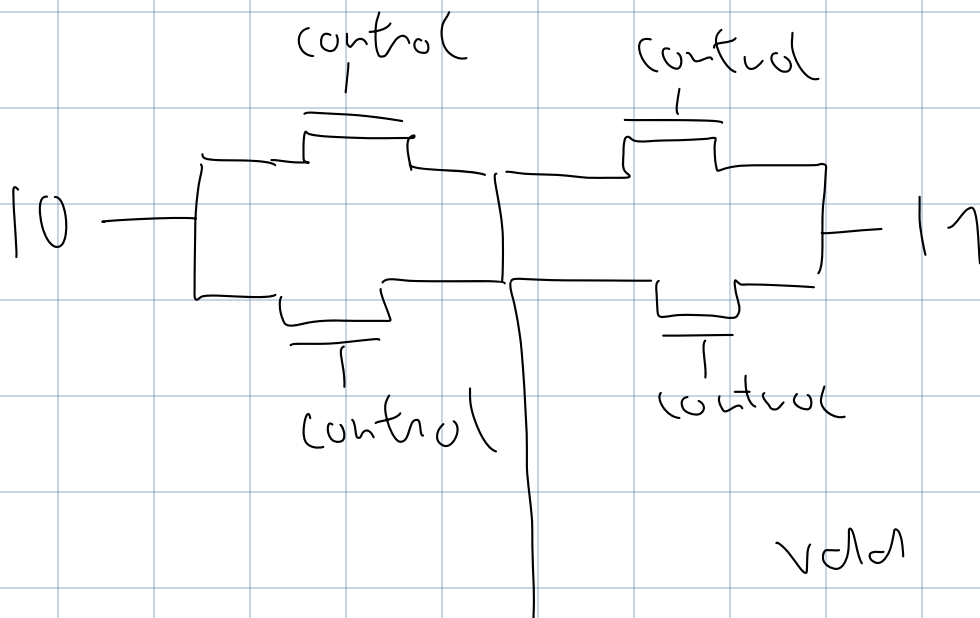
Transmission Mux2

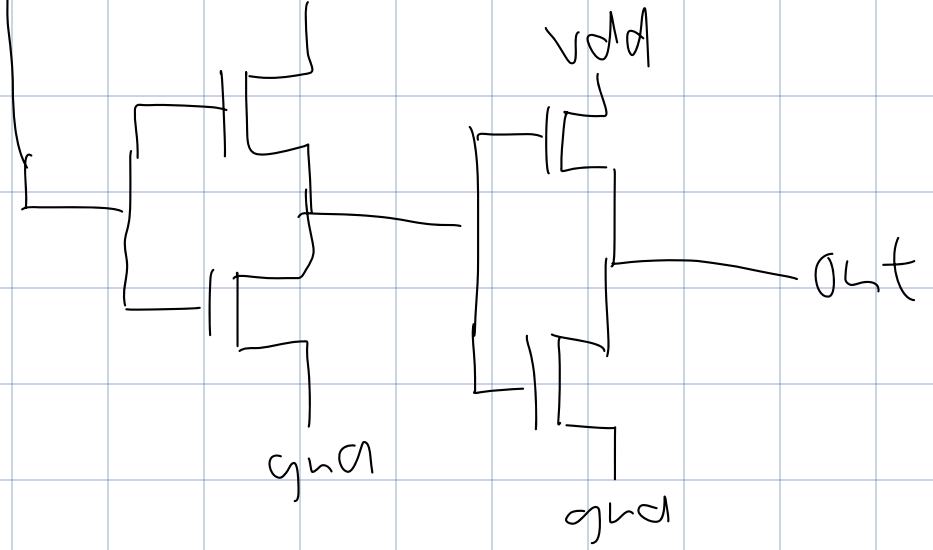


Inverter - Transmission - Inverter



Transmission - Inverter - Inverter





Mux4

— not drawing everything again :)

↳ should be able to extrapolate from MUX 2

Two-Level MUX: design idea

