

Dian-Lun Lin's Resume

Website: <https://dian-lun-lin.github.io>
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EDUCATION

PhD – ECE Department, University of Wisconsin-Madison

MS – EECS Department, National Taiwan University

BS – EE Department, National Cheng Kung University

Wisconsin, US

Taipei, Taiwan

Tainan, Taiwan





RESEARCH Interests:

Parallel and Heterogeneous Computing, Electronic Design Automation (EDA), Machine Learning

Research Achievements

I'm a fifth-year Ph.D. candidate at the Department of Electrical and Computer Engineering at the University of Wisconsin-Madison. During my prior PhD study, I have published four top-tier papers (DAC 2024, DAC 2023, ICPP 2022, and Euro-Par 2021) and one top-tier journal (IEEE TPDS 2022), all as **the first author**. I received **second place** in ACM/PACT Student Research Competition (SRC 2022). I also received the **champion award** in a research competition (IEEE HPEC Challenge 2020). I was a presenter at prominent C++ conferences (CppCon 2023, CppNow 2023, and CppCon 2021). I also gave talks in MediaTek Research, Berkeley National Lab, and NVIDIA Research. My recent work focuses on building a CPU-GPU task programming system using modern C++ Coroutine and CUDA.

Open-Source Projects

Software	GitHub
 Task-based asynchronous programming system using C++ Coroutine	https://github.com/dian-lun-lin/taro - Presented in CppCon 2023
 Taskflow: A General-purpose Parallel and Heterogeneous Task Programming System	https://github.com/taskflow/taskflow - Over than 9K stars in GitHub - Core developer
 RTLflow: From RTL to CUDA - A GPU acceleration flow for RTL simulation with multiple testbenches	https://github.com/dian-lun-lin/verilator_rtlflow - Cooperated with NVIDIA Research - Accepted by ICPP 2022 - Second place at PACT Student Research Competition 2022
 SNIG: Accelerated Large Sparse Neural Network Inference using Task Graph Parallelism	https://github.com/dian-lun-lin/SNIG - Champion of 2020 IEEE HPEC Neural Network Challenge - Implemented in CUDA, CUDA Graph, and Taskflow

Selected Awards

- **Second place** in ACM/PACT Student Research Competition (SRC), 2022
- **Champion** of the IEEE/MIT/Amazon HPEC Large Sparse Neural Network Challenge, 2020
- ACM ISPD Wafer-Scale Physics Modeling Contest – Honorable Mention, 2021
- ACM/IEEE DAC Young Student **Fellowship**, 2023
- ACM/IEEE DAC Young Student **Fellowship**, 2021

- ACM/IEEE DAC Young Student **Fellowship**, 2020
- **Best Master Thesis Nomination**, Department of EE, NTU, 2019
- **Presidential Award**, Department of EE, NCKU, Fall 2015

Work Experience

- Research Intern at NVIDIA (full time) *NVIDIA, US; May. 2022 – Aug. 2022*
- Research Intern at NVIDIA (part time) *NVIDIA, US; Aug. 2021 – Nov. 2021*
- Research Intern at NVIDIA (full time) *NVIDIA, US; May. 2021 – Aug. 2021*
- Graduate Teaching Assistant for “Algorithms” *National Taiwan University, Taiwan; Sep. 2018 – Jan. 2019*
National Taiwan University, Taiwan; Sep. 2017 – Jan. 2018
- *Research Assistant at NTU AI center* *National Taiwan University, Taiwan; Sep. 2018 – Dec. 2018*
- Web Backend Engineer at Edent *Kaohsiung, Taiwan; Jan. 2016 – July. 2017*

Selected Papers

- **Dian-Lun Lin** (co-first author), Boyang Zhang, Che Chang, Cheng-Hsiang Chiu, Bojue Wang, Wan Luan Lee, Chih-Chun Chang, Donghao Fang, and Tsung-Wei Huang, “G-PASTA: GPU Accelerated Partitioning Algorithm for Static Timing Analysis,” ACM/IEEE Design Automation Conference (**DAC**), 2024
- **Dian-Lun Lin**, Yanqing Zhang, Haoxing Ren, Shih-Hsin Wang, Brucek Khailany, and Tsung-Wei Huang, “GenFuzz: GPU-accelerated Hardware Fuzzing using Genetic Algorithm with Multiple Inputs”, *ACM/IEEE Design Automation Conference (DAC)*, 2023
- **Dian-Lun Lin**, Haoxing Ren, Yanqing Zhang, Brucek Khailany and Tsung-Wei Huang, “From RTL to CUDA: A GPU Acceleration Flow for RTL Simulation with Multiple Testbenches,” *ACM International Conference on Parallel Processing (ICPP)*, 2022
- **Dian-Lun Lin** and Tsung-Wei Huang, "Accelerating Large Sparse Neural Network Inference using GPU Task Graph Parallelism," *IEEE Transactions on Parallel and Distributed Systems (TPDS)*, 2022
- Tsung-Wei Huang, **Dian-Lun Lin**, Chun-Xun Lin, and Yibo Lin, "Taskflow: A Lightweight Parallel and Heterogeneous Task Graph Computing System", *IEEE Transactions on Parallel and Distributed Systems (TPDS)*, 2022
- **Dian-Lun Lin** and Tsung-Wei Huang, “Enabling Efficient GPU Computation using Task Graph Parallelism,” *European Conference on Parallel and Distributed Computing (Euro-Par)*, 2021
- **Dian-Lun Lin** and Tsung-Wei Huang, “A Novel Inference Algorithm for Large Sparse Neural Network using Task Graph Parallelism”, *IEEE High-performance and Extreme Computing Conference (HPEC)*, 2020 (**champion award**)

Talks

- “A Task Graph-based Programming System for CPU-GPU Heterogeneous Computing”
 - o NERSC - GPUs for Science Day *California, US; 2023*
- “Taro: Task graph-based Asynchronous Programming Using C++ Coroutines”
 - o CppCon (<https://www.youtube.com/watch?v=UCeJPLSCaol>) *Colorado, US; 2023*
- “An Introduction to C++ Coroutines Through a Thread Scheduling Demonstration”
 - o CppNow (<https://youtu.be/kIPzED3VD3w>) *Colorado, US; 2023*
 - o Berkeley National Lab *Remote, US; 2023*
- “cudaFlow: A Modern C++ Programming Model for GPU Task Graph Parallelism”
 - o CppCon (<https://youtu.be/-tIQblhTAv8?t=2344>) *Colorado, US; 2021*
- “Accelerating Hardware Design Verification: Exploring Simultaneous Execution of Multiple Stimuli with RTLflow and GenFuzz”
 - o MediaTek Research *Remote, US; 2023*

- “G-Fuzz: GPU-accelerated hardware fuzzing”
 - NVIDIA Research *Remote, US; 2022*
- “RTLflow: A GPU acceleration flow for parallel RTL simulation”
 - NVIDIA Research *Remote, US; 2021*
 - ICPP <https://youtu.be/00K8S3tNUSg> *Remote, US; 2022*

Activities

- Program Committee in CppNow, 2024
- Program Committee in CppCon, 2023
- Program Committee in CppNow, 2023
- Program Committee in CppCon, 2022
- Invited reviewer of *Concurrency and Computation: Practice and Experience*, 2024
- Invited reviewer of *IEEE Access Journal*, 2023
- Invited reviewer of *The Journal of Supercomputing*, 2023
- Invited C++ Coroutine posts by Rainer Grimm, 2023
 - <https://www.modernescpp.com/index.php/a-concise-introduction-to-coroutines-by-dian-lun-li/>
 - <https://www.modernescpp.com/index.php/coroutines-a-scheduler-for-tasks-by-dian-lun-li/>

Societies

- Utah Dance Contest – **Top 4** *Utah, US; 2021*
- University of Utah Taiwan Student Association Cooking Contest – **3rd place** *Utah, US; 2021*
- Invited dancer for 2019 Double Tenth Day parade – in front of **presidential palace** *Taipei, Taiwan; 2019*
- Invited dancer for 2017 Taiwan Power Company’s year-end banquet *Taichung, Taiwan; 2017*
- Volunteer teacher at Tainan Jingliao Elementary School *Tainan, Taiwan; 2014*
- Cycling around Taiwan *Taiwan; 2012*

SKILLS

C++11/14/17/20, CUDA, Parallel Programming, Vim