

Eight Discipline Report (8D Report)

To :	8D report No. :
From : Chicony Power Technology	RMA claim No.: N/A
CC:	Chicony Power P/N: W065RP76P-AX01
	Customer P/N : 0A001-01056600
Submit date:	Product description : 65W PD
Receive date : 2023/9/28	Defect D/C or Lot No. :
1 10001VC date - 2023/9/20	Delege D/O of Lot No

Subject:客戶告知 X01 版本單體, PPS 模式 current ripple 稍大,需再改善以增加 margin。

FW(韌體), Regulation

D1.) 問題解決成員:Use Team Approach

主持者 (Team Leader): **Edward Ho** 內部成員 (Internal Team Members):

PM: Laura Fang

Sales: Jennie Chiang

RD: Will Wu

外部成員 (External Team Member):

Richtek: Macro Chang · Yiwei Wu · Ben Chi · Patty Wu

D2.)問題說明:Problem Description:

(Note: Use who, what, when, where, why, how, how many to specify the Customer's problem.)

客戶告知 X01 版本單體,PPS 模式 current ripple 稍大,需再改善以增加 margin。

樣機單體: W065RP76P-AX01 / Version X01。



D3.)內部或客戶的暫時解決辦法及實施日期:Implement and Verify Containment Action:

(Note: Internal / external containment action effectiveness and date.)

2023/10/4 提供客戶分析報告與解決方案。

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D4.)不良原因確認: Define and Verify Root Causes:

(Note: Identify and verify all suspect causes, which needs explain why the problem occurred.)

1. 比較 PD mode 與 PPS mode 的 comp pin 波形, 發現 PD mode comp pin 波形比較 smooth。

PD Mode

The Gate Unitary Help

Corners Measure

Earth Fight

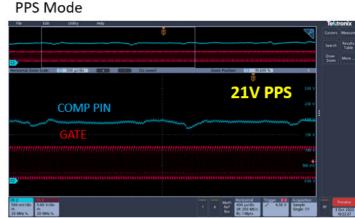
Total Corners Measure

Earth Fight

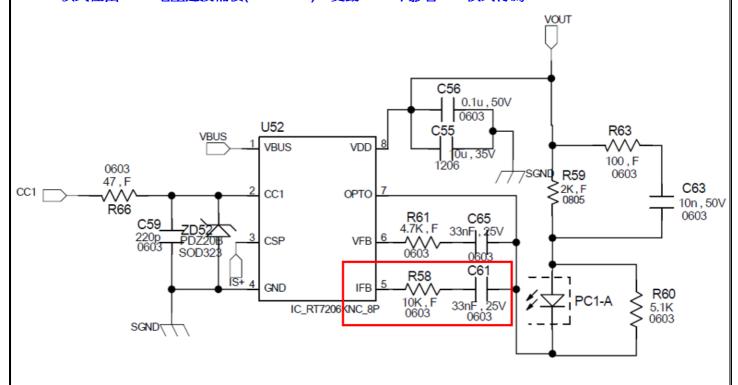
Corner Measure

Earth Fight

Earth



- 2. PPS 模式中,CC 模式由 IFB 電流廻授補償(R58+C61)。
- 3. PD 模式僅由 VFB 電壓迴授補償(R61+C65), 更動 R58 不影響 PD 模式行為。



D5.)改善措施:Corrective Action Verification:

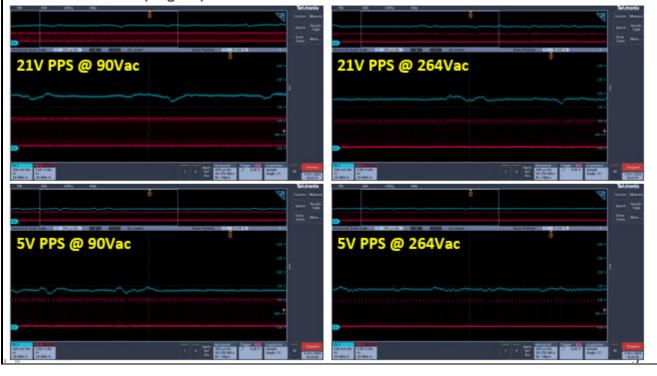
(Note: Be make sure the corrective actions is effective in process as well as able to fix the customer complaint problem)



- 1. 調整 IFB 電阻 R58,10Kohm -> 1Kohm。
- 2. Improved current ripple from 48mA to 36mA •

R58 = 10Kohm	(Original)									
Output Voltage	Input Vac	Output Current (A)		Spec	Current Ripple (mA)		Spec	Waveform		
5V	90V/47Hz	3.25 ~ 3.29		3.25 ~ 3.29		$3.25A \pm 0.1A$	44		50mA	<u>W1</u>
3 v	264V/63Hz	3.25 ~	~ 3.3	5.23A ± 0.1A	48					
21V	90V/47Hz	3.14		3.095A (65W max)		34	50mA	wa		
21 V	264V/63Hz	3.14		3.093A (03 W IIIAX)	48		JUILA	<u>W2</u>		
R58 = 1Kohm										
Output Voltage	Input Vac	Output Current (A)		Spec	Current Ripple (mA)		Spec	Waveform		
5V	90V/47Hz	3.26 ~ 3.29		Hz $3.26 \sim 3.29$ $3.25A \pm 0.1A$		(28	50mA	<u>W3</u>	
3 v	264V/63Hz	3.26 ~	3.29	3.23A± 0.1A	- 1	32	JUILA	<u>w 3</u>		
21V	90V/47Hz	3.1	14	3.095A (65W max)		32	50mA	WA		
21 V	264V/63Hz	3.1	14	3.033A (03 W IIIAX)	l	36 <i>J</i>	JUILA	<u>W4</u>		

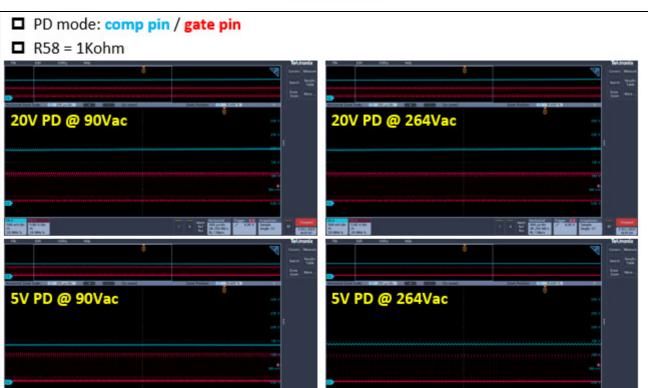
- 3. 實測 comp pin signal、peak load、transient load,驗證 R58 更動不影響 PD 模式行為。
 - PPS mode: comp pin / gate pin
 - R58 = 10Kohm (original)











4. 調整 IFB 電阻後,peak load 無明顯差異。

R58 = 10Kohm (original)

Vaut		100Vac		240	Vac		t
Vout	Vin	SPEC1	SPEC2	SPEC1	SPEC2	SPEC	Test Result
20V	Min	19.20V	19.10V	19.30V	19.10V	18.1V	Pass
200	Max	20.20V	20.20V	20.30V	20.20V		
Vout		100Vac		240	Vac	SPEC	Test Result
	Vin	SPEC1	SPEC2	SPEC1	SPEC2	SPEC	Test Nesult
15\/	Min	14.40V	14.20V	14.40V	14.30V	13.5V	Pass
15V	Max	15.20V	15.20V	15.20V	15.20V		

R58 = 1Kohm

Vout	Vout		100Vac		Vac	SPEC	Test Result
	Vin	SPEC1	SPEC2	SPEC1	SPEC2	SPEC	Test Nesult
20V	Min	19.30V	19.10V	19.30V	19.10V	18.1V	Pass
200	Max	20.30V	20.30V	20.30V	20.30V		
Vout		100Vac		240Vac		SPEC	Test Result
	Vin	SPEC1	SPEC2	SPEC1	SPEC2	SPEC	Test Nesult
15V	Min	14.40V	14.30V	14.40V	14.30V	13.5V	Pass
134	Max	15.30V	15.20V	15.30V	15.30V		

5. 調整 IFB 電阻後,transient load 無明顯差異。



R58 = 10Kohm (original)

Vout		90Vac		264	Vac	CDCC	T . D . b
Vin		condition1	condition2	condition1	condition2	SPEC	Test Result
5V	Max.	5.28V	5.28V	5.28V	5.30V	4.6~5.8V	
30	Min.	4.92V	4.92V	4.92V	4.92V	4.0~ 3.6 V	
9V	Max.	9.17V	9.19V	9.19V	9.19V	8.55V~9.45V	
90	Min.	8.79V	8.79V	8.79V	8.79V	0.33 V ~ 3.43V	
VoutV		90Vac		264Vac		SPEC	PASS
Vout		100Hz	50KHz	100Hz	50KHz	SPEC	PASS
15V	Max.	15.4V	15.3V	15.4V	15.3V	1425 1575/	
150	Min.	14.7V	14.8V	14.6V	14.8V	14.25~15.75V	
20V	Max.	20.5V	20.3V	20.4V	20.3V	19~21V	
200	Min.	19.6V	19.7V	19.6V	19.7V	15~210	

R58 = 1Kohm

Vout		90Vac		264Vac		SPEC	Test Result
Vin		condition1	condition2	condition1	condition2	SPEC	lest Result
5V	Max.	5.28V	5.28V	5.30V	5.30V	4.6~5.8V	
30	Min.	4.90V	4.90V	4.90V	4.90V	4.0~ 3.6 V	
9V	Max.	9.18V	9.18V	9.18V	9.20V	8.55V~9.45V	
90	Min.	8.78V	8.78V	8.78V	8.78V	0.55 V ~ 9.45V	
VoutVo		90\	/ac	264	Vac	CDEC	PASS
Vout		100Hz	50KHz	100Hz	50KHz	SPEC	PASS
15V	Max.	15.4V	15.3V	15.4V	15.3V	14.25~15.75V	
120	Min.	14.6V	14.8V	14.6V	14.8V	14.25~ 15.75V	
20V	Max.	20.5V	20.3V	20.4V	20.3V	10 211/	
200	Min.	19.6V	19.7V	19.6V	19.7V	19~21V	

6. 提供客戶分析報告與解決方案,符同意後在下次生產版本導入。

D6.)改善措施實施日期:Implement Permanent Corrective Actions:

(Note: Be provide the phase-in date or lot# of corrective actions implementation in process)

Immediately.

D7.)預防再發生措施:Prevent Recurrence:

(**Note:** Modified the management, operating systems, practices, and procedures to prevent recurrence for the problems as well as lessons learned cases.)

Same as D5.

D8.)確認並感謝問題解決成員:Check and Congratulate the Team:

(Note: Recognize the collective efforts of the team.)

Thanks to you all!!!

PM: Laura Fang

Sales: Jennie Chiang

RD: Will Wu

Richtek: Macro Chang · Yiwei Wu · Ben Chi · Patty Wu



Signature	Edward Ho	
Team Leader:	Edward no	
Signature by Approver:	Edward Ho	