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KPLUG I Kit  
(C) Diana Kancinski 2025  
Multiple uP Frame Board

(280, 6502, 65816, 68K P8)

- 4 I/O Ports

- o 2 x 8 bit <sup>Input</sup> Output Bi-D

- o 1 x 8 bit Output

- o 1 x 4 bit <sup>output</sup> Bi-D ~~6/0~~

- o 1 x 4 bit Input

- Supports W29 Ram

2864 E2am

- Address Code Display

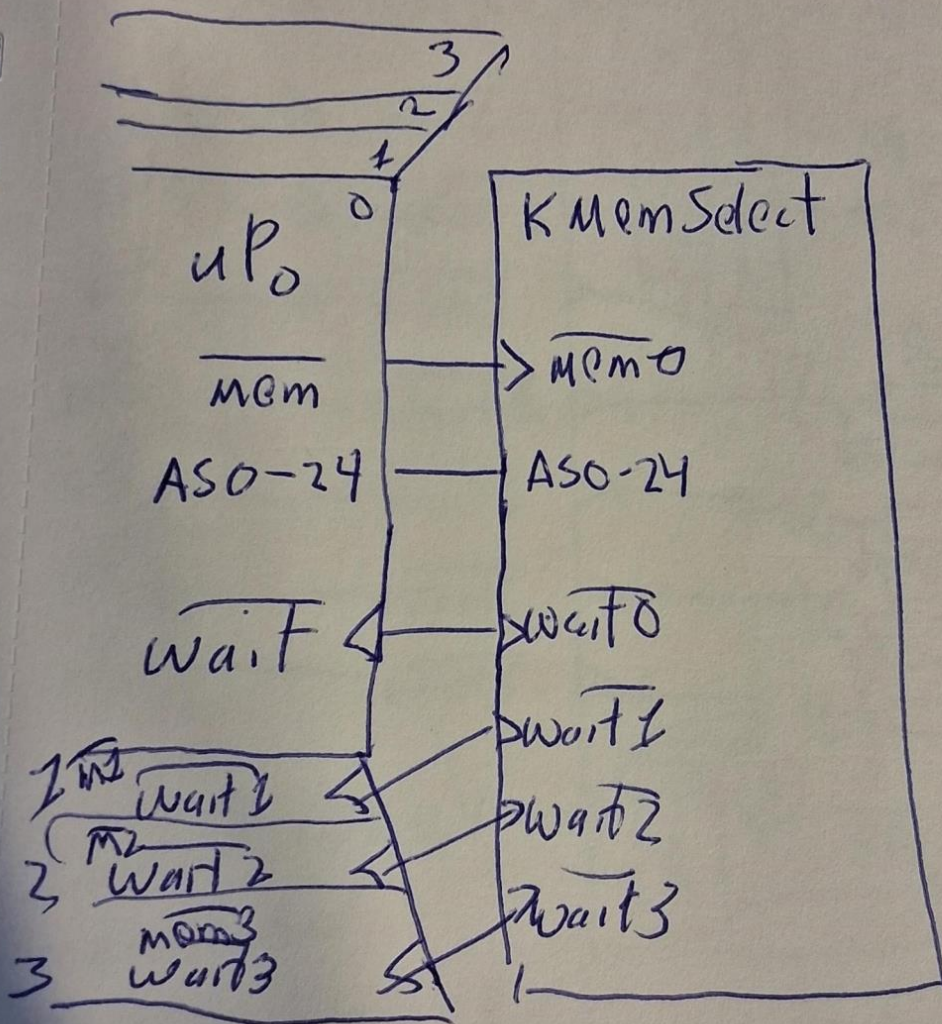
A0-7



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# K Shared Mem Ctrl Part 2

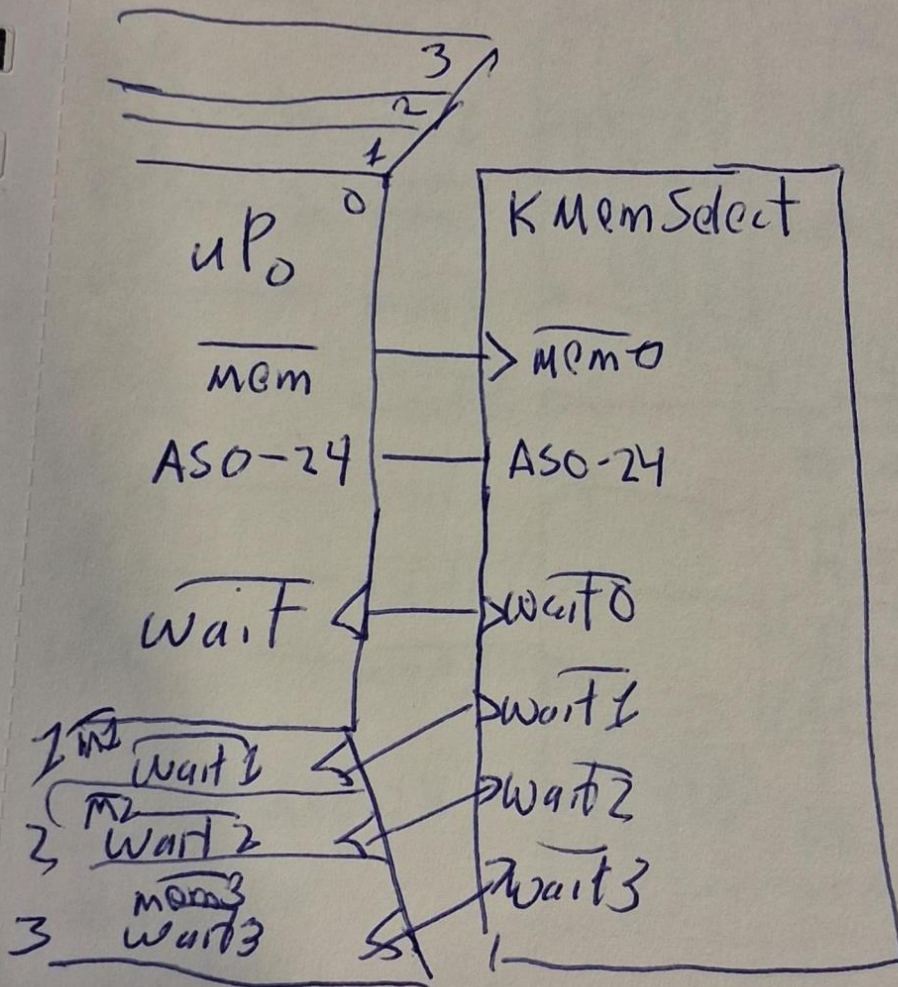




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# K Shared Mem Ctrl Part 2

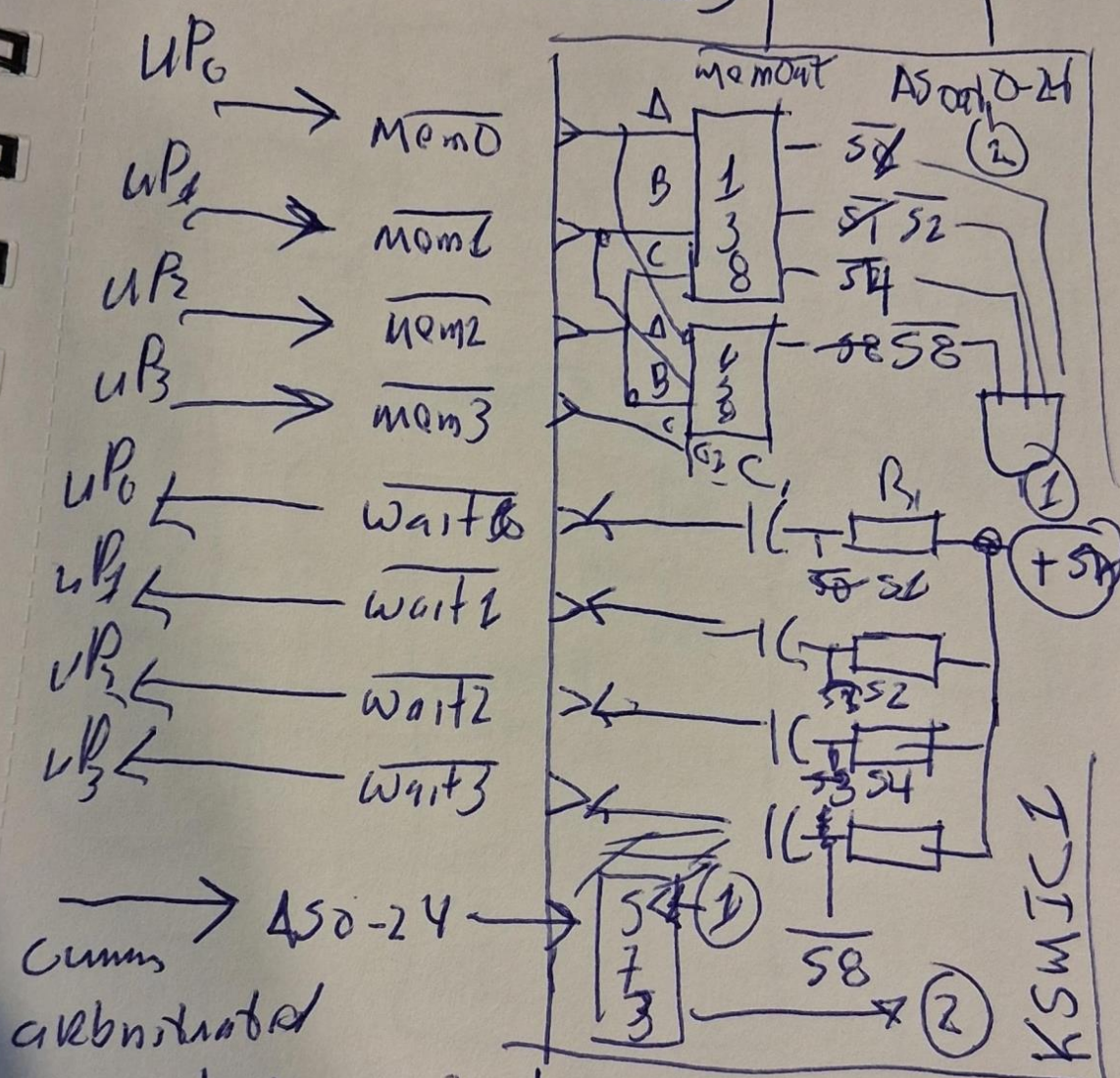




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# K5 Shared Mem Port X3

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Common  
 arbitrator  
 show bus

	0	1	2	3
0000	1	1	1	1
0001	1	1	1	0
0000	1	1	0	0
0011	1	1	1	1
0100	1	0	1	1

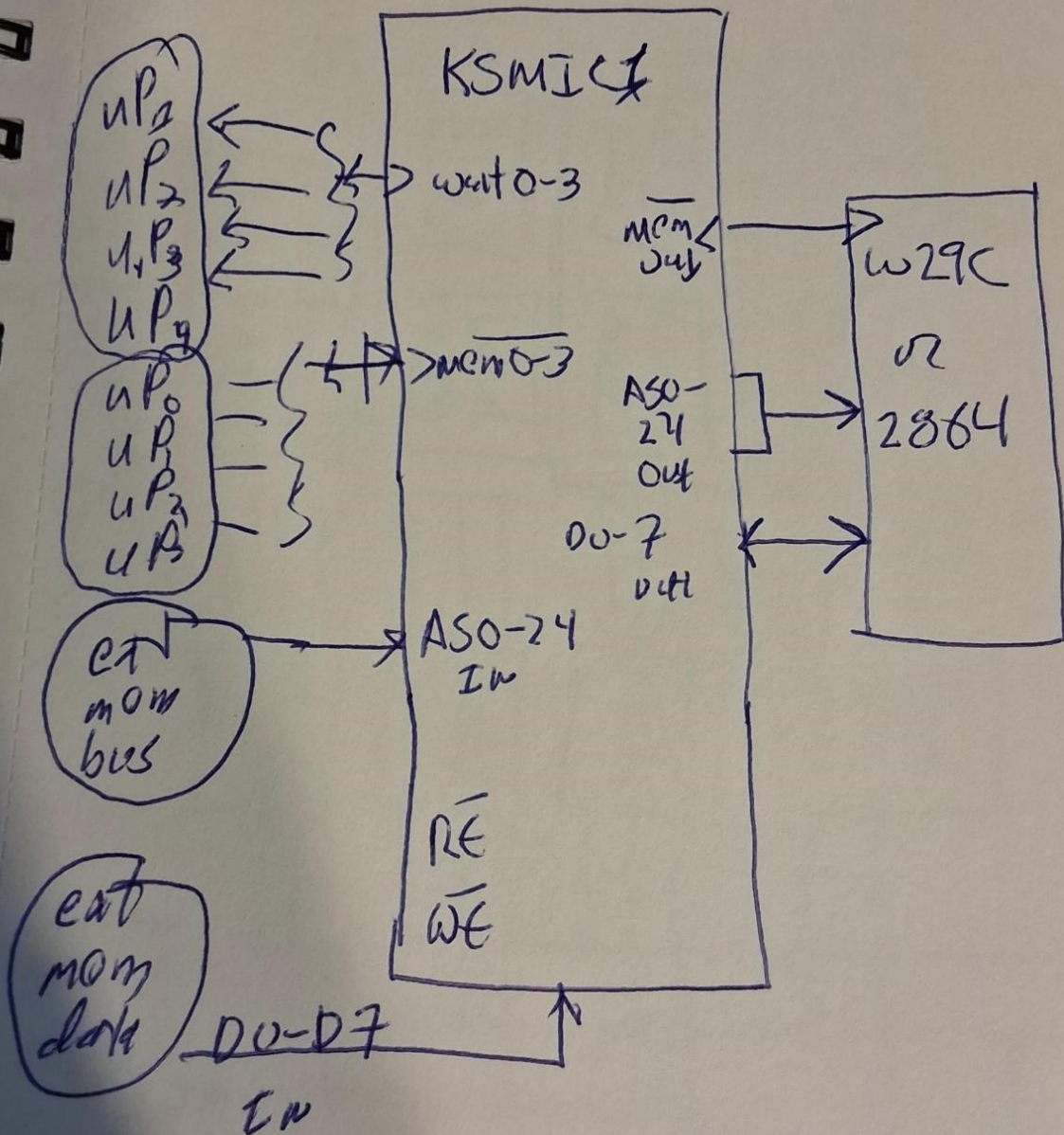
	0	1	2	3
0101	1	1	1	1
0110	1	1	1	1
0111	0	1	1	1
1000	1	1	1	1
1001	1	1	1	1
1010	1	1	1	1



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# KSharl Mem Part 8 4



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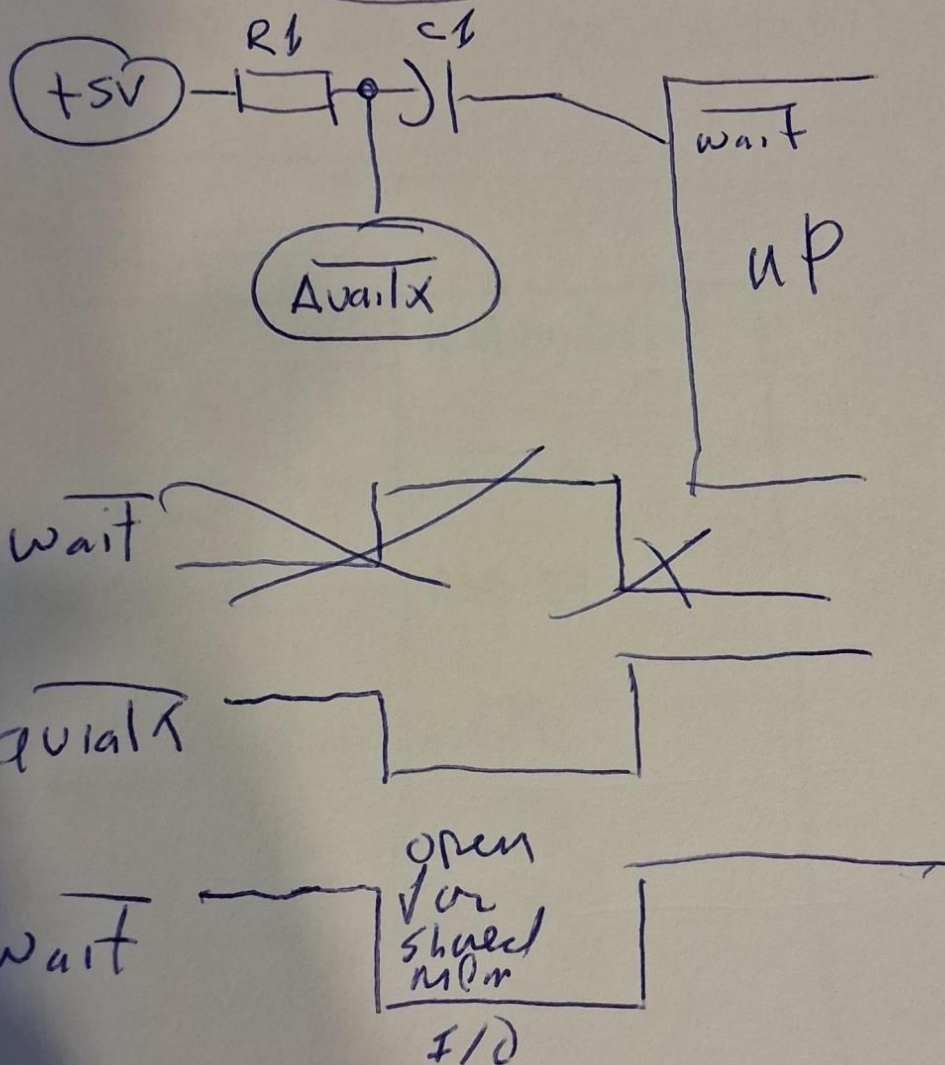




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## Shared Mem Ctrl Part I



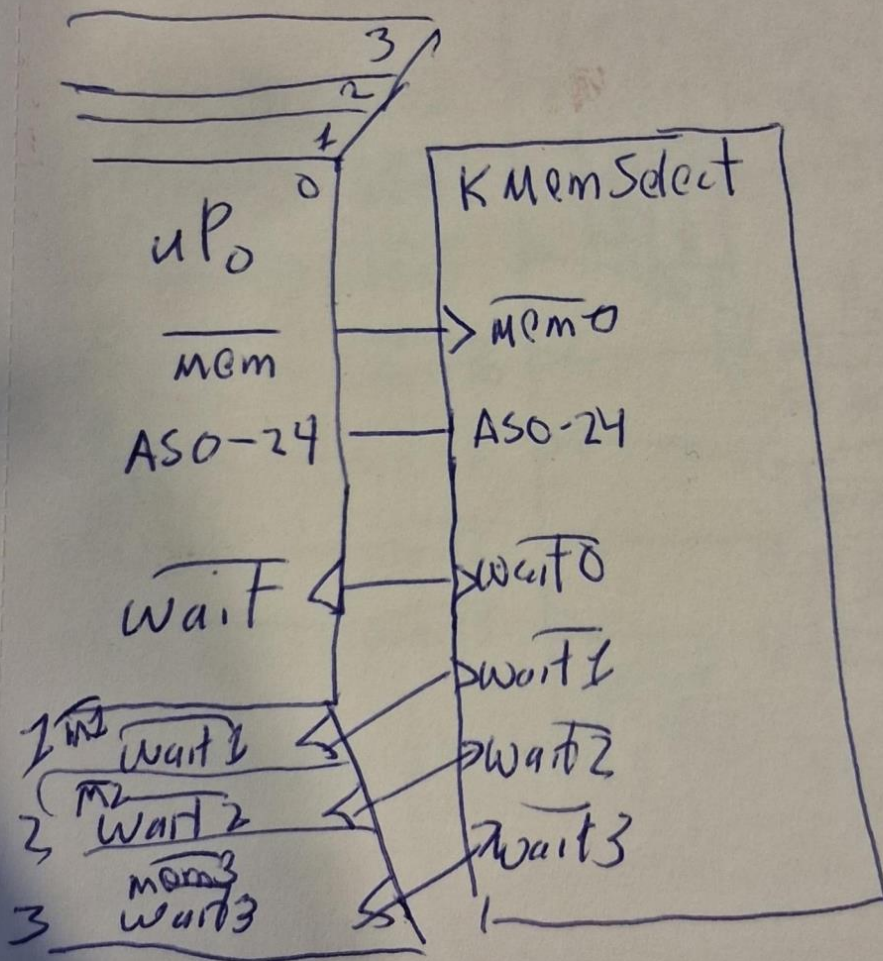
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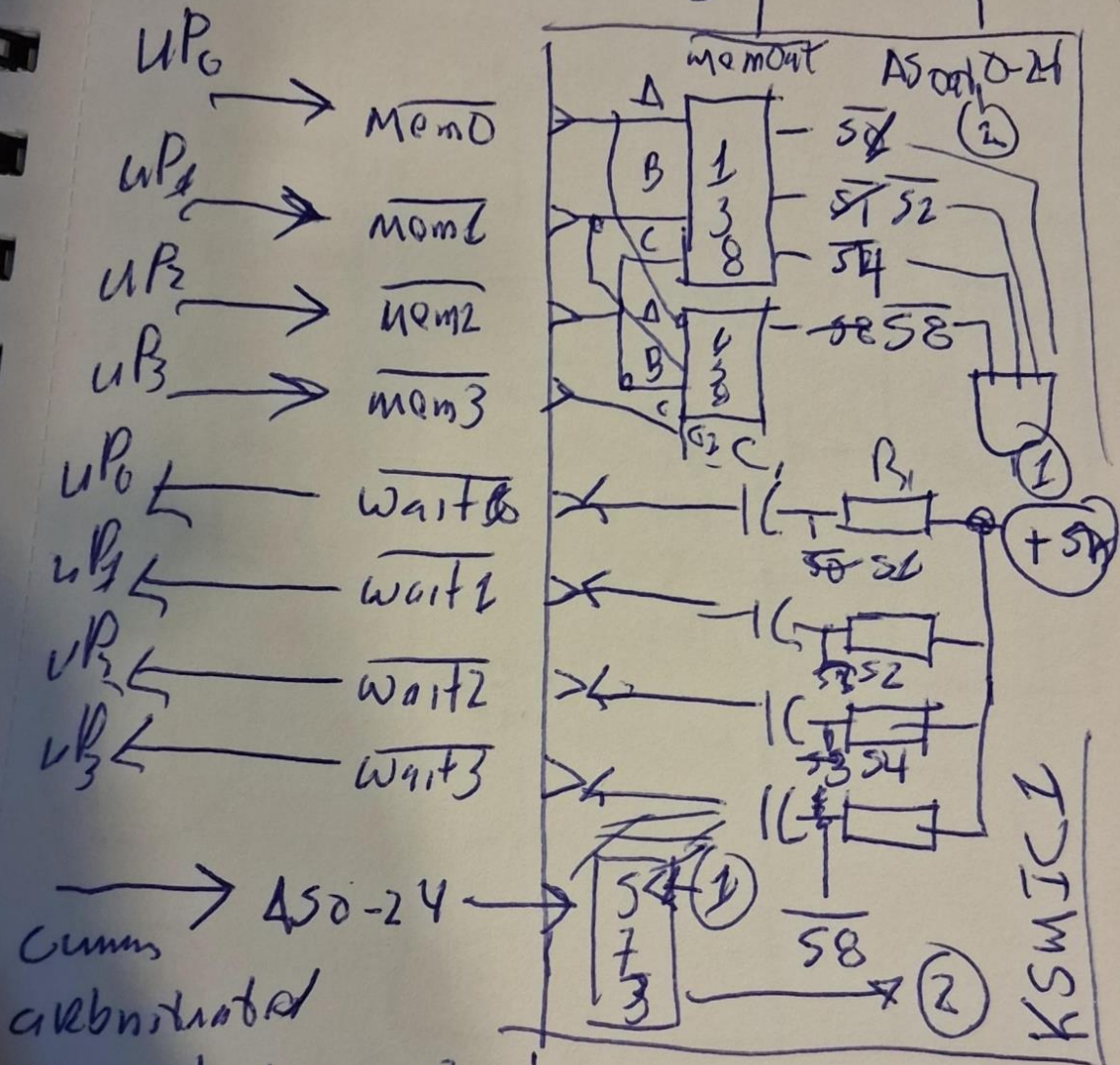
# K Shaver Mem Ctrl Part 2





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# K5 Shared Mem Port X3



Comm  
 arbitrated  
 shared bus

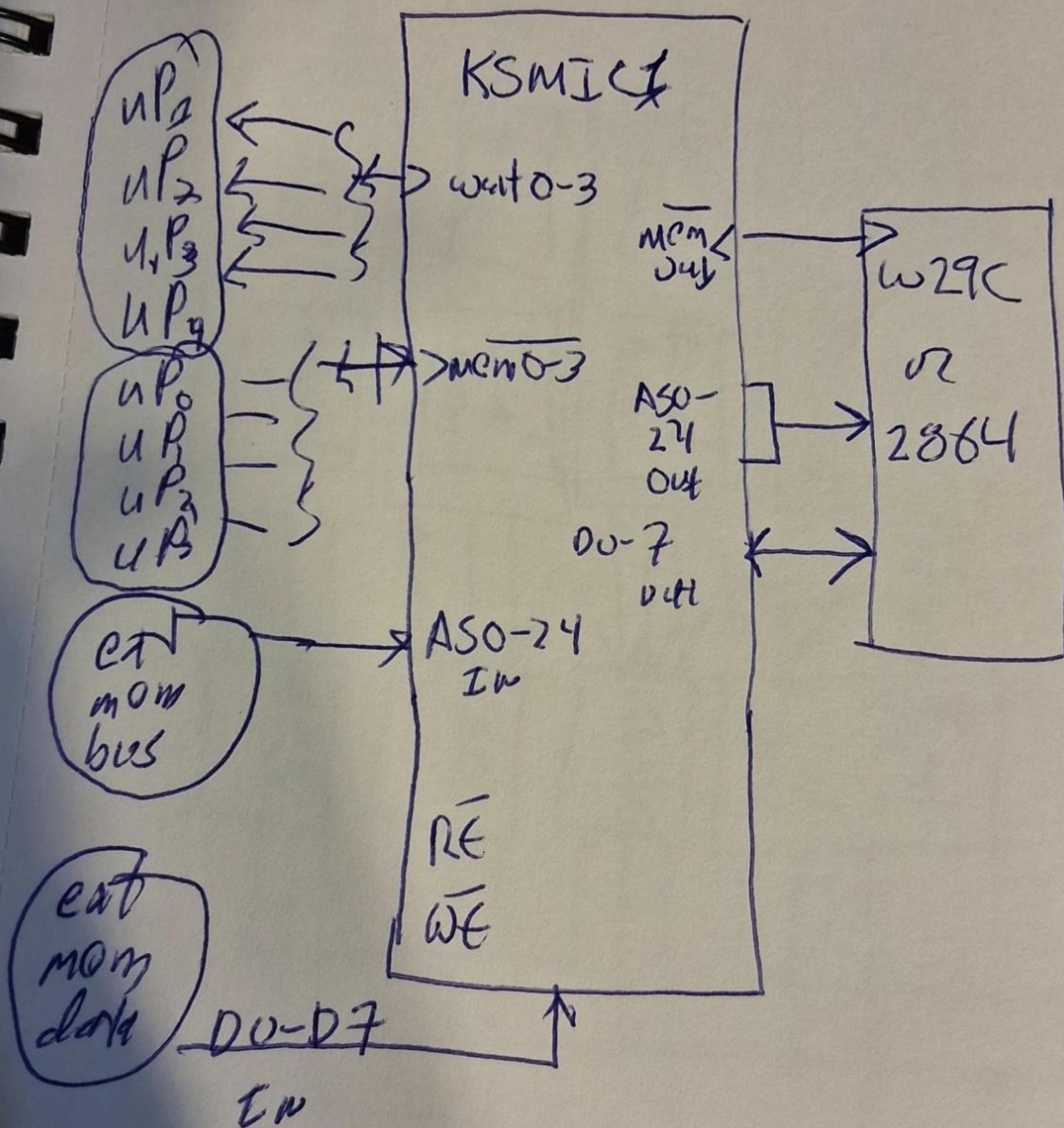
	0	1	2	3		0	1	2	3
0000	1	1	1	1	0101	1	1	1	1
0001	1	1	1	0	0110	1	1	1	0
0000	1	1	0	0	0111	0	1	1	1
0011	1	1	1	1	1000	1	1	1	0
0100	1	0	1	1	1001	1	1	1	1
					1010	1	1	1	1



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# KSh and Mem Part 3 & 4



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KShanel mem Part 7/5

