

# Automated Reasoning and Formal Verification

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Anno 2024-2025

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## 1 25-02-2025

### intro

Slides will be on his webpage along with the recordings.

The exam will consist of a script and an oral exam on the topics of the whole course.

### boolean/propositional logic

A propositional **formula** can be:

- $\top, \perp$
- Propositional **atoms**  $A_1, A_2, \dots, A_n$
- A combination of other formulas. If  $\varphi_1$  and  $\phi_2$  are formulas, so are:

- $\neg\varphi_1$
- $\varphi_1 \wedge \phi_2$
- $\varphi_1 \vee \phi_2$
- $\varphi_1 \rightarrow \phi_2$
- $\varphi_1 \leftarrow \phi_2$
- $\varphi_1 \leftrightarrow \phi_2$
- $\varphi_1 \oplus \phi_2$

We define a function  $Atoms(\varphi)$  representing the set  $\{A_1, \dots, A_n\}$  of atoms in  $\phi$

A **clause** is a disjunction of literals  $\bigvee_j l_j$  or  $(A_1 \vee \neg A_2 \vee \dots)$

A **cube** is a conjunction of literals  $\bigwedge_j l_j$  or  $(A_1 \wedge \neg A_2 \wedge \dots)$

## trees and DAGS

A tree is a natural representation of an expression, but in the worst cases it can grow exponentially. The same information about the formula can be conveyed by a *Directed Acyclic Graph*, which can grow linearly in size.

## Total Truth Assignment

They can also be abbreviated as *Total Assignment*.

A total truth assignment  $\mu : Atoms(\varphi) \mapsto \{\top, \perp\}$  represents *one* possible state of the formula.

## Partial Truth Assignment

A partial truth assignment  $\mu : \mathcal{A} \mapsto \{\top, \perp\}, \mathcal{A} \subset Atoms(\varphi)$  represents  $2^k$  total assignments, where  $k$  is the number of unassigned literals.

$\mu$  defined for total and partial truth assignments can be seen as a set of literals (positive and negative ones) or a formula.

## Set of models

$M(\varphi) \triangleq \{\mu \mid \mu \models \phi\}$  is the set of all models of  $\phi$ .

## Properties

- $\varphi$  is *valid* if every  $\mu$  models  $\phi$
- $\varphi$  valid  $\iff \neg\phi$  unsatisfiable
- $\alpha \models \beta \iff \alpha \rightarrow \beta$  valid
- $\alpha \models \beta \iff \alpha \wedge \neg\beta$  not satisfiable

Deduction  
theorem

corollary

## Equivalence and Equi-satisfiability

$\alpha$  and  $\beta$  are *equivalent* if  $\forall \mu. \mu \models \alpha \iff \mu \models \beta$ .

In other terms,  $M(\alpha) = M(\beta)$ .

**Equi-satisfiability**  $M(\alpha) \neq \emptyset \iff M(\beta) \neq \emptyset$ . This property is mostly used when applying transformations to formulas  $\beta \triangleq T(\alpha)$ .

Transformations can be *validity preserving* if they preserve the validity of the formula they're being applied to, or *satisfiability preserving* if they preserve its satisfiability.

## Shannon's expansion

$$\exists v.\varphi := \phi|v = \perp \vee \phi|v = \top$$

The existential is a disjunction between two possible formulas. One where  $v$  is set to true, and one where it is set to false.

$$\forall v.\varphi := \phi|v = \perp \wedge \phi|v = \top$$

The universal one is similar, with a conjunction between the two.

## Polarity of subformulas

Polarity is a metric defined for each subformula of a formula  $\varphi$  that tells us under how many nested negations it occurs. It can either be positive, negative, or both in some cases.

The recursive rules to determine the polarity are shown in the image below

- $\varphi$  occurs positively in  $\varphi$ ;
- if  $\neg\varphi_1$  occurs positively [negatively] in  $\varphi$ , then  $\varphi_1$  occurs negatively [positively] in  $\varphi$
- if  $\varphi_1 \wedge \varphi_2$  or  $\varphi_1 \vee \varphi_2$  occur positively [negatively] in  $\varphi$ , then  $\varphi_1$  and  $\varphi_2$  occur positively [negatively] in  $\varphi$ ;
- if  $\varphi_1 \rightarrow \varphi_2$  occurs positively [negatively] in  $\varphi$ , then  $\varphi_1$  occurs negatively [positively] in  $\varphi$  and  $\varphi_2$  occurs positively [negatively] in  $\varphi$ ;
- if  $\varphi_1 \leftrightarrow \varphi_2$  or  $\varphi_1 \oplus \varphi_2$  occurs in  $\varphi$ , then  $\varphi_1$  and  $\varphi_2$  occur positively and negatively in  $\varphi$ ;

If we assume  $\top = 1, \perp = 0$  we can also see the polarity of a subformula as "how much it contributes to the overall value of the formula".

## 2 Normal forms

### 2.1 Negative Normal Form - NNF

A negative normal form is a formula in which each negations has been pushed down to the atoms. This implies that every subformula in  $NNF(\varphi)$  has positive polarity.

#### Properties

- Every formula can be made into negative normal form
- NNF transformation preserves equivalence

### 2.2 Conjunctive Normal Form - CNF

$$\bigvee_{i=1}^L \bigwedge_{j=1}^{K_i} l_{ij}$$
$$(l_{11} \wedge l_{12}) \vee (l_{21} \wedge l_{22} \wedge l_{23}) \vee (\dots) \vee \dots$$

Every formula can be converted in *Conjunctive Normal Form*, but there are different ways to do so.

#### 2.2.1 Naive CNF conversion

The more intuitive and straightforward method consist of:

1. Expanding implications and equivalences
2. Pushing down negations like in NNF
3. Recursively applying DeMorgan's rule to get the CNF shape

This method produces a CNF that is equivalent to the original formula and has the same atoms. It is however rarely used in practical applications because it can be up to exponentially larger than the original formula.

### 2.2.2 Labeling CNF conversion

This is a more efficient *bottom-up* approach, which can be executed while parsing the expression. The main idea is that of introducing new variables that serve as "*labels*" for each subformula. The smaller formulas can be converted to CNF with the naive approach, and then assembled through the labels.

This method introduces new atoms, but  $\exists(B_1, \dots, B_k).CNF_l(\phi)$  equiv  $\phi$  where  $B_1, \dots, B_k$  are the newly introduced variables. This means that  $\phi$  and  $CNF_l(\phi)$  are equisatisfiable.

The representation obtained from the  $CNF_l$  can be reduced further in size by using polarization to change some implications around.

## 3 Basic SAT-solving techniques

**Example:** A classic problem is that of checking a query under a (usually much larger) knowledge base. This problem can be reduced to SAT.  $KB \models \alpha$  or  $M(KB) \subseteq M(\alpha)$

$$KB \models \alpha \iff SAT(KB \vee \neg\alpha) = false$$

### 3.1 Intro - Unit propagation

**Resolution rule** Deduction of a new clause from a pair of clauses with exactly one incompatible variable (which is called the "*resolvent*").

$$(a \vee b \vee c) \wedge (d \vee e \vee \neg c) = (a \vee b \vee d \vee e)$$

**Removal of valid clauses** If a clause is valid (always true) it can be removed from the formula.

**Clause subsumption** If a clause appears on its own and inside another clause, we can remove the second, bigger, clause.

**Unit resolution** Having a clause composed of a single literal forces said literal to be true. This means we can remove all instances of the negated literal.

**Unit subsumption** Like clause subsumption but with a literal instead of a clause

These unit propagation rules can happen in a chain. After modifying the formula once we can create new unary clauses for example.

### 3.2 Resolution algorithm

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**Algorithm 1:** Resolution algorithm

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```

Assume input is in CNF;
 $\varphi$  is a set of clauses;
//Search for a refutation of  $\varphi$ ;
repeat
| apply resolution rule to pairs of clauses;
until a false clause is generated  $\vee$  the rule is not applicable ;

```

---

This algorithm is correct and complete, but operates in exponential memory and is time inefficient.

### 3.3 Tableaux

Search assignments satisfying  $\varphi$  by applying *elimination rules* on its connectors.  
Try to be clever and put smaller clauses first in the branching order.

## 4 LAB 1

### 4.1 DIMACS

Dimacs is the standard format for representing SAT problems.

#### File structure

- Comments start with 'c'
- header of the form 'p cnf 7 8'
  - 7: number of variables
  - 8: number of clauses
- 1 clause for line of form "1 -2 0"
  - trailing 0 is a constant
  - 1 first variable
  - -2 second variable, negated

**command** `mathsat -input=dimacs -model file.cnf`  
`-model` tells it to provide a model, otherwise it gives a yes/no answer

## 5 19/03/202

(set-option :produce-models true) Generation of models  
(set-option :produce-unsat-cores true) Extraction of UNSAT cores  
(set-option :produce-proofs true) Building UNSAT proof  
(set-logic ; logic ; ) Set background logic

## 6 Kripke Models

The semantic framework for a variety of logics like Modal Logics, Description Logics, and *Temporal Logics*.

The **practical role** of a Kripke model is to describe *reactive systems*. This means nonterminating systems with infinite behaviors (like communication protocols and circuits).

### 6.1 Formal Definition

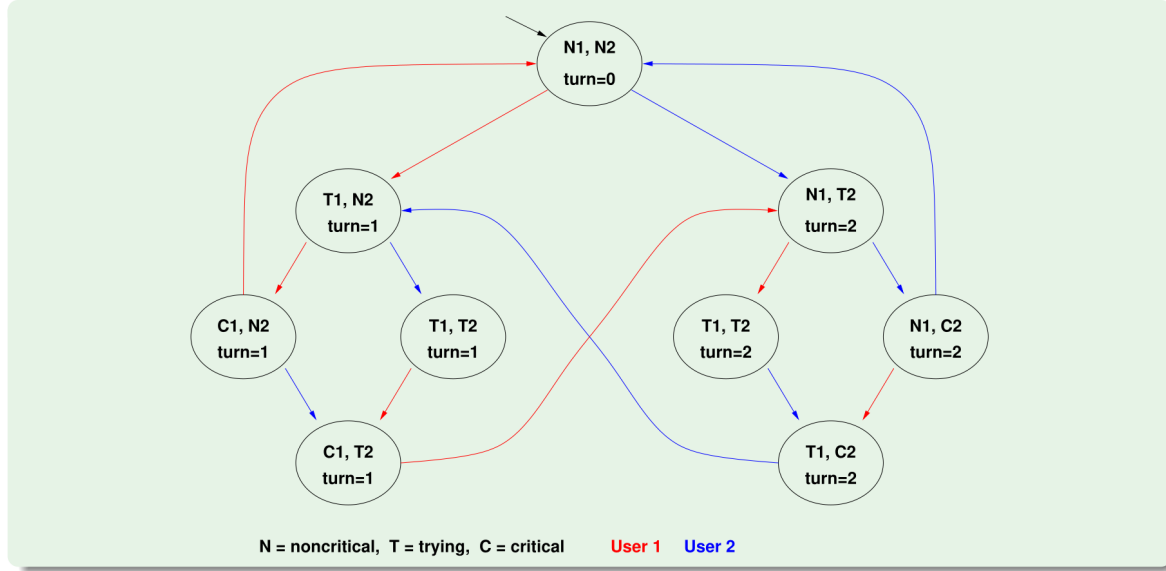
A Kripke model  $\langle S, I, R, AP, L \rangle$  consists of:

- a finite set of states  $S$
- a set of initial states  $I \subseteq S$
- a set of transitions  $R \subseteq S \times S$
- a set of atomic propositions  $AP$
- a labeling function  $L : S \mapsto 2^{AP}$

We assume  $R$  to be total, so for every state  $s$  there exists at least one state  $s'$  such that  $s, s' \in R$ . Sometimes we use variables with discrete bounded values  $v_i \in \{d_1, \dots, d_k\}$

**Remark.** Unlike with other types of automata, in Kripke models the values of all variables are always assigned in each state

## Example: a Kripke model for mutual exclusion



8/97

**Path** A path in a Kripke model  $M$  is an *infinite* sequence of states  $\pi = s_0, s_1, \dots \in S^\omega$

**Reachable** A state is reachable if there exists a path that includes it

**Asynchronous Composition/Product** At each time instant, one component is selected to perform a transition

It is a typical formalization for protocols since it models agents well.

$$M \stackrel{def}{=} M_1 || M_2 \stackrel{def}{=} \langle S, I, R, AP, L \rangle$$

- $S \subseteq s_1 \times s_2$  s.t.  $\forall \langle s_1, s_2 \rangle \in S, \forall I \in AP_1 \cap AP_2, I \in I_1(s_1) \text{ iff } I \in L_2(s_2)$
- $I \subseteq \dots$

### Asynchronous product of Kripke models

Let  $M_1 \stackrel{def}{=} \langle S_1, I_1, R_1, AP_1, L_1 \rangle, M_2 \stackrel{def}{=} \langle S_2, I_2, R_2, AP_2, L_2 \rangle$ . Then the **asynchronous product**  $M \stackrel{def}{=} M_1 || M_2$  is  $M \stackrel{def}{=} \langle S, I, R, AP, L \rangle$ , where

- $S \subseteq S_1 \times S_2$  s.t.,  $\forall \langle s_1, s_2 \rangle \in S, \forall I \in AP_1 \cap AP_2, I \in L_1(s_1) \text{ iff } I \in L_2(s_2)$
- $I \subseteq I_1 \times I_2$  s.t.  $I \subseteq S$
- $R(\langle s_1, s_2 \rangle, \langle t_1, t_2 \rangle) \text{ iff } (R_1(s_1, t_1) \text{ and } s_2 = t_2) \text{ or } (s_1 = t_1 \text{ and } R_2(s_2, t_2))$
- $AP = AP_1 \cup AP_2$
- $L : S \mapsto 2^{AP}$  s.t.  $L(\langle s_1, s_2 \rangle) \stackrel{def}{=} L_1(s_1) \cup L_2(s_2)$ .

Note: combined states must agree on the values of Boolean variables.

**Synchronous Composition/Product** At each time instant, every component performs a transition.

It is a typical formalization for circuits, since it models nicely the behaviour of a clock.

#### Synchronous product of Kripke models

Let  $M_1 \stackrel{\text{def}}{=} \langle S_1, I_1, R_1, AP_1, L_1 \rangle$ ,  $M_2 \stackrel{\text{def}}{=} \langle S_2, I_2, R_2, AP_2, L_2 \rangle$ . Then the **synchronous product**  $M \stackrel{\text{def}}{=} M_1 \times M_2$  is  $M \stackrel{\text{def}}{=} \langle S, I, R, AP, L \rangle$ , where

- $S \subseteq S_1 \times S_2$  s.t.,  $\forall \langle s_1, s_2 \rangle \in S$ ,  $\forall I \in AP_1 \cap AP_2$ ,  $I \in L_1(s_1)$  iff  $I \in L_2(s_2)$
- $I \subseteq I_1 \times I_2$  s.t.  $I \subseteq S$
- $R(\langle s_1, s_2 \rangle, \langle t_1, t_2 \rangle)$  iff  $(R_1(s_1, t_1) \text{ and } R_2(s_2, t_2))$
- $AP = AP_1 \cup AP_2$
- $L : S \mapsto 2^{AP}$  s.t.  $L(\langle s_1, s_2 \rangle) \stackrel{\text{def}}{=} L_1(s_1) \cup L_2(s_2)$ .

Note: combined states must agree on the values of Boolean variables.

## 6.2 Descriptor languages

most often a Kripke model is not given explicitly but represented in a structured language (like SMV, VHDL, etc...)

### 6.2.1 The SMV Language

**riprendere.**

## 6.3 Standard Programming Languages

Standard programming languages can be seen as a transition relation in terms also of the program counter.

## 6.4 Properties

**Safety Properties** Bad events never happen (deadlock and other bad conditions). This can be seen as imposing that no reachable state satisfies a "bad" condition (e.g. never two processes in critical section)

This property can be refuted by a finite behaviour (we just need to prove *one* bad execution). This is fairly obvious.

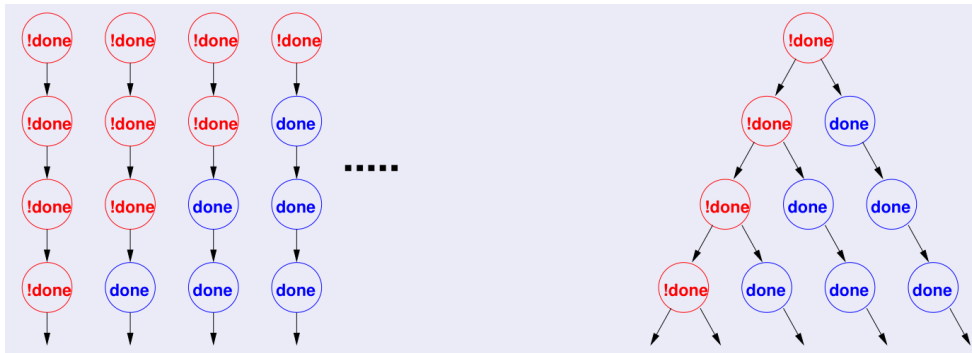
**Liveness Properties** Something desirable will eventually happen. This can be refuted by *infinite* behaviour.

Since we're working with finite machines, infinite behaviours are only achieved by loops. Hence they can be detected with (advanced) loop detection.

**Fairness Properties** Something desirable will happen *infinitely often*. This can be seen as a further restriction on the liveness property, since we no longer require that something happens but we require that it also *keeps* happening.

## 6.5 Computation trees vs paths

Given a Kripke structure its execution can be seen as an infinite set of computation paths or an infinite computations tree



## 7 Temporal Logics

They are divided in *Linear Temporal Logic (LTL)* and *Computation Tree Logic (CTL)*