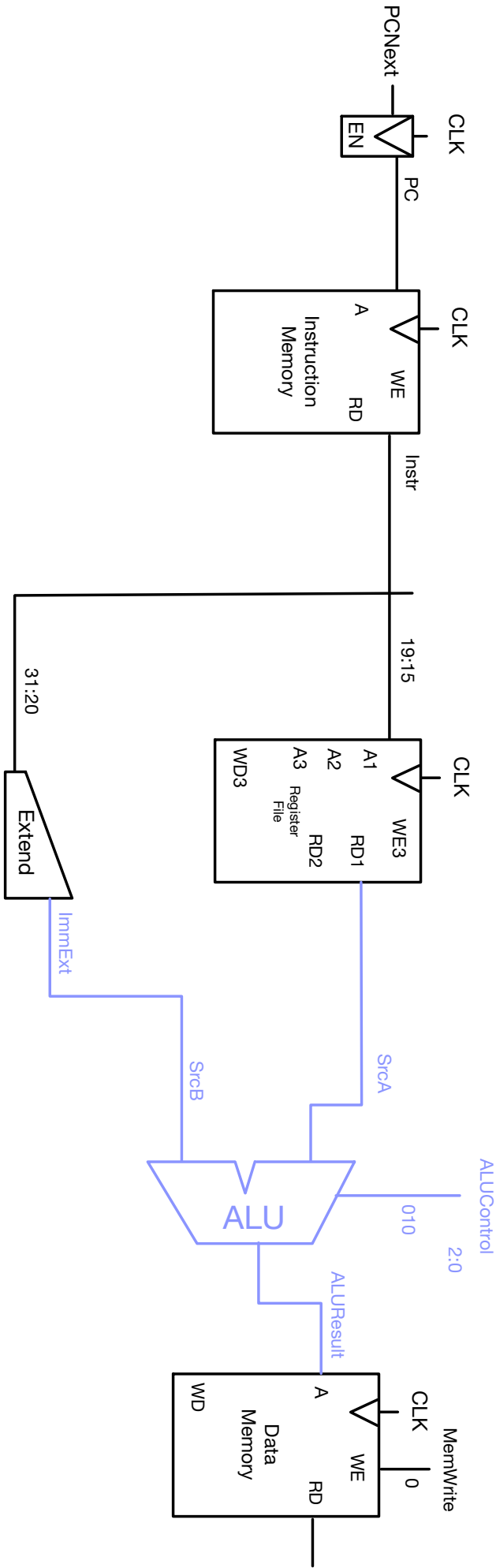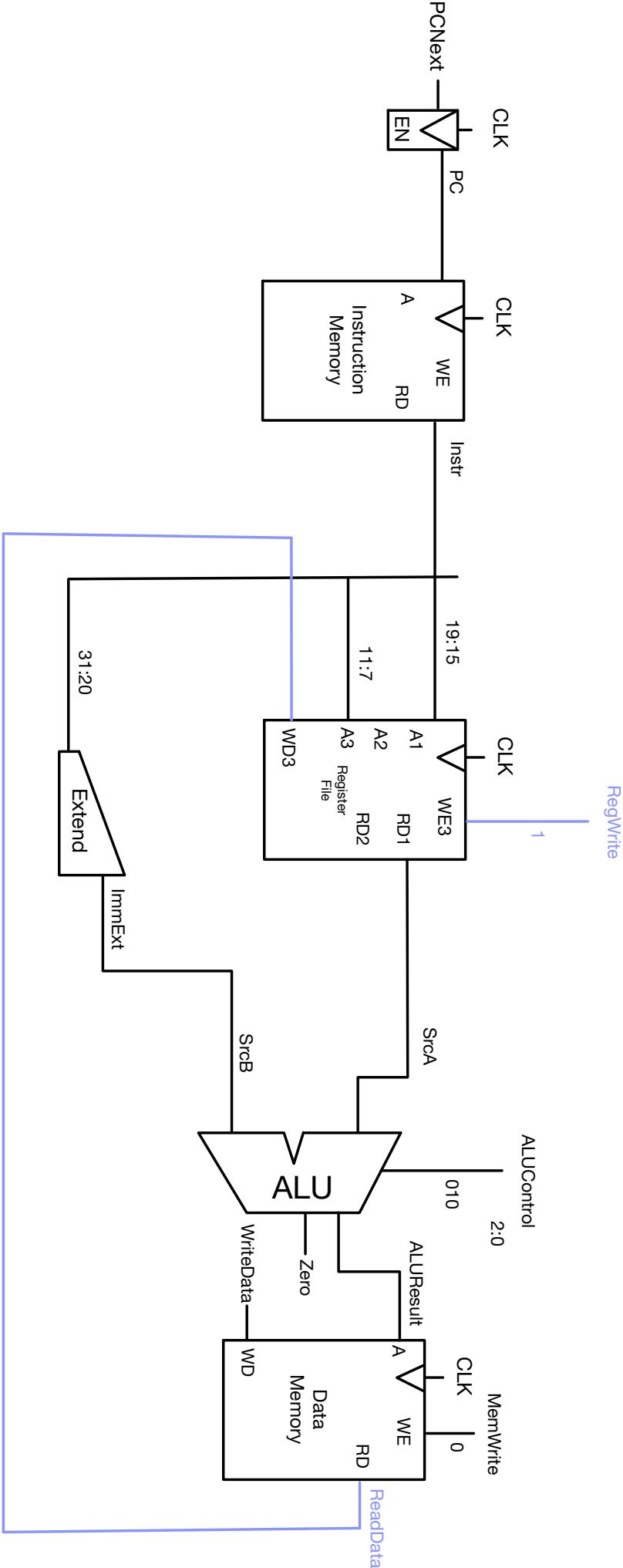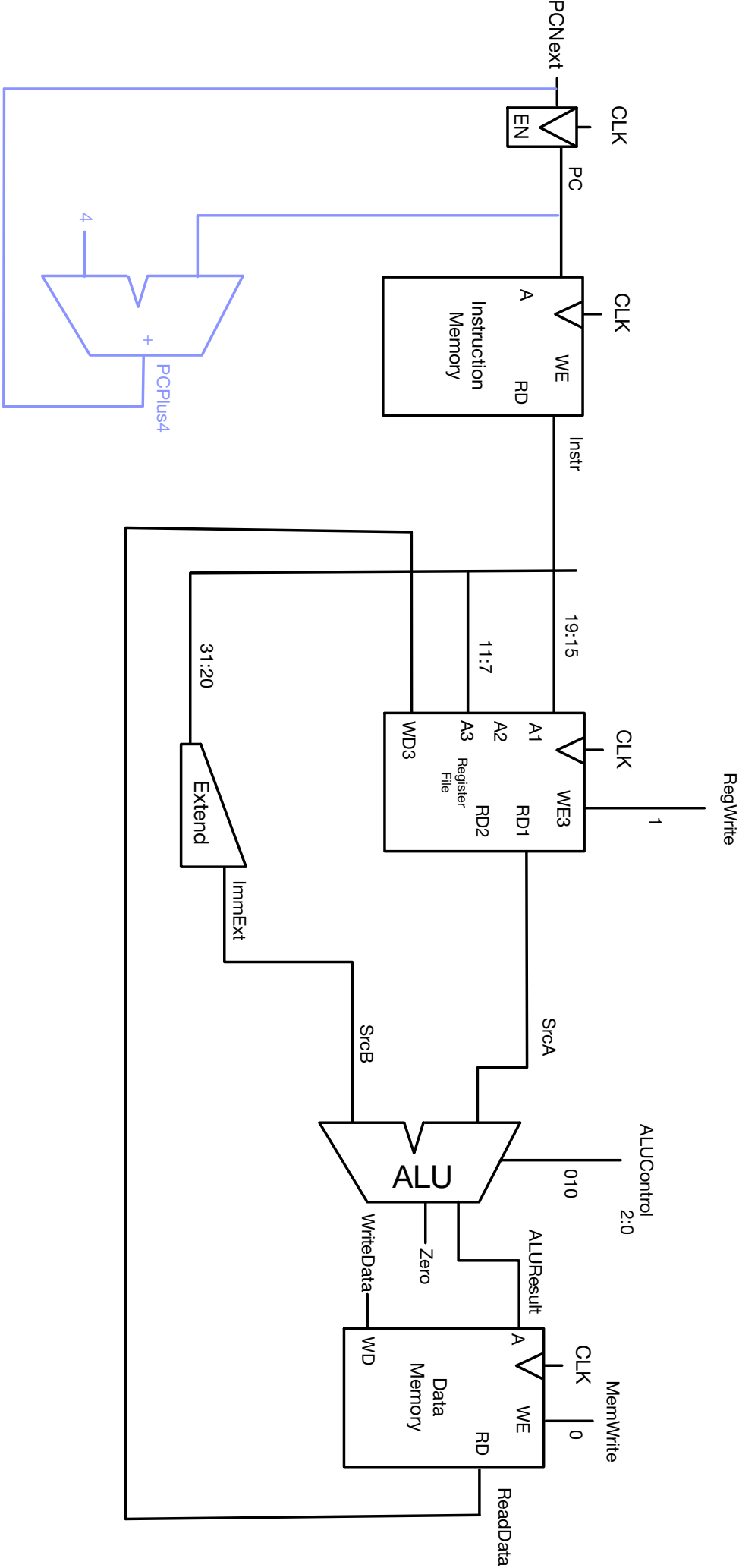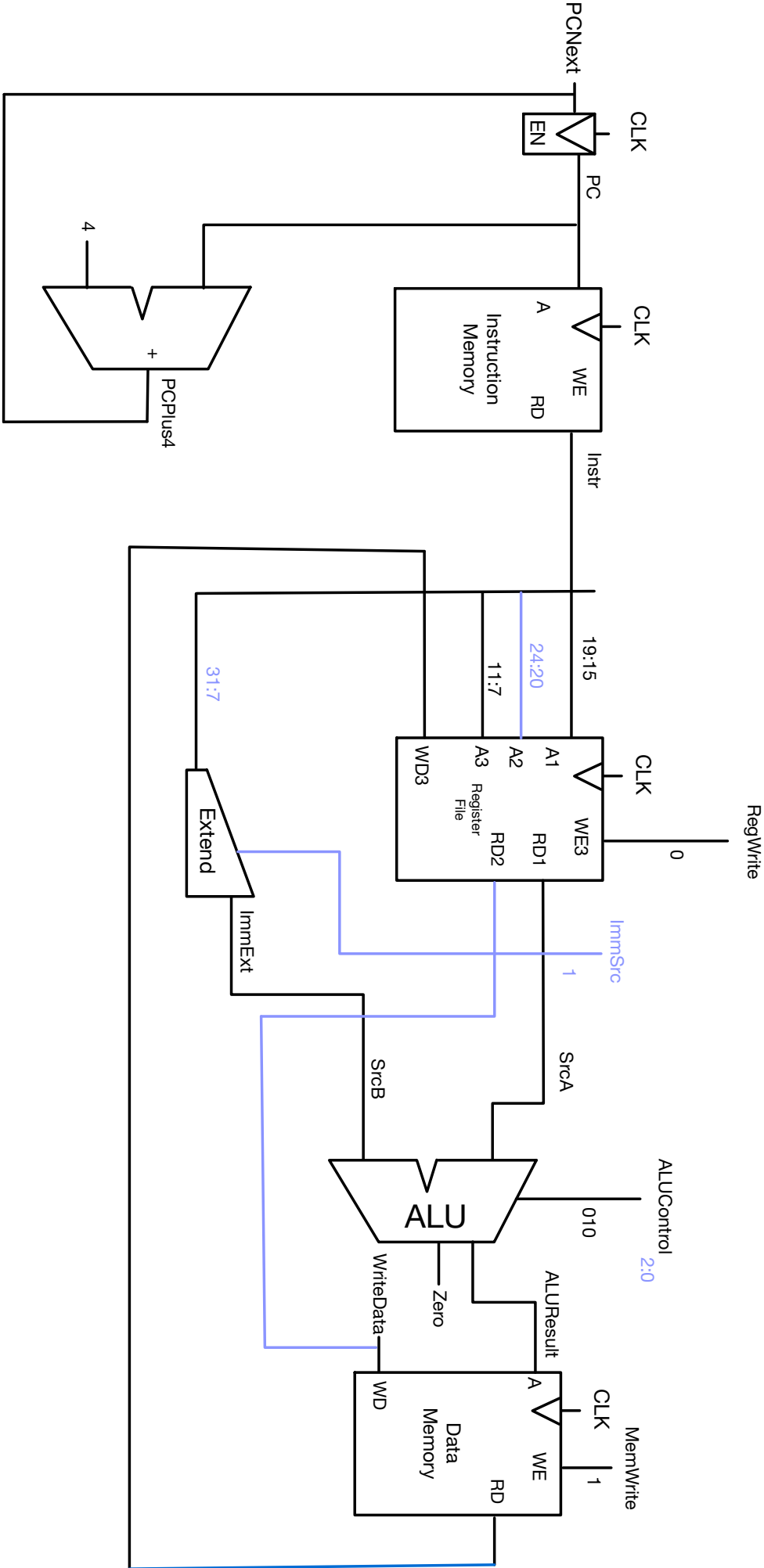# lw address

LDR Mem Read

PCPLUS4

S type

R-Type

ImmSrc   ALUSrc ALUControl   MemWrite   ResultSrc

CLK   PCNext   PC   CLK   WE3   SrcA   Zero   CLK   WE

A   RD   Instr   A1   RD1   ALUResult   ReadData   1
Instruction   A2   RD2   SrcB   A   RD   0
Memora   A3   ALU   Data
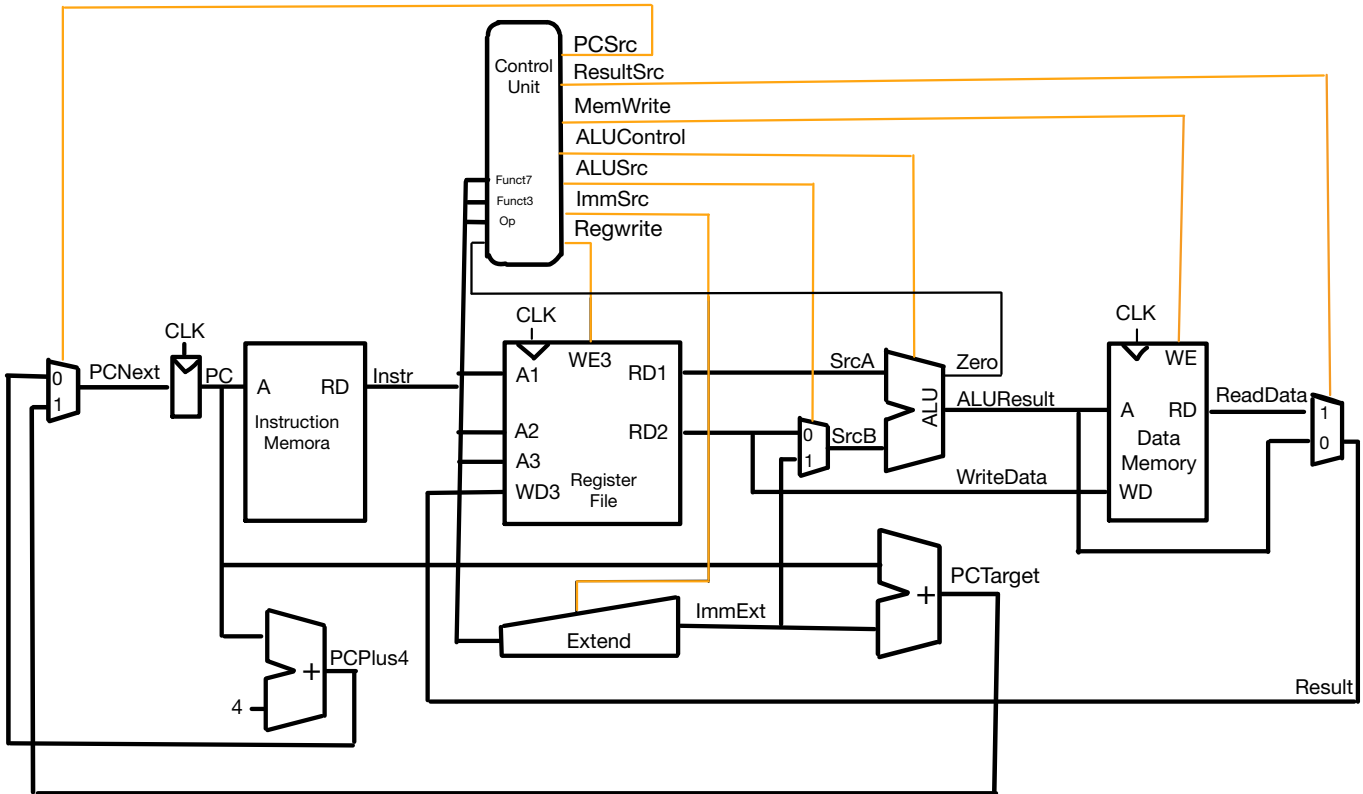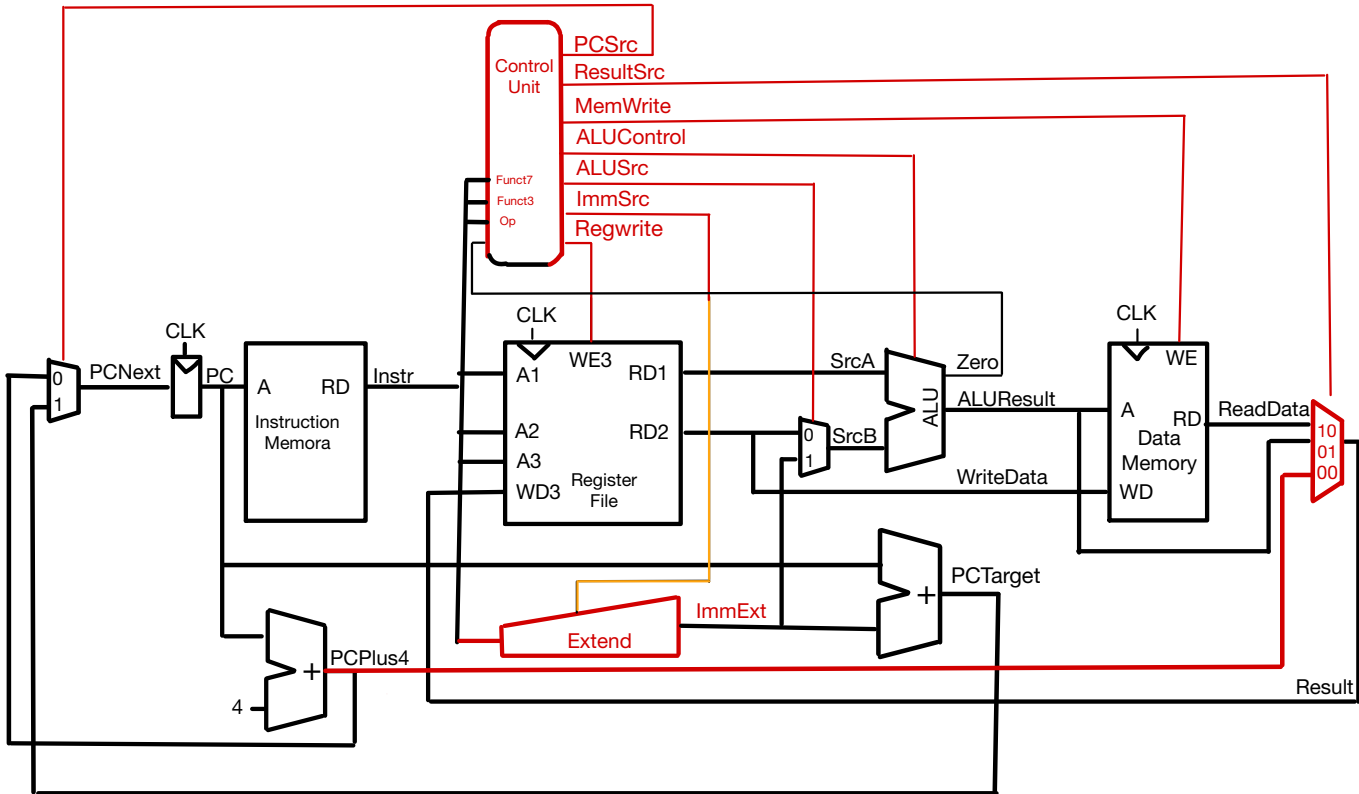WD3   Register   WriteData   Memory
File   WD

ImmExt

Extend

+   PCPlus4

4

Result
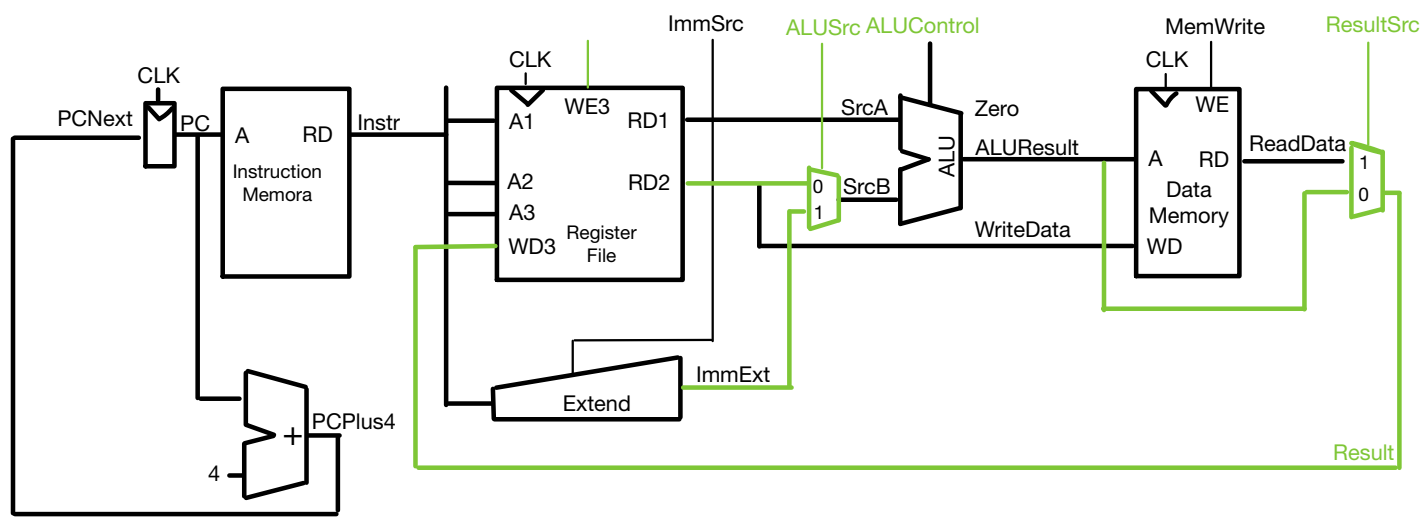
B-Type
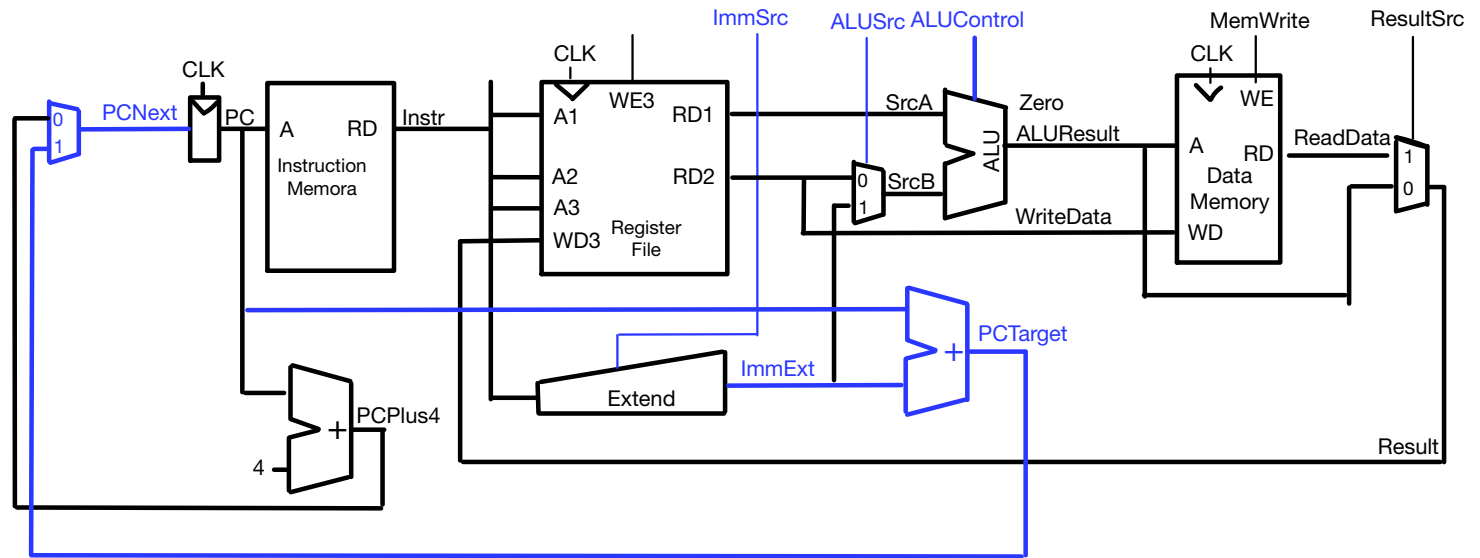
Processor

I-Type

ALU Decoder