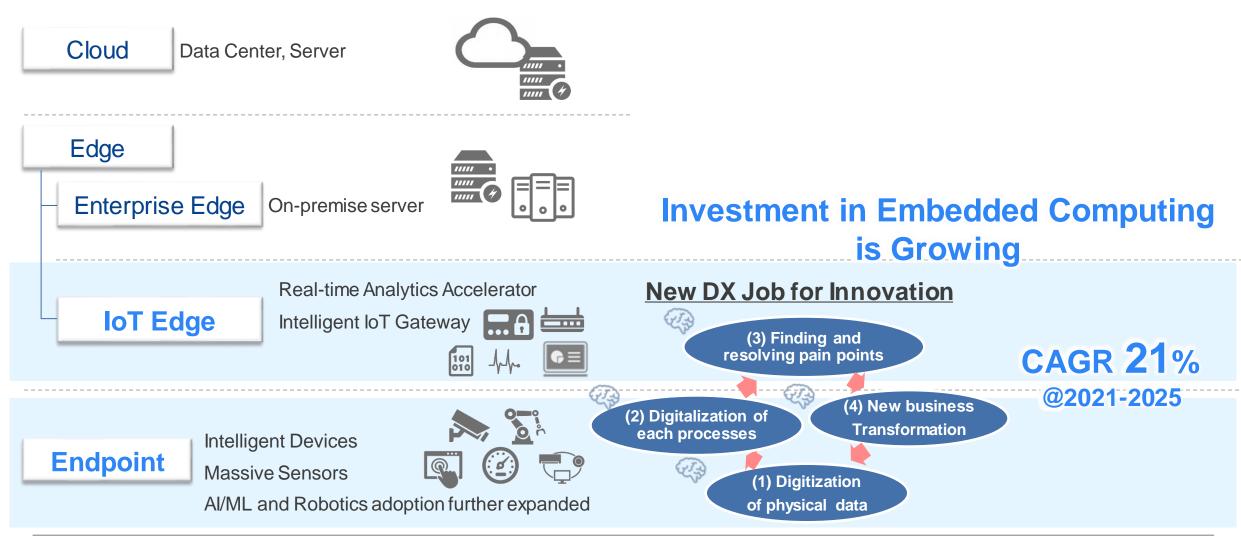
RZ MPU PRODUCTS INTRODUCTION

VERSION 1.11

RENESAS ELECTRONICS

INTELLIGENCE MOVING FROM THE CLOUD TO "THE IOT EDGE" AND "THE ENDPOINT" DX AND AI/ML ACCELERATE INVESTMENT TO INNOVATE



THE BEST IOT EDGE/ENDPOINT SOLUTION

RENESAS EMBEDDED PROCESSING PLATFORM PROVIDES LOW LATENCY/REAL-TIME, BANDWIDTH, SECURITY/ROBUSTNESS, AND AUTONOMOUS

Renesas' **Embedded Processing Platform**

IoT Edge



Many cores @GHz operation Al acceleration with multitasking **Cyber security compliant**

OSS (Open Source Software) availability







Endpoint



Scalable MCU/MPU lineups Real-time control & networking Al acceleration

with real-time and low-power **Security and safety compliant**

Combination with sensors & analogs

OSS availability

























RZ FAMILY

DEMONSTRATE MAXIMUM PERFORMANCE FOR HMI, INDUSTRIAL NETWORK & VISION AI

Human machine interface



2D Graphics + RTOS



Multimedia /3D Graphics + Linux

Vision Al



RZ/V Series

Al accelerator + Linux



Industrial Network



Multi-protocol Industrial Ethernet + 5ports GMAC with Switch & Redundancy

Industrial real-time control



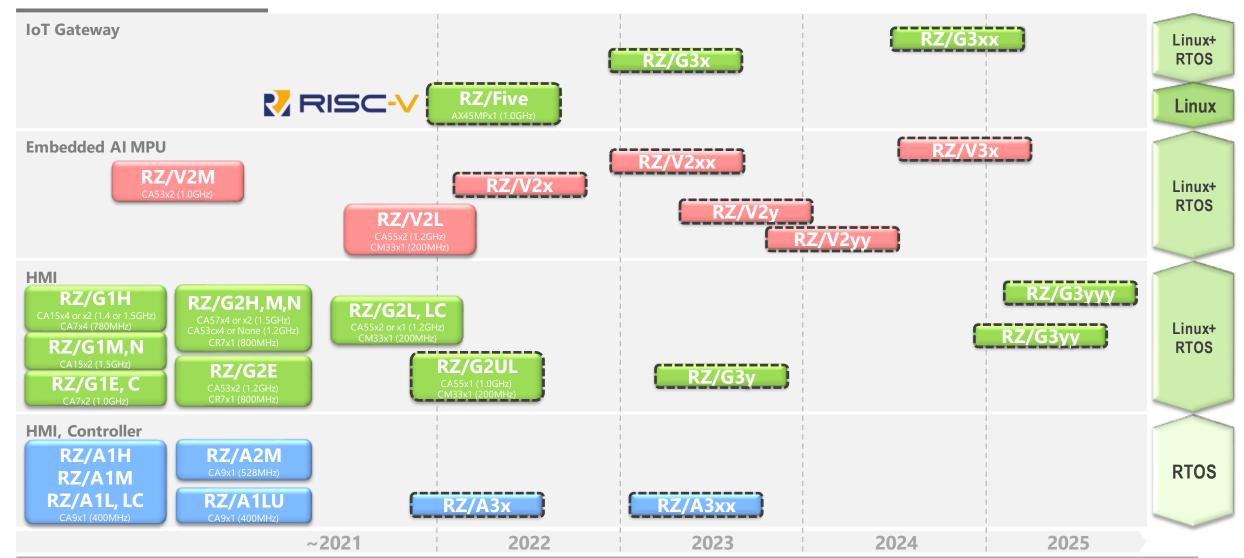
Real-time control+ Multi-protocol Industrial Ethernet

RENESAS' RZ MPU FAMILY

	H	VII	Networking	Control	Al	
RENESAS	RZ/A	RZ/G	RZ/N	RZ/T	RZ/V	
RZ	2D Graphics + RTOS	Multimedia /3D Graphics + Linux	Multi-protocol Industrial Ethernet + 5ports switch w/ GMAC & redundancy	Real-time control+ Multi-protocol Industrial Ethernet, FuSa	Al accelerator + Linux	
loT Edge		Linux + RTOS • Wider portfolio from High to Entry class • Super long-term support Linux(CIP)	Linux + RTOS • Up to 5 ports TSN switch with GMAC • Redundancy network support		Linux + RTOS • DRP enabling good power efficiency (performance / watt) • CIP Linux & Flexible Al Tool support	
Endpoint	RTOS	Linux + RTOS	Linux + RTOS	RTOS	Linux + RTOS	
	 Integrated Large RAM (w/o DRAM) Easy to use GHz CPU like MCU 	 Wider portfolio from High to Entry class CIP Linux & GUI Framework support 	 Up to 5 ports TSN switch w/ GMAC Multi-protocol IA network support 	 Realtime CPU & large size TCM Multi-protocol IA network & FuSa 	 DRP enabling good power efficiency (performance / watt) CIP Linux & Flexible Al Tool support 	

RZ MPU FOR HMI, AI AND IOT GATEWAY





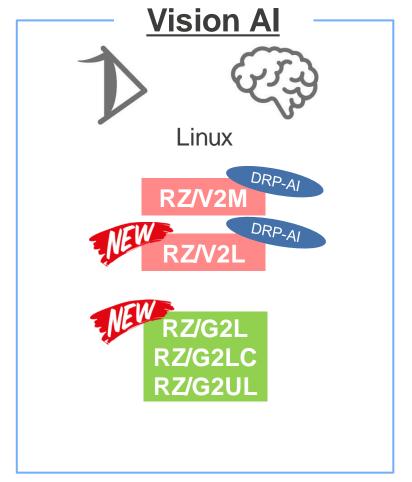
RZ MPU AND ASSP FOR INDUSTRIAL AUTOMATION

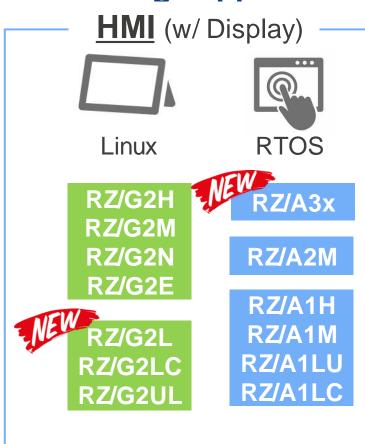


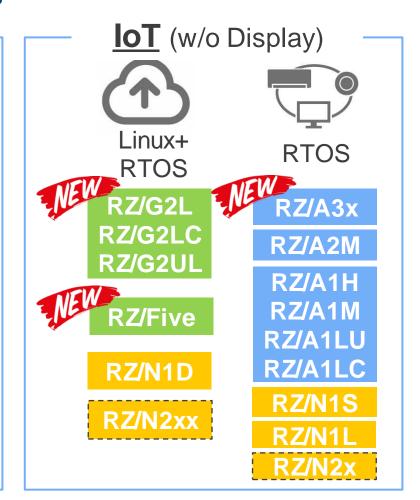
PLC / Controller	RZ/N1D CA7x2 (500MHz) CM3 (125MHz) RZ/N1S CA7 (500MHz) CM3 (125MHz)			RZ/N	2xx	Linux+ RTOS
Servo / Inverter / Industrial Robot	RZ/T1 CR4 (600MHz) CM3 (150MHz)	RZ/T	2x RZ/T2xx	RZ/T2	XXX	Linux+ RTOS
Remote IO / Network module R-IN32 M4-CL3 R-IN32 M3-CL TPS-1 R-IN32 M3-EC	RZ/N1L CM3 (125MHz)	RZ	/N2x		RZ/Nxx	RTOS
Module product	R-IN32M3 Module					
	~2020	2021	2022	2023	2024	

RZ FAMILY TARGET APPLICATION

✓ RZ Family can realize wide range applications





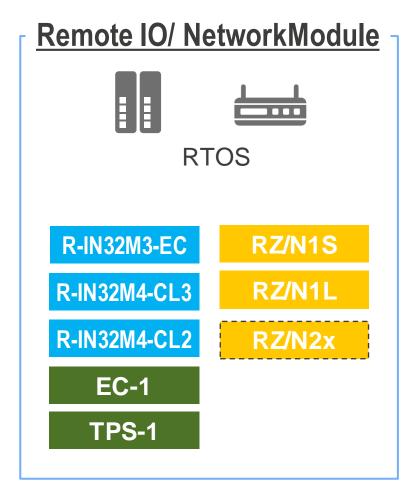


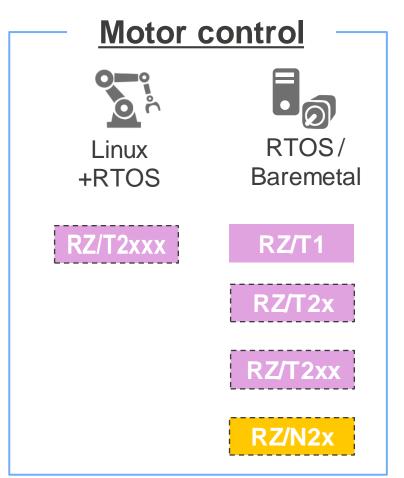
RZ FAMILY TARGET APPLICATION

✓ F

RZ Family can realize wide range applications

PLC/ Controller Linux / **RTOS** Linux+RTOS RZ/G2H RZ/N1S RZ/G2M RZ/N1D RZ/N2xx

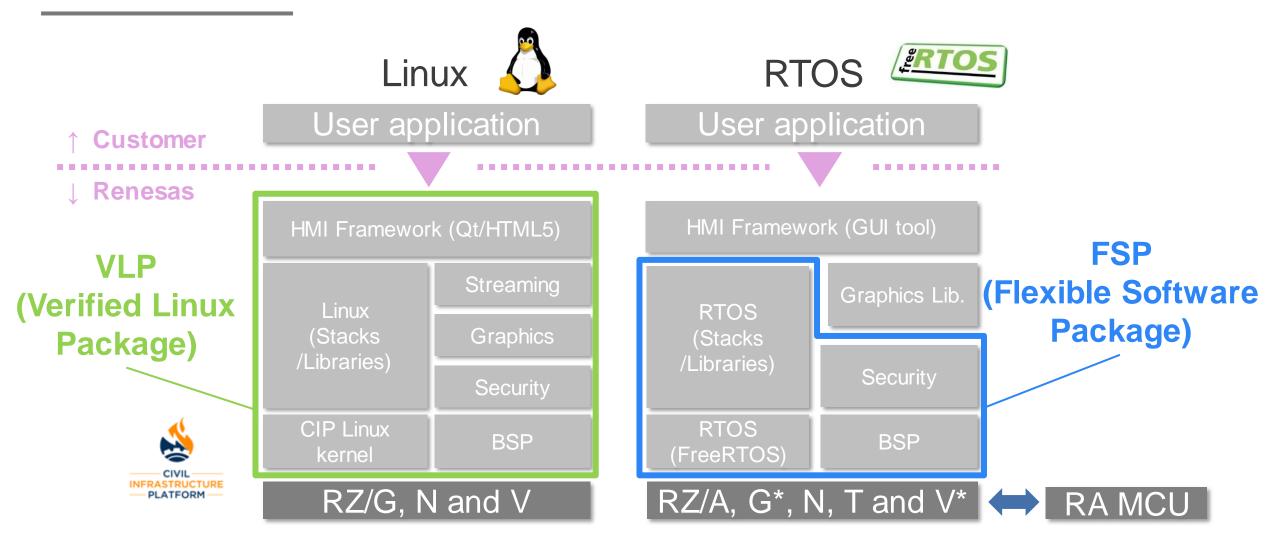






RZ FAMILY SOFTWARE SCALABILITY FOR LINUX & RTOS

"VLP" FOR LINUX & "FSP" FOR RTOS SUSTAIN SW SCALABILITY ACROSS MCU AND MPU

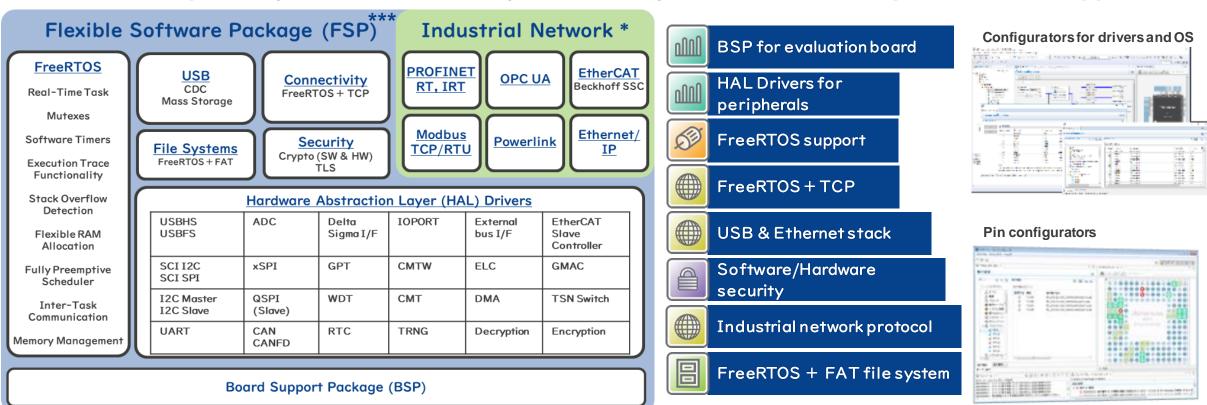


CIP: Civil Infrastructure Platform, BSP: Board Support Package

*: Multi OS solution which Cortex-M core operates with RTOS

FLEXIBLE SOFTWARE PACKAGE (FSP)

RZ/A, RZ/G**, RZ/N, RZ/T and RZ/V** supports Flexible Software Package (FSP) and Smart Configurator, which has compatibility with RA MCU family. Additionally, industrial network protocols are supported.



^{*:} RZ/N and RZ/T only. Supported functions or specifications will be extended in phase. Industrial network protocol stacks are provided by Renesas or our partners

^{** :} Multi OS solution which Cortex-M core operates with FSP (RTOS). Please see next page for more information.

^{*** :} Supported device drivers and middleware differ depending on each product

VERIFIED LINUX PACKAGE (VLP)

For more details: Verified Linux Package | Renesas

Customers can use verified S/W including middleware necessary for various applications. Application development can be started from a stable operating environment

Verified Linux Package

Renesas carries out verification

Docker

• Easily turn on the container type virtualization

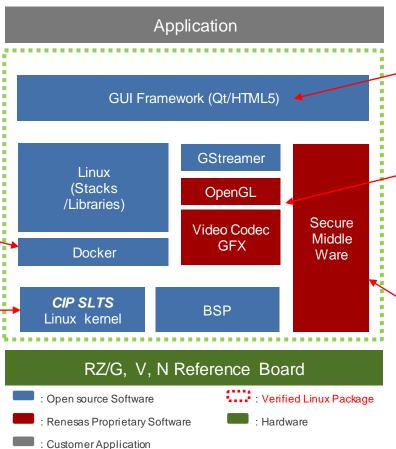


docker

CIP SLTS Kernel

- Civil Infrastructure Platform project
- 10+ years super long term support Reliability/Security/Real-time

(cf. LTS kernel: 2-6years support)







GUI Framework (for RZ/G, N)

- Qt application framework(Qt5.6.3)
- HTML5 application framework

Multimedia (for RZ/G, V)

- · H.264 Codec
- · H.265 Codec
- 2D/3D graphics

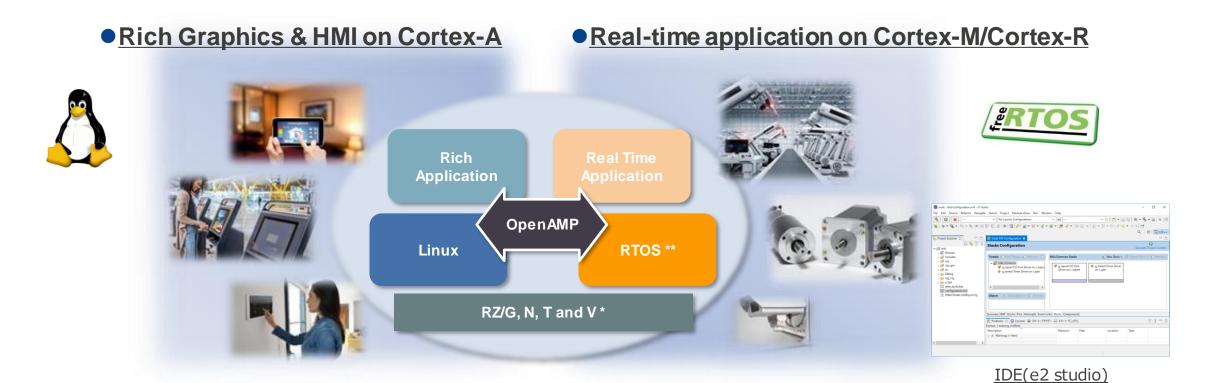
Security Software (for RZ/G, N)

- Secure boot
- Security communication
- Secure storage



MULTI OS SOLUTION

"LINUX + RTOS" SOLUTION REALIZES BOTH RICH HMI AND REAL-TIME OPERATION IN THE ONE SYSTEM



Providing OpenAMP for Inter-Processor communication

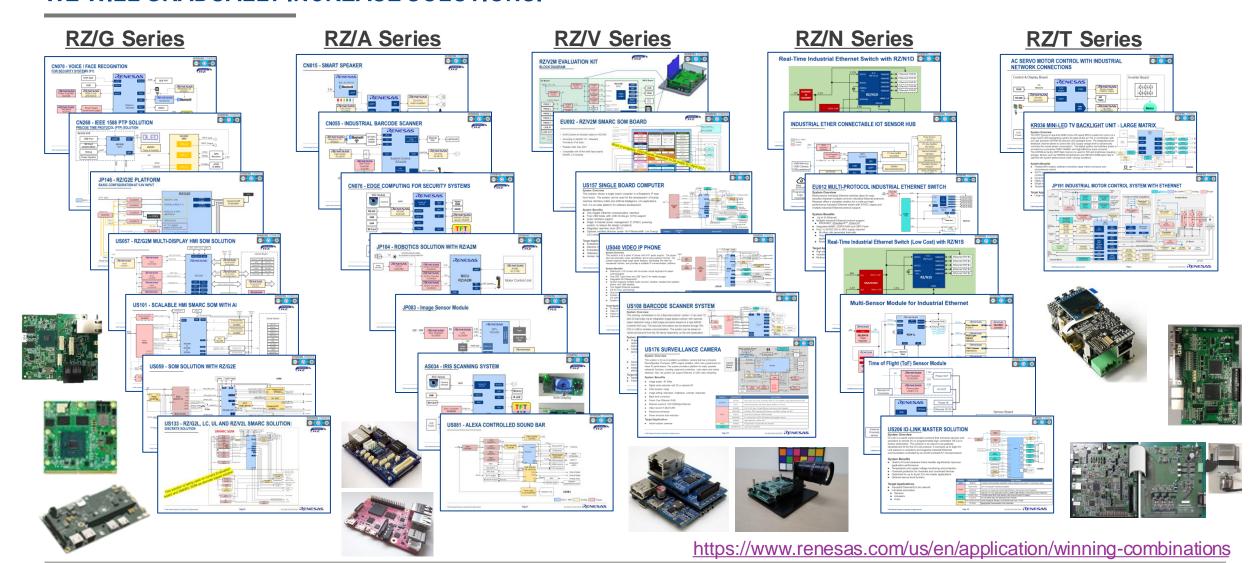
- · Provides OpenAMP environment and sample software for Linux and FreeRTOS
- * Supported devices: RZ/G2L, G2LC, G2UL, V2L, N1D, N2xx, T2xxx
- ** Supported device drivers and middleware differ depending on each product

Providing IDE (e2 studio) for Cortex-M/Cortex-R

- e2 studio(IDE) enables efficient software development.
- Providing software that complies with the Flexible Software Package (FSP)
 Drivers and middleware can be added and set using the e2 studio GUI.

MANY EXAMPLES OF SOLUTIONS USING RENESAS PRODUCTS

WINNING COMBINATION: WE ARE POSTING, BUT NOT ALL, SOLUTIONS FOR EACH FAMILY AND SERIES, AND WE WILL GRADUALLY INCREASE SOLUTIONS.





APPENDIX (PRODUCT COMPARISON)

RZ FAMILY FEATURES FOR VISION AI





High Performance

V2M

HW Accelerator

42fps (TinyYOLOv2, FP*) 55fps (Mobilenetv2,FP*)

DRP-AI Translator (Converted from ONNX)

MIPI CSI-2 x2

ISP (30fps/4K)

H.265 Enc/Dec

H.264 Enc/Dec

Pin Compatible V₂L

HW Accelerator

28fps (TinyYOLOv2,FP*) 39fps (Mobilenetv2, FP*)

DRP-Al Translator (Converted from ONNX)

MIPI CSI-2 x1

ISP (15fps/FHD)

H.264 Enc/Dec

Entry Class



CPU (x2)

18fps (Mobilenetv2, INT8*)

Linux+RTOS

ONNX, Tensorflow Lite, Tensorflow, Arm NN

G₂L

CPU (x2)

MIPI CSI-2 x1

H.264 Enc/Dec

18fps (Mobilenetv2, INT8*)

ONNX, Tensorflow Lite, Tensorflow, Arm NN

MIPI CSI-2 x1

Pin Compatible

G2UL

CPU (x1)

10fps (Mobilenetv2, INT8*)

ONNX, Tensorflow Lite, Tensorflow, Arm NN

MIPI CSI-2 x1

Linux

MIPI is a registered trademark of MIPI Alliance, Inc.

*FP: Floating Point, INT8: 8bit Integer

RZ FAMILY FEATURES FOR HMI





4	K		WXGA					
Pin Com	patible		Р	Pin Compatible				
G2H G2N	M G2N	G2E	G2E V2L G2L		G2LC	G2UL	A3x	A1 A2M
Cortex-A57&53 LPDDR4	3* * RZ/G2N has Cortex-A57 only (No Cortex-A53)	Cortex-A53 DDR3L	Cortex-A		Cortex-A55 DDR3L/4	Cortex DDR3L/4	(-A55 DDR3L/4 Octa	Cortex-A9 Int. RAM
H.265dec H.264enc/dec	,	H.265dec H.264enc/dec	- H264enc/	dec	-	-	Cotta	-
3DGfx		3DGfx	3DGfx		3DGfx	-		2DGfx
HDMI/LVDS MIPI CSI-2		LVDS MIPI CSI-2	MIPI DS MIPI CSI	-	MIPI DSI MIPI CSI-2	Para MIPI (LVDS MIPI CSI-2*

HDMI is a registered trademark of HDMI Licensing LLC.

Linux

*: RZ/A2M only

RTOS

Linux+RTOS

RZ FAMILY FEATURES FOR IOT EDGE





8 to 2 cores			2	cores		1 core				
P	Pin Compatible				Pin Compatible					**********
G2H	G2M	G2N	G2E	G2L	G2LC	G2UL	Five	A3x	A1 A2M	N2x
4xCortex-A57	2xCortex-A57	2xCortex-A57	-	-			-		-	-
4xCortex-A53	4xCortex-A53	-	2xCortex-A53	2xCort	ex-A55	1xCortex-A55	1xAX45MP	1xCortex-A55	1xCortex-A9	-
1xCortex-R7	1xCortex-R7	1xCortex-R7	1xCortex-R7	1xCorte	ex-M33	1xCortex-M33	-	-	-	1xCortex-R52
LPDDR4x64	LPDDR4x64	LPDDR4x32	DDR3Lx32	DDR3I	_/4x16	DDR3L	./4x16	DDR3L/4x16 Octa x8	Int. RAM	Int. RAM
PCIe	PCIe	PCle	PCle	-			-		-	Gbit Ether
Gbit Ether	Gbit Ether	Gbit Ether	Gbit Ether	Gbit I	Ether		Gbit Ether		Ether	3port Switch
USB3, USB2	USB3, USB2	USB3, USB2	USB3, USB2	US	B2		USB2		USB2	& TSN**
CAN	CAN	CAN	CAN	CA	۸N		CAN		CAN	USB2, CANFD
Security	Security	Security	Security	Sec	urity	Sec	urity	-	Security*	Security
	Lii	nux		Li	nux+RT(os	Linux		RTOS	

**TSN: Time Sensitive Networking

Under development/planning

*: RZ/A2M only

RZ FAMILY FEATURES FOR IOT EDGE (INDUSTRIAL AUTOMATION)





for H	ligh-ed	control	ler
(PLC/	motion	contro	ller)

for network controller (Gateway, remote I/O..)

for Realtime controller (Robot, servo, inverter)

N2xx

N₁D

N1S

N2x

N₁L



T2x

T1



4xCortex-A55 2xCortex-R52 LPDDR4x32

3xGMAC, **4port Switch**

TSN & IE*

Display out PCIe, USB3, USB2, CANFD

Security

2xCortex-A7 1xCortex-M3 DDR3x16 2xGMAC **5port Switch** IE*

Display out USB2

CAN

Security

1xCortex-A7 1xCortex-M3 Int. RAM 2xGMAC **5port Switch** TSN & IE*

> USB₂ CAN

Security

1xCortex-R52

Int. RAM

Gbit Ether

3port Switch TSN & IE*

USB2 CANFD

Security

1xCortex-M3

Int. RAM

2xGMAC 3port Switch

TSN & IE*

USB₂

CAN

Security

4xCortex-A55 2xCortex-R52

LPDDR4x32

3xGMAC,

3port Switch

TSN & IE*

PCIe, USB3,

USB2, CANFD, Encoder I/F

Security

2xCortex-R52 1xCortex-R52

Int. RAM Int. RAM

Gbit Ether Gbit Ether

3port Switch

TSN & IE*

USB2

CANFD.

Encoder I/F Security

T2xx

EtherCAT

USB2

CANFD.

Encoder I/F

Security

1xCortex-R52

1xCortex-R4 1xCortex-M3

Int. RAM Int. RAM

Gbit Ether Gbit Ether 3port Switch 3port Switch

TSN & IE*

USB2

USB2 CAN. CANFD,

Encoder I/F

Security Security

Linux+RTOS

RTOS / Baremetal

Linux+RTOS

RTOS/Baremetal

*IE: Multi-protocol Industrial Ethernet



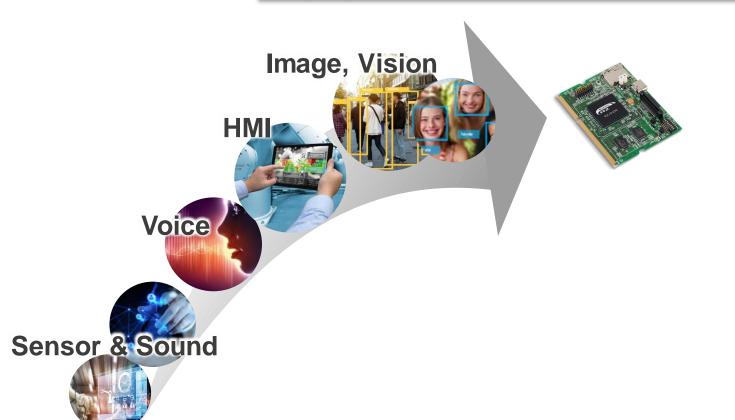
Under development/planning



APPENDIX (AI PART)

EVOLUTION OF AI APPLICATIONS

Deployable on Embedded MPU/MCU



Common use cases

Failure prediction

Reduce maintenance cost Improve service quality



Recognition

Objects, industrial automation, retails



















AI EVOLUTION ACCELERATES FURTHER

EXPANSION OF AI APPLICATION SCENES



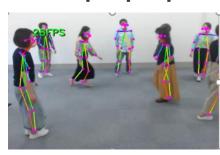


Human Behavior Centric

Worker's pose estimation



Multiple people



Abnormal/dangerous behavior detection as safety function







Al will sustain growing needs in diversified services involving human behavior

READYTOUSE

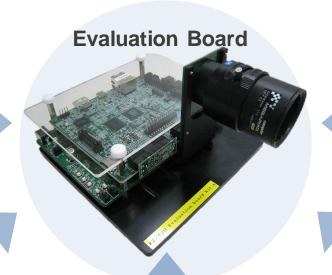
FULFILLING DEVELOPMENT SUPPORT ENVIRONMENT

■ Renesas prepare tools, document for easy evaluation, design, and development.



Al development support

 Convert AI model from ONNX to object code for DRP-AI





- Initial settings for ISP parameter
- Adjustment to install product on field



System and software development support

- CIP Linux BSP
- ISP Support Package(multimedia software)
- DRP-Al Support Package



- Design guide for circuit
- Schematics of board
- Pattern information
- Parts list



Document-

Development support

- Datasheet
- User's Manual
- Guide for Al implementation
- API specification

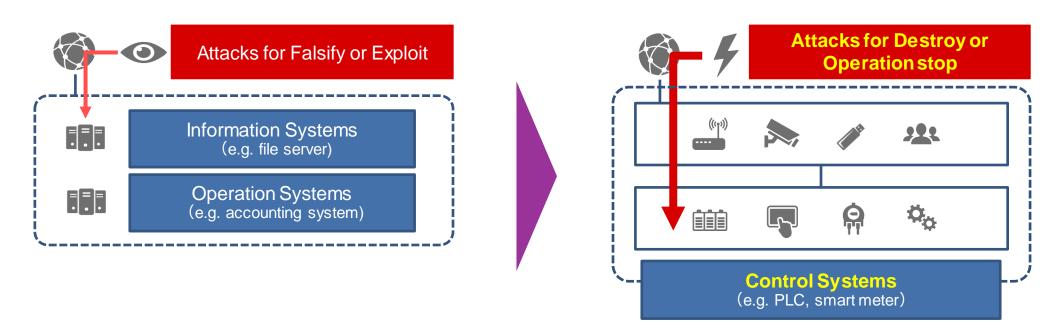
APPENDIX (SECURITY)

E2E SECURITY

THREATS OF CYBER-ATTACK GROWING IN THE ENDPOINT

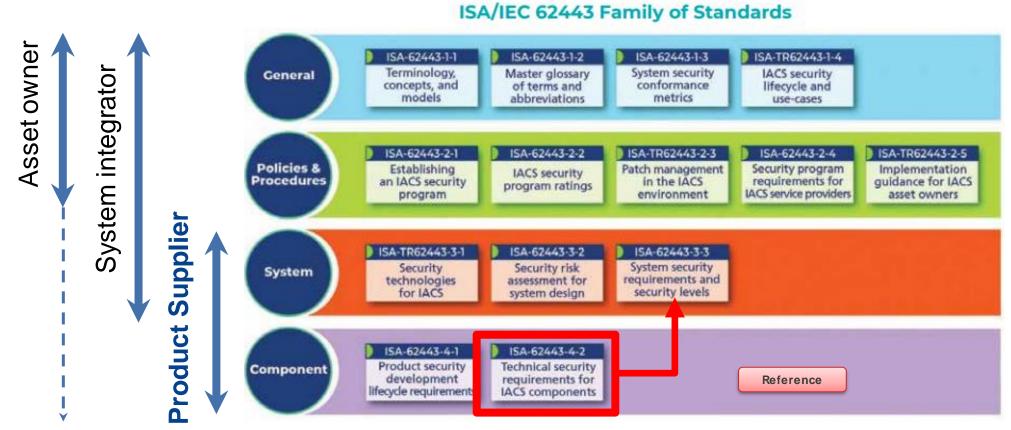
Targets are changing from information assets to control systems

- With the evolution of IoT, control systems built on stand-alone networks are becoming targets of cyber-attacks
- Attackers aim to generate physical impacts such as operation stops and system destruction
- Security management is shifting from protecting information assets to protecting the control system itself



E2E SECURITY

IEC 62443 TARGETS ALL VERTICALS AND PLAYERS



Cited from Quick Start Guide: An Overview of ISA/IEC 62443 Standards, Security of Industrial Automation and Control Systems

READY-TO-USE RENESAS SOLUTION



E2E SECURITY

Renesas RZ/G2 MPUs provide optimized security between cost vs IEC 62443-4-2 requirement

Sufficient, but not excessive, security features for SL-3 without impacting product cost

SUFFICIENT INSUFFICIENT EXCESSIVE RZ/G2 supports embedded No hardware security, An additional secure element security IP compliant for SL-3 Not compliant with SL-3 increases BOM cost Additional secure elements such as Trusted **BOM** cost Additional Platform Module approach would improve security, secure element while increasing product cost SW RZ/G2 RZ/G2 MPU offer effective security IP for SL-3 **ONLY** inside, no additional security HW needed Security strength SL-1

