

# DIEGO MORENO

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## EDUCATION

<b>University of Texas at Austin</b>	Bachelor of Science in Electrical Engineering <i>Overall GPA: 3.2</i>	Fall 2014
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## EXPERIENCE

<b>Cirrus Logic</b> – <i>Entry Level Validation Engineer</i> ; Austin, TX.	Jan 2014 - Present
<ul style="list-style-type: none"><li>• Conceptualized and developed framework software for multi-DUT validation tests</li><li>• Assisted in the development and execution of DSP validation tests</li><li>• Knowledgeable in multi-mode mixed-signal simulation tools, mixed-signal macro-modeling, and test-bench creation, organization, and automation</li></ul>	
<b>Cisco Systems</b> – <i>GDS Lab Services Intern</i> ; San Jose, California	Summer 2013
<ul style="list-style-type: none"><li>• Supported Cisco's RSPTG Engineering labs by configuring and deploying virtual machines, PDU's, switches, servers, and routers</li><li>• Aided with the ordering, shipping, and receiving of lab equipment</li></ul>	
<b>College Houses</b> – <i>IT &amp; Computer Facilities Manager</i> ; Austin, Texas	Summer 2012
<ul style="list-style-type: none"><li>• Configured and maintained computer lab and equipment at 21st Street Co-op</li><li>• Managed Ethernet network and DML for a 100+ student dormitory</li></ul>	

## PROJECTS

<b>Senior Design Project – SeizeAlert; University of Texas at Austin</b>	2014
Conceptualized, designed and developed a seizure detection and notification system for the Pebble smartwatch in the Android environment	
<b>Freelance Scripting for SXSW</b>	2014
Enhanced film documentation and logistics using JavaScript and Google Apps Script	
<b>Digital Design</b>	2011
Designed and programmed a piano in a computer keyboard with the use of a Xilinx Spartan board using VHDL programming	
<b>Echelon NodeBuilder Serial Communication</b>	2011
Controlled a network of Echelon NodeBuilders through serial communication using a Neuron C based program	

## PROFESSIONAL DEVELOPMENT

<b>Alpha Lambda Delta &amp; Phi Eta Sigma</b>	Present
Honor societies for students who obtained and maintained 3.5 or higher GPA and are in the top 20% of their class	

## ADDITIONAL INFORMATION

### Excellent Writing and Communication Skills

**Test/measurement:** Signal generators, oscilloscopes, digital power analyzers, soldering

**Assembly languages:** TI TMS320C6700 DSP, LC-3B ISA

**High-Level languages:** C, Java, JavaScript, HTML, CSS, Google Apps Script

**Software development:** TI Code Composer Studio

**Algorithm development:** LabVIEW, MATLAB

**Systems simulated:** Software-defined radio

**Real-time implementation:** Voiceband transceiver

**Team collaboration:** GitHub, Tortoise SVN, Assembla, Confluence, Jira

**Spoken languages:** English, Spanish, French

**Interests:** Healthy Cooking, Soccer, Motorcycles, Reading