DIEGO MORENO

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To obtain an Electrical Engineering position where I can further utilize my knowledge and experience in the field while applying them for the benefit of the company

EDUCATION

Sept '09 - Present

BS, Electrical and Computer Engineering, Dec. 2014

GPA 3.43/4

University of Texas at Austin, Cockrell School of Engineering Relevant Coursework:

Intro to Embedded Systems, Linear Systems and Signals, Digital Systems Design, Circuit Theory, Computer Architecture, Digital Signal Processing, Algorithms, Intro to Automatic Control, Software Design/Implementation

WORK EXPERIENCE

Jan '13 - May '13

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& Aug '13 – Present

Entry-Level Validation Engineer, Cirrus Logic

- Assisted in the development and execution of DSP validation tests
- Became familiarized with multi-mode mixed signal simulation tools, mixed signal macro modeling, and test bench creation, organization, and automation

May '13 - Aug '13

GDS Lab Services Intern, Cisco Systems

- Supported the Cisco's RSPTG Engineering labs by configuring and deploying virtual machines, PDU's, switches, servers, and routers
- Aided with the ordering, shipping, and receiving of lab equipment

May '12 - Aug '12

IT & Computer Facilities Manager, College Houses

- Configured and maintained computer lab and equipment at 21st Street Co-op
- Managed Ethernet network and DML for a 100+ student dormitory

PROJECTS

Jan '11 - May '11

Digital Design

- Designed and programmed a piano in a computer keyboard with the use of a Xilinx Spartan board
- Key Skills: VHDL programming

Jan '11 – May '11

NodeBuilder Serial

- Controlled a Neuron C based program through serial communication and the use of Echelon NodeBuilders
- Key Skills: Neuron C programming

SKILLS

Test/measurement: Signal generators, oscilloscopes, digital power analyzers, soldering

Assembly languages: TI TMS320C6700 DSP, LC-3B ISA

High-Level languages: C, Java, HTML, CSS

Software development: TI Code Composer Studio Algorithm development: LabVIEW, MATLAB Systems simulated: Software-defined radio

Real-time implementation: Voiceband transceiver

Spoken languages: English, Spanish

MEMBERSHIPS

Oct '12 - Present

Alpha Lambda Delta & Phi Eta Sigma

• Honor societies for students who obtained and maintained 3.5 or higher GPA and are in the top 20% of their class

Employability Status: Full Employability