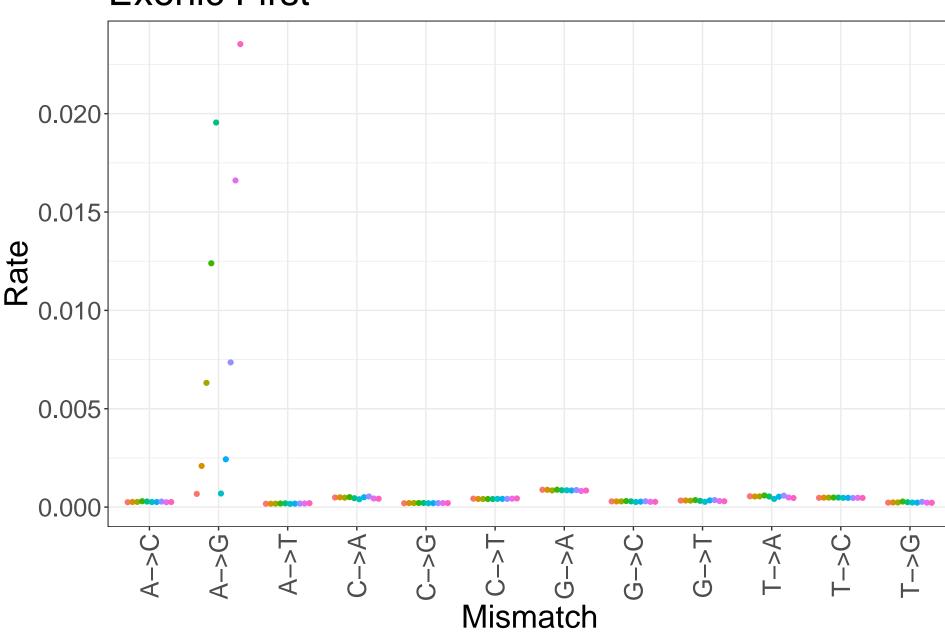
# A comparison of metabolic labeling and statistical methods to study genome-wide dynamics of RNA turnover Supplementary File 3

This file includes mismatch statistics for all nucleotide conversion protocols obtained with GRAND-SLAM (auto-generated plots). The first 96 pages include mismatch rates for all first and second reads mapped to exons, rates distinguishing sense and antisense reads (only antisense shown, same as overall rates), intronic reads (but intronic reads are not considered in the analysis), and rates stratified by mismatch type. The subsequent pages show mismatches for each position in a read for all samples combined, per protocol. The first 100 positions correspond to the first read, and positions 100 to 200 to the second read. Genomic bases are on the top, and the color encodes bases in the reads. Opposite=0 means sense, opposite=1 means antisense. Overlap=0 means outside of any doubly sequenced part, overlap=1 means within the doubly sequenced part. For more information, the reader is referred to https://github.com/erhard-lab/gedi/wiki/GRAND-SLAM

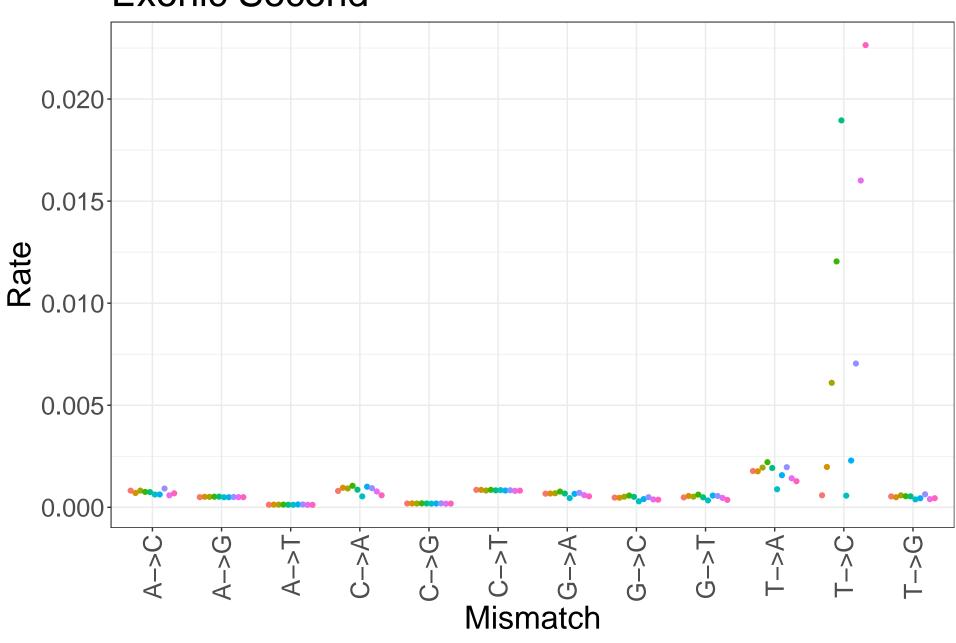
#### **Exonic First**



- 107307A\_SLAM\_0h\_no4sU
  107309A\_SLAM\_1h
  107311A\_SLAM\_2h
  107313A\_SLAM\_4h
  107315A\_SLAM\_8h
  107337B\_SLAM\_0h\_no4sU
  107331B\_SLAM\_1h
  107331B\_SLAM\_1h

- 107341B\_SLAM\_2h
  107343B\_SLAM\_4h
  107345B\_SLAM\_8h

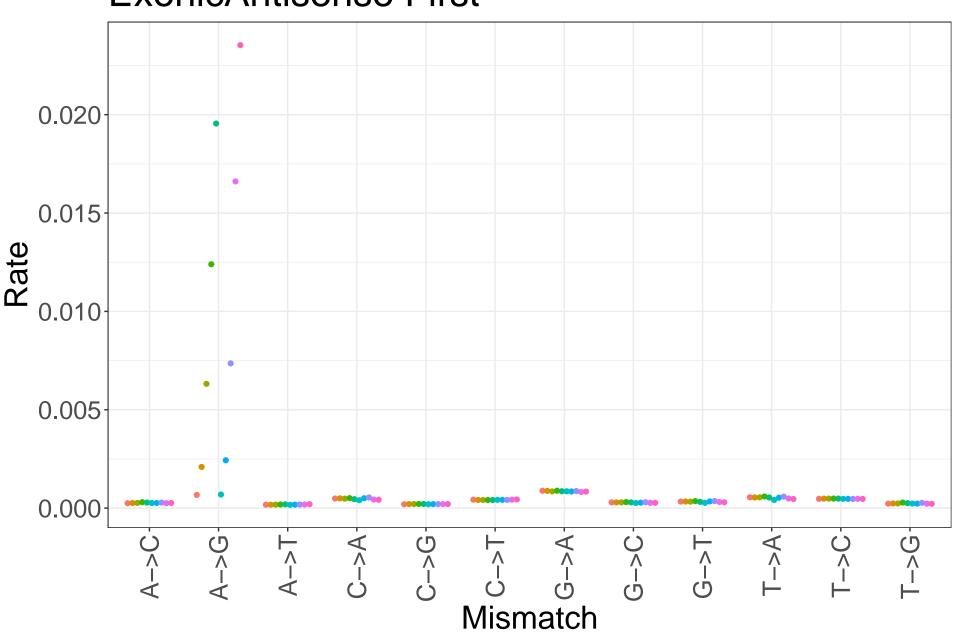
#### **Exonic Second**



- 107307A\_SLAM\_0h\_no4sU
  107309A\_SLAM\_1h
  107311A\_SLAM\_2h
  107313A\_SLAM\_4h
  107315A\_SLAM\_8h
  107337B\_SLAM\_0h\_no4sU
  107339B\_SLAM\_1h
  107341B\_SLAM\_2h

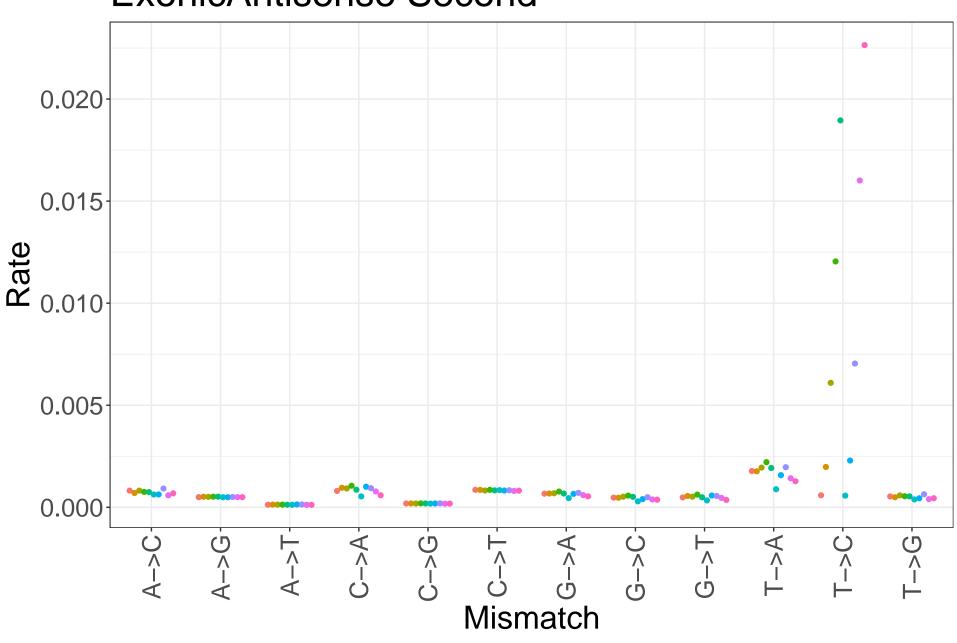
- 107341B\_SLAM\_2h 107343B\_SLAM\_4h 107345B\_SLAM\_8h

#### **ExonicAntisense First**



- 107307A\_SLAM\_0h\_no4sU
  107309A\_SLAM\_1h
  107311A\_SLAM\_2h
  107313A\_SLAM\_4h
  107337B\_SLAM\_8h
  107337B\_SLAM\_0h\_no4sU
  107341B\_SLAM\_1h
  107343B\_SLAM\_2h
  107345B\_SLAM\_8h

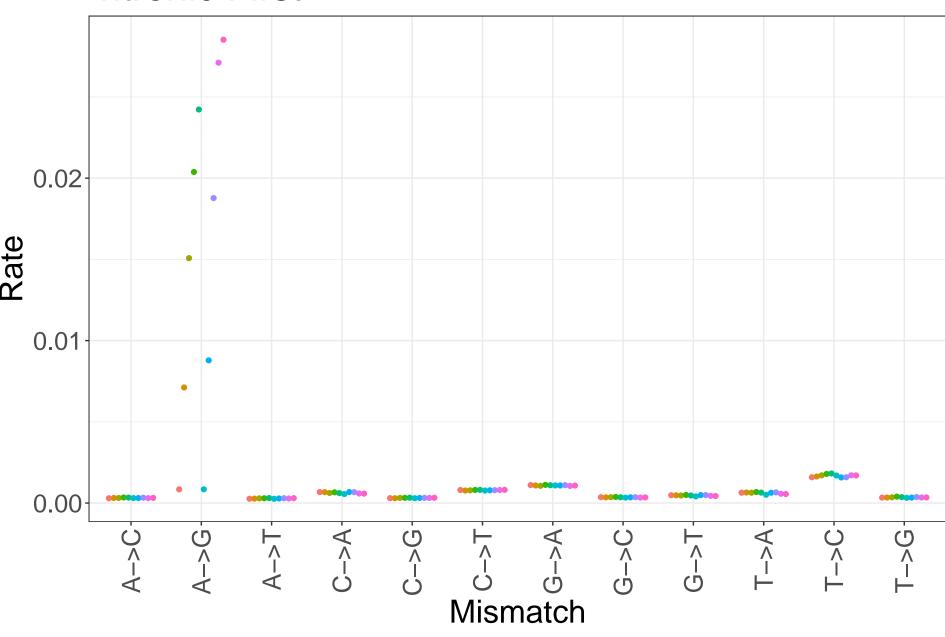
#### **ExonicAntisense Second**



- 107307A\_SLAM\_0h\_no4sU
  107309A\_SLAM\_1h
  107311A\_SLAM\_2h
  107313A\_SLAM\_4h
  107315A\_SLAM\_8h
  107337B\_SLAM\_0h\_no4sU
  107331B\_SLAM\_1h
  107331B\_SLAM\_1h

- 107341B\_SLAM\_2h 107343B\_SLAM\_4h 107345B\_SLAM\_8h

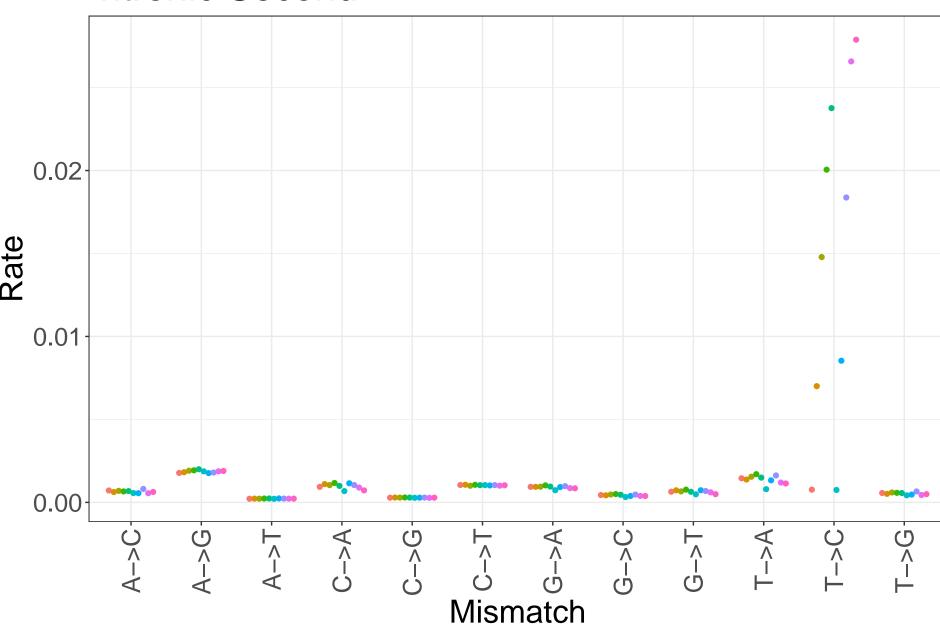
#### Intronic First



- 107307A\_SLAM\_0h\_no4sU107309A\_SLAM\_1h

- 107311A\_SLAM\_2h
  107313A\_SLAM\_4h
  107315A\_SLAM\_8h
- 107337B\_SLAM\_0h\_no4sU107339B\_SLAM\_1h
- 107341B\_SLAM\_2h
- 107343B\_SLAM\_4h
   107345B\_SLAM\_8h

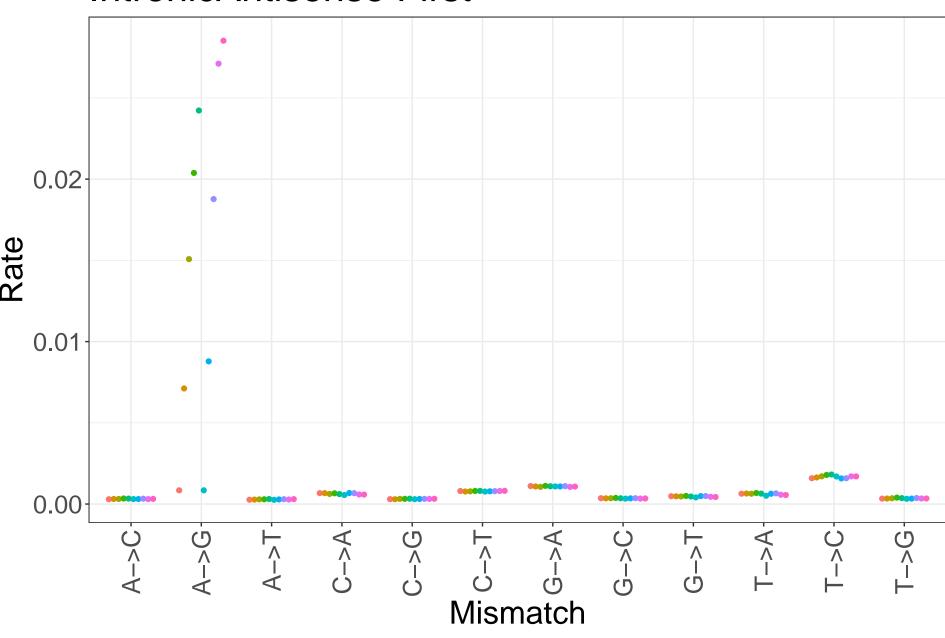
#### Intronic Second



- 107307A\_SLAM\_0h\_no4sU107309A\_SLAM\_1h

- 107311A\_SLAM\_2h
   107313A\_SLAM\_4h
   107315A\_SLAM\_8h
- 107337B\_SLAM\_0h\_no4sU107339B\_SLAM\_1h
- 107341B\_SLAM\_2h
- 107343B\_SLAM\_4h 107345B\_SLAM\_8h

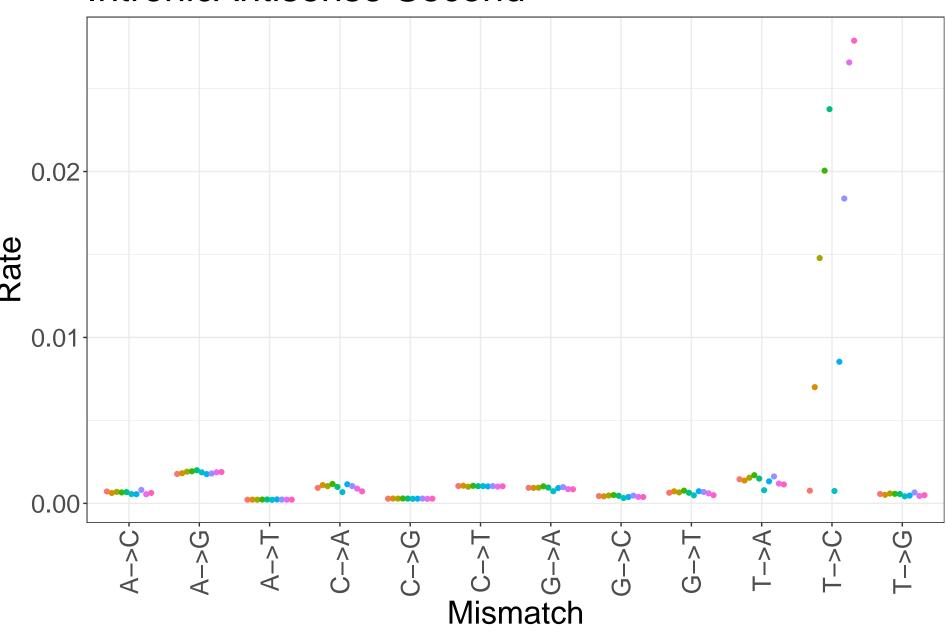
#### IntronicAntisense First



- 107307A\_SLAM\_0h\_no4sU107309A\_SLAM\_1h
- 107311A\_SLAM\_2h
  107313A\_SLAM\_4h
  107315A\_SLAM\_8h

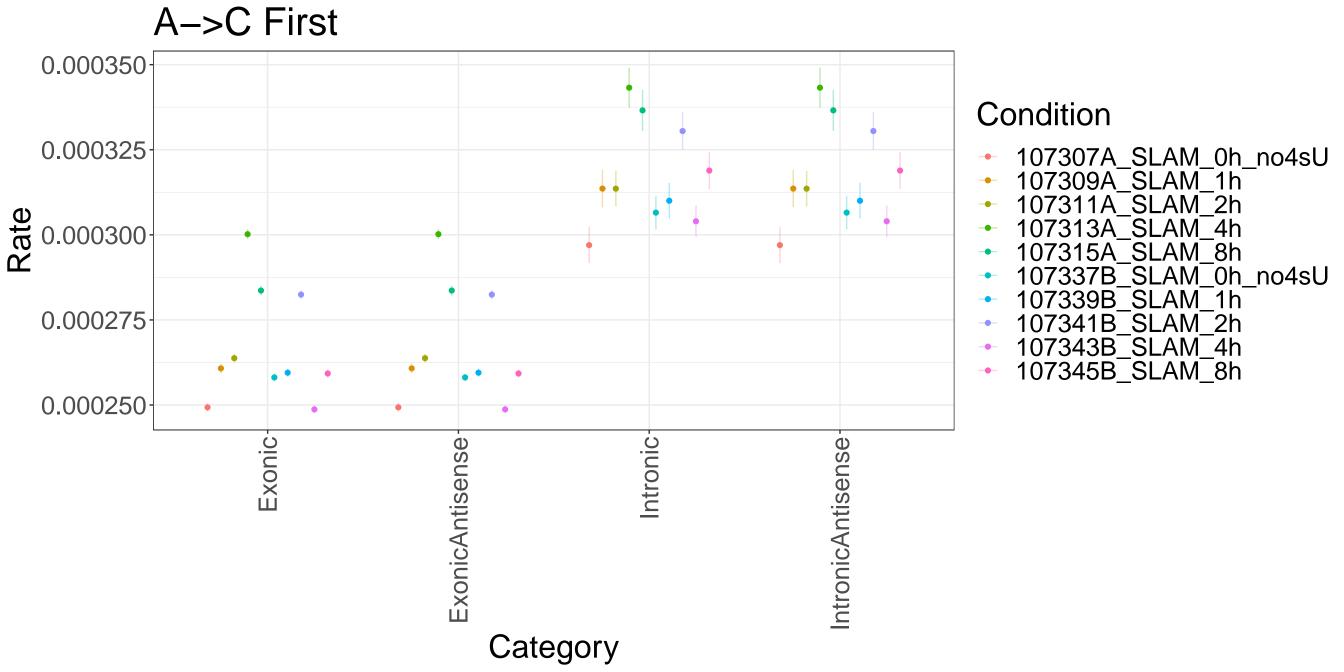
- 107337B\_SLAM\_0h\_no4sU107339B\_SLAM\_1h
- 107341B\_SLAM\_2h
- 107343B\_SLAM\_4h
- 107345B\_SLAM\_8h

#### IntronicAntisense Second

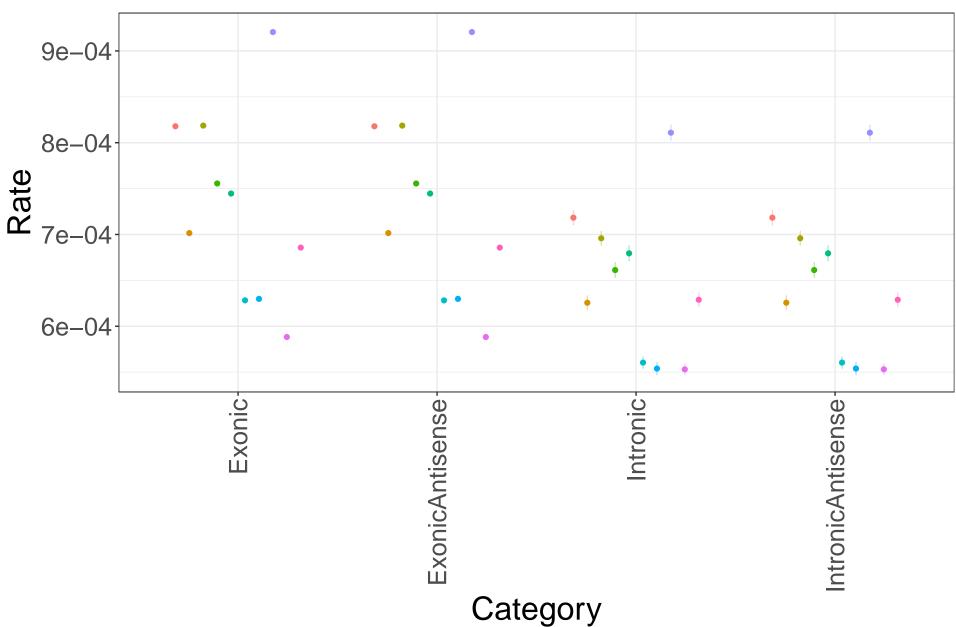


- 107307A\_SLAM\_0h\_no4sU107309A\_SLAM\_1h

- 107311A\_SLAM\_2h
   107313A\_SLAM\_4h
   107315A\_SLAM\_8h
- 107337B\_SLAM\_0h\_no4sU107339B\_SLAM\_1h
- 107341B\_SLAM\_2h
- 107343B\_SLAM\_4h
- 107345B\_SLAM\_8h



# A->C Second

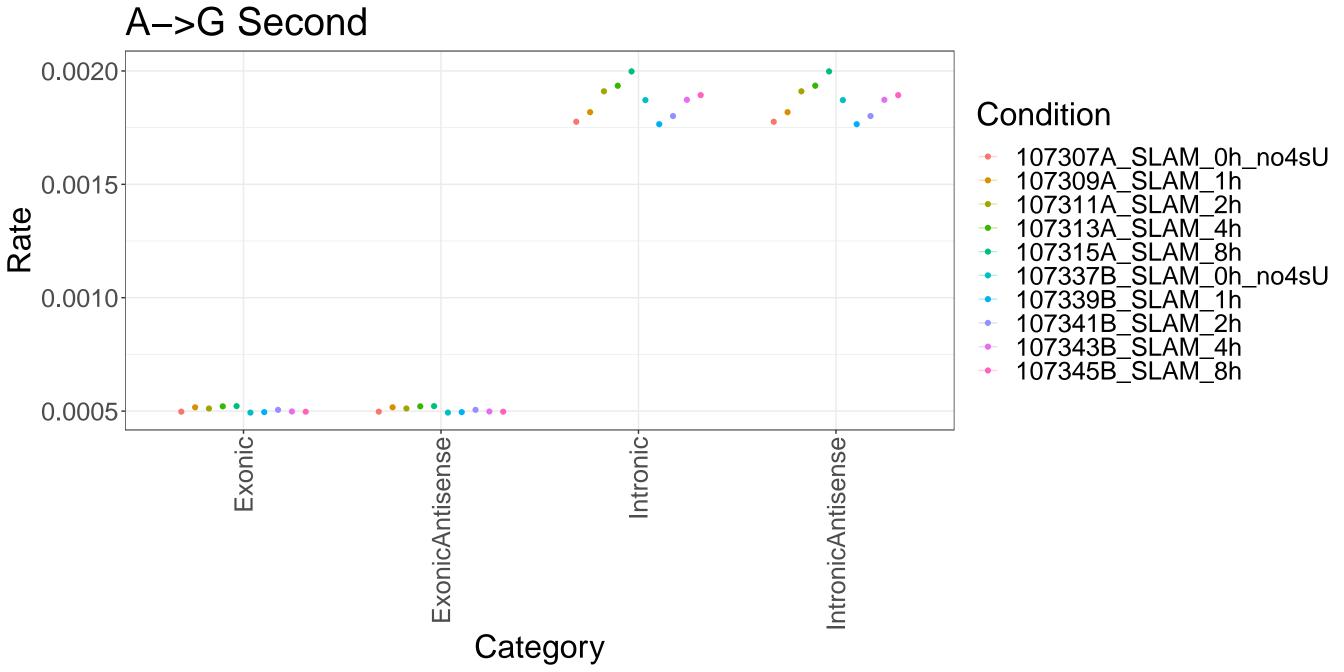


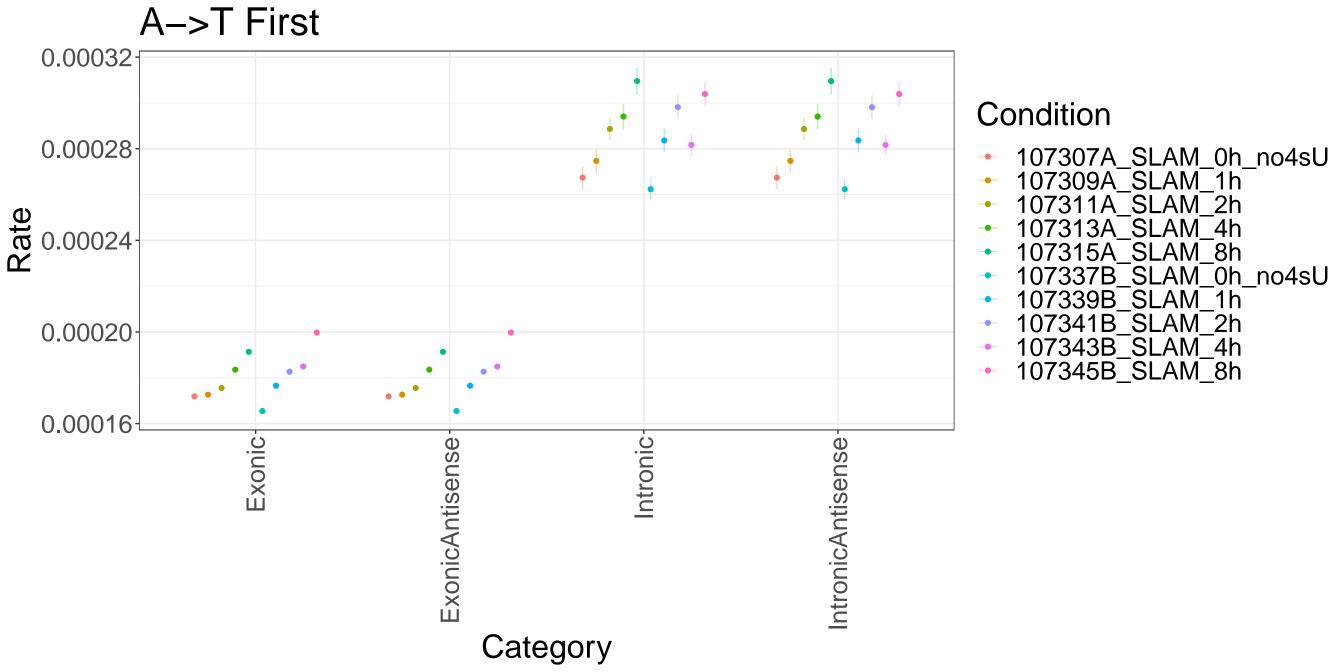
- 107307A\_SLAM\_0h\_no4sU 107309A\_SLAM\_1h

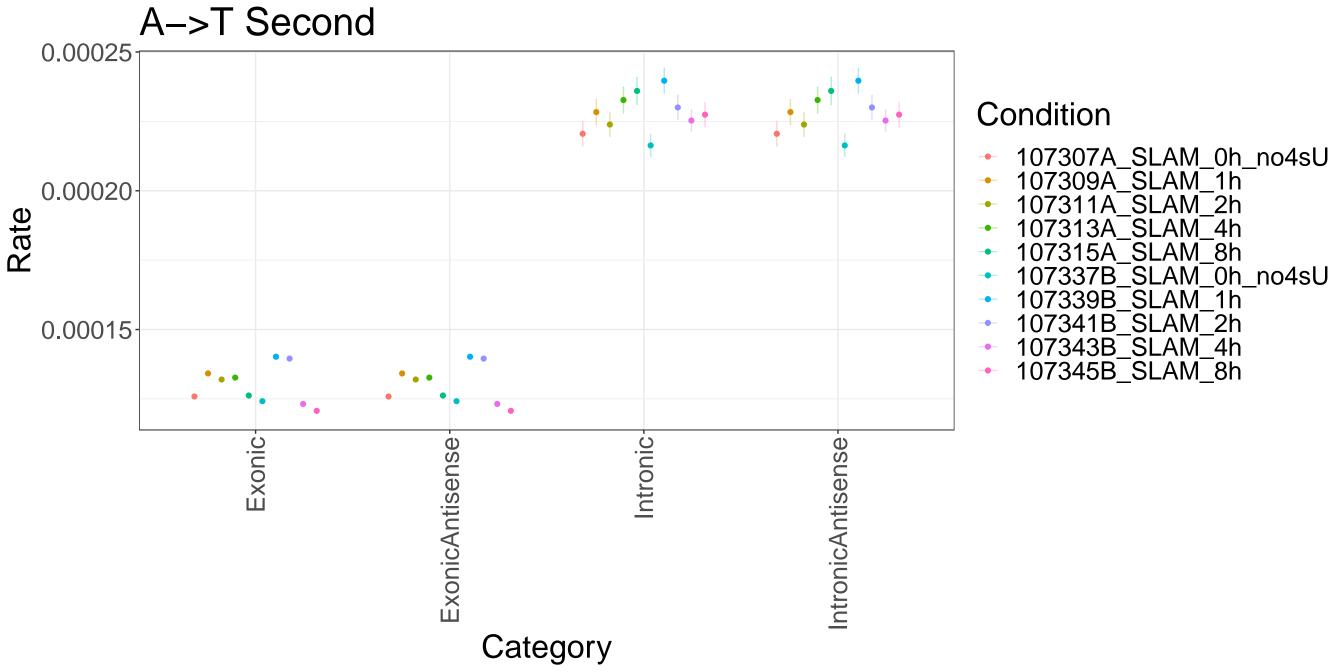
- 107311A\_SLAM\_2h 107313A\_SLAM\_4h 107315A\_SLAM\_8h 107337B\_SLAM\_0h\_no4sU 107339B\_SLAM\_1h
- 107341B\_SLAM\_2h 107343B\_SLAM\_4h
- 107345B\_SLAM\_8h

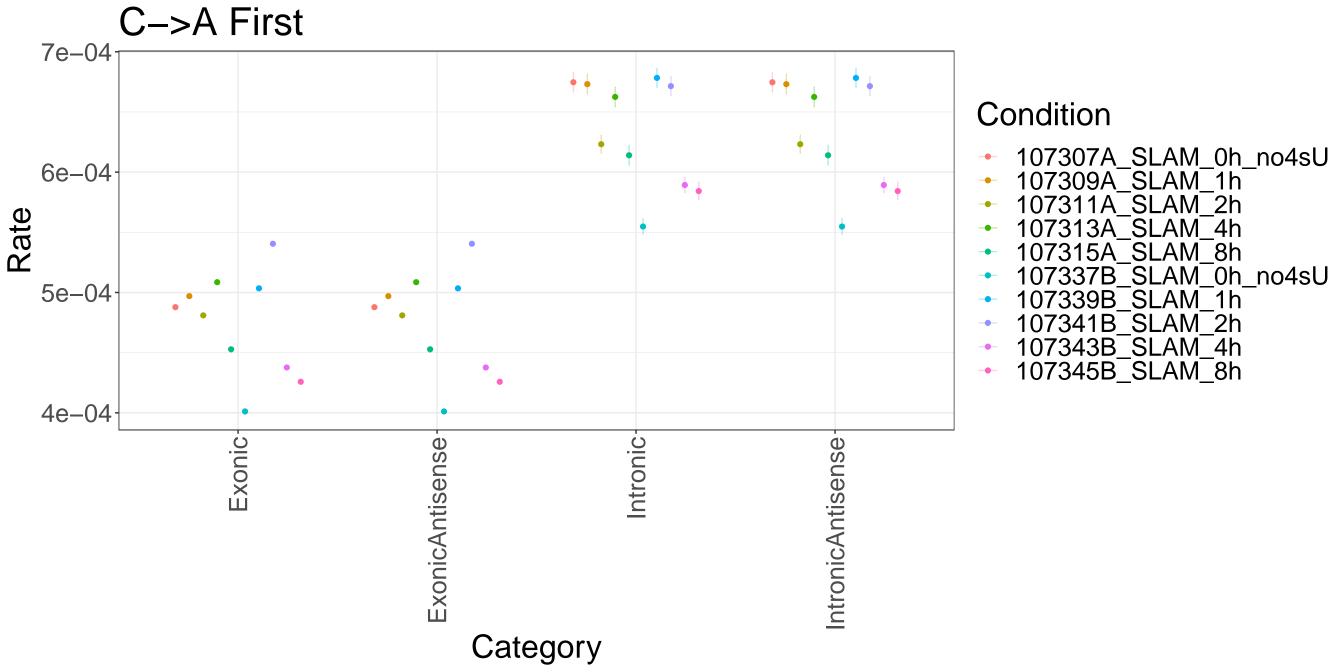
### A->G First Condition 107307A\_SLAM\_0h\_no4sU 107309A\_SLAM\_1h 0.02 107311A\_SLAM\_2h 107313A\_SLAM\_4h 107315A\_SLAM\_8h 107337B\_SLAM\_0h\_no4sU 107339B\_SLAM\_1h Rate 0.01 107341B\_SLAM\_2h 107343B\_SLAM\_4h 107345B\_SLAM\_8h 0.00 Intronic IntronicAntisense ExonicAntisense

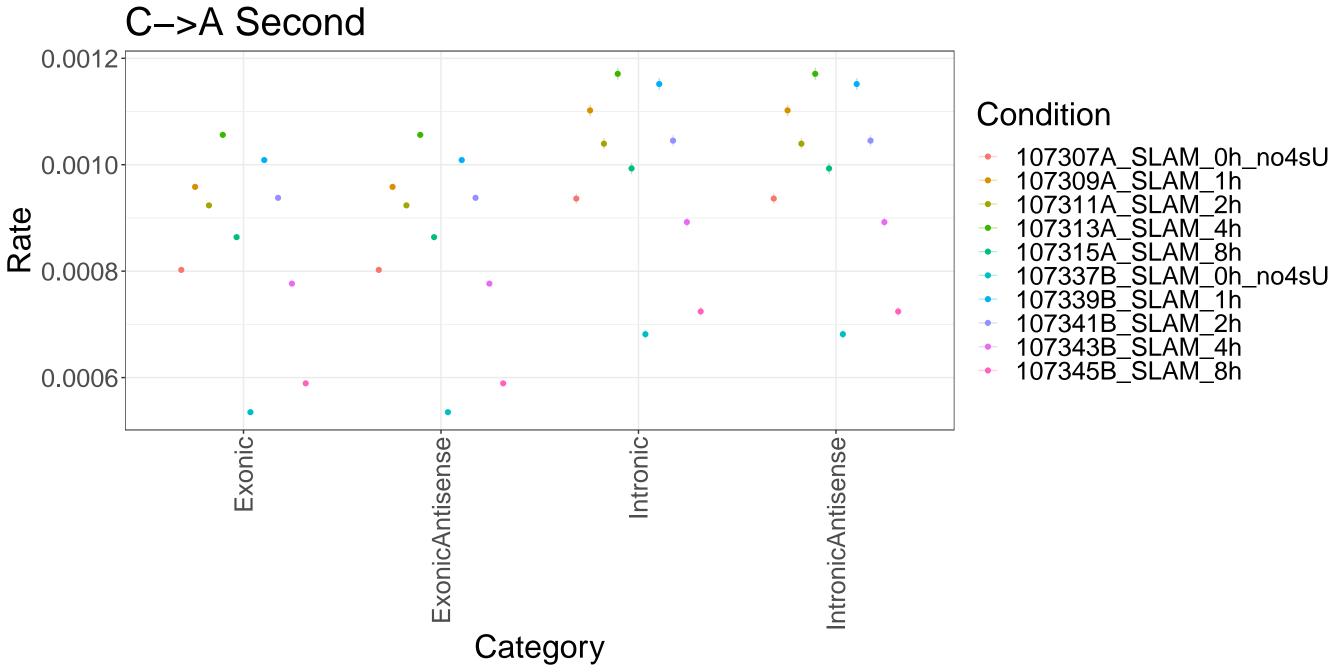
Category



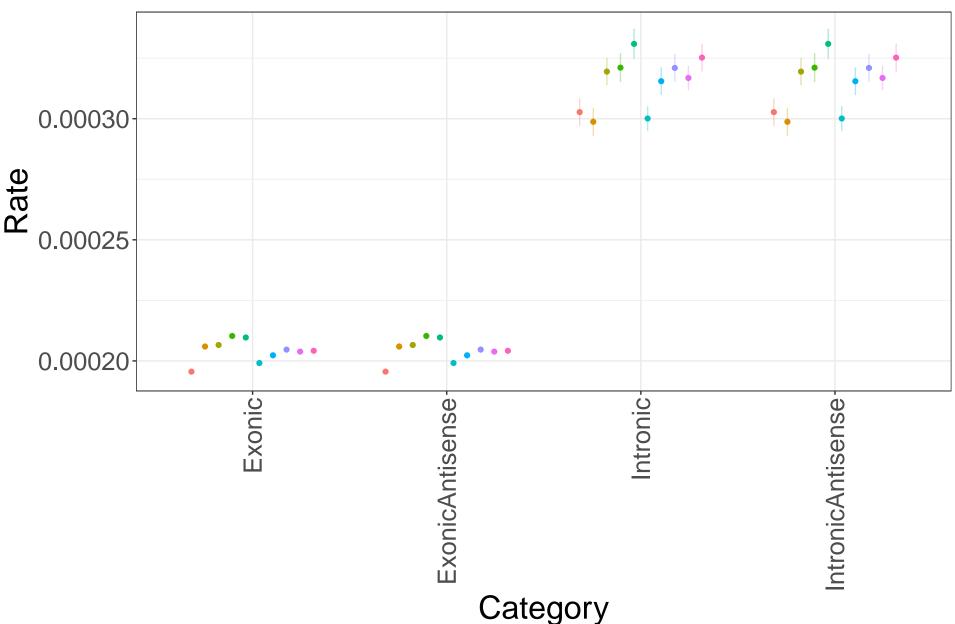








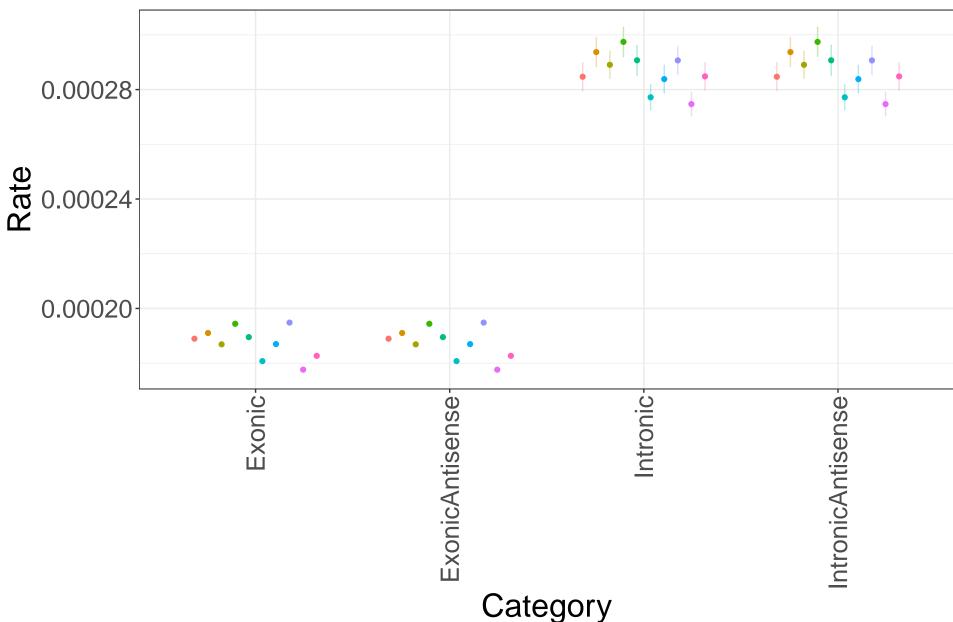
## C->G First



- 107307A\_SLAM\_0h\_no4sU 107309A\_SLAM\_1h 107311A\_SLAM\_2h 107313A\_SLAM\_4h 107315A\_SLAM\_8h 107337B\_SLAM\_0h\_no4sU 107339B\_SLAM\_1h

- 107341B\_SLAM\_2h 107343B\_SLAM\_4h
- 107345B\_SLAM\_8h

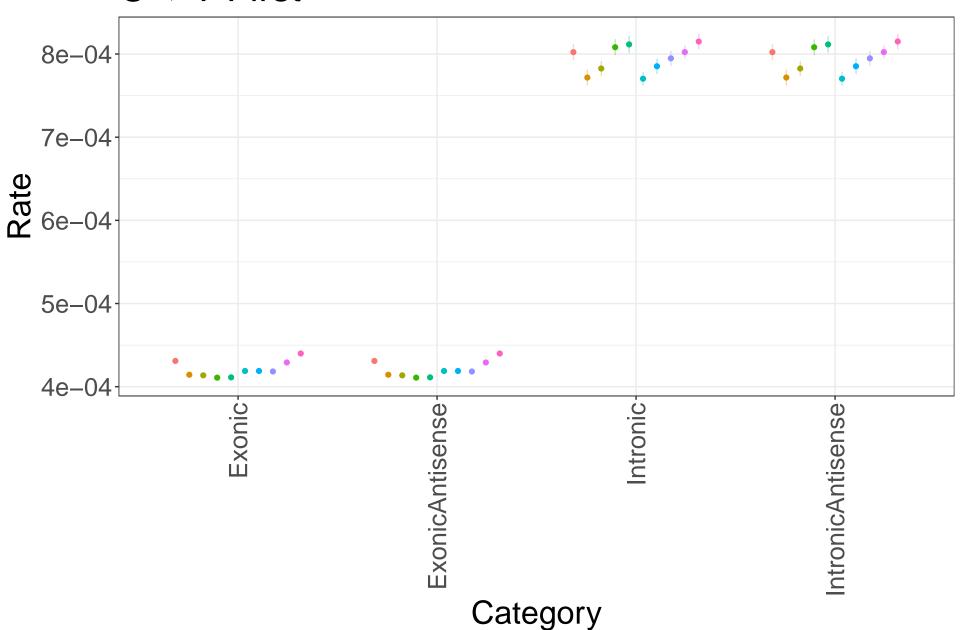
### C->G Second



- 107307A\_SLAM\_0h\_no4sU 107309A\_SLAM\_1h 107311A\_SLAM\_2h 107313A\_SLAM\_4h 107315A\_SLAM\_8h 107337B\_SLAM\_0h\_no4sU 107339B\_SLAM\_1h

- 107341B\_SLAM\_2h 107343B\_SLAM\_4h
- 107345B\_SLAM\_8h

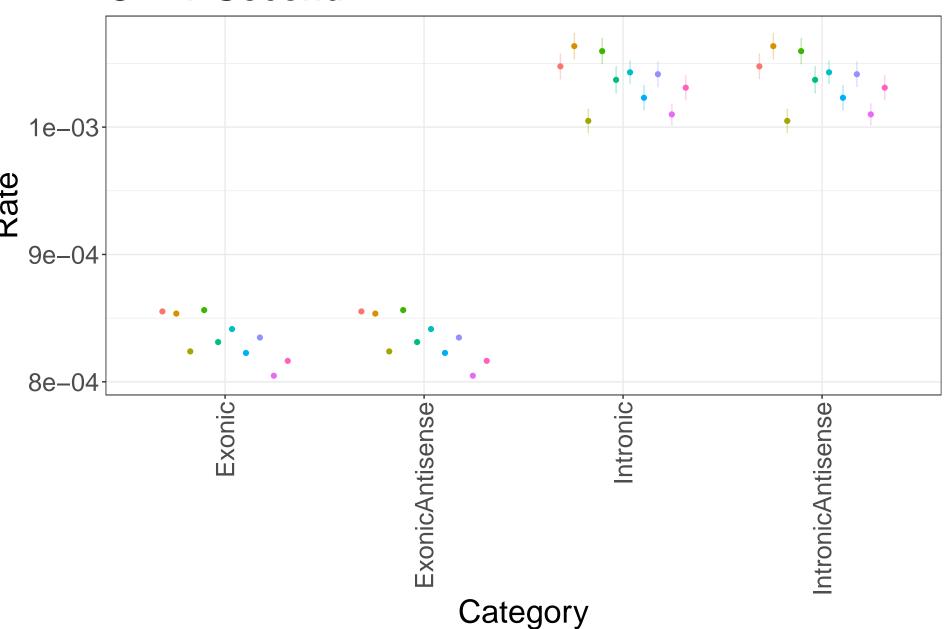
# C->T First



- 107307A\_SLAM\_0h\_no4sU 107309A\_SLAM\_1h

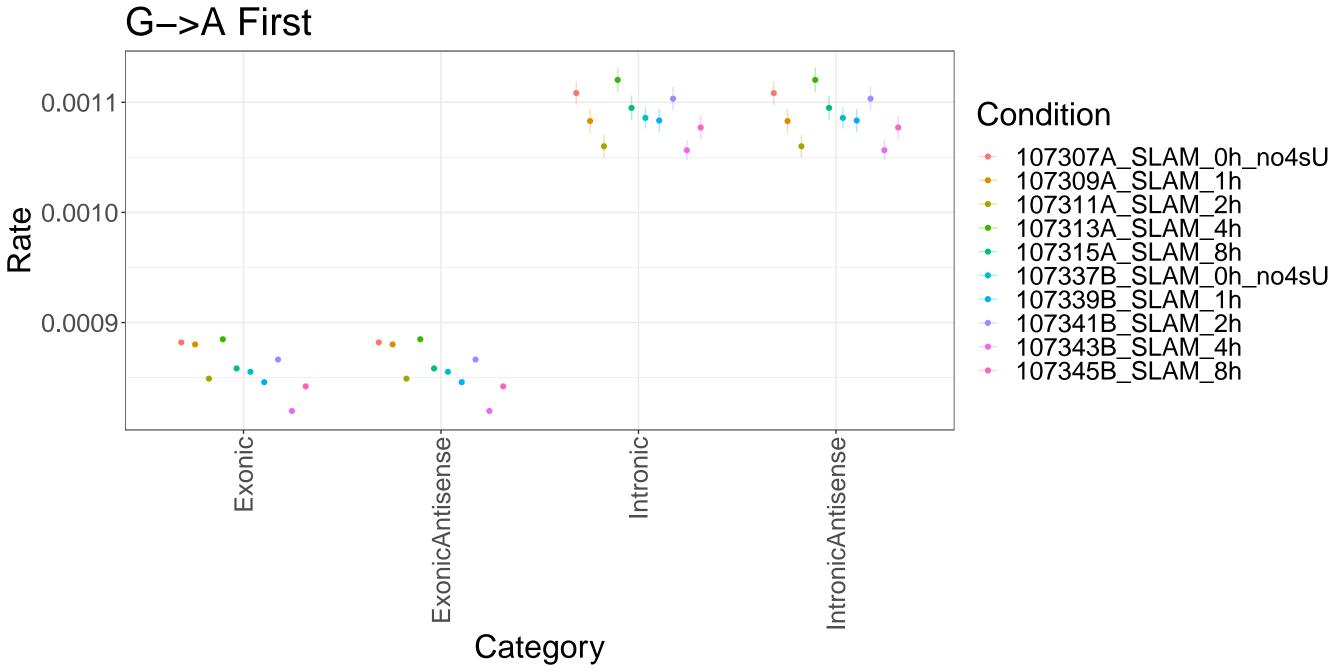
- 107303A\_SLAM\_11 107311A\_SLAM\_2h 107313A\_SLAM\_4h 107315A\_SLAM\_8h 107337B\_SLAM\_0h\_no4sU 107339B\_SLAM\_1h
- 107341B\_SLAM\_2h 107343B\_SLAM\_4h
- 107345B\_SLAM\_8h

#### C->T Second

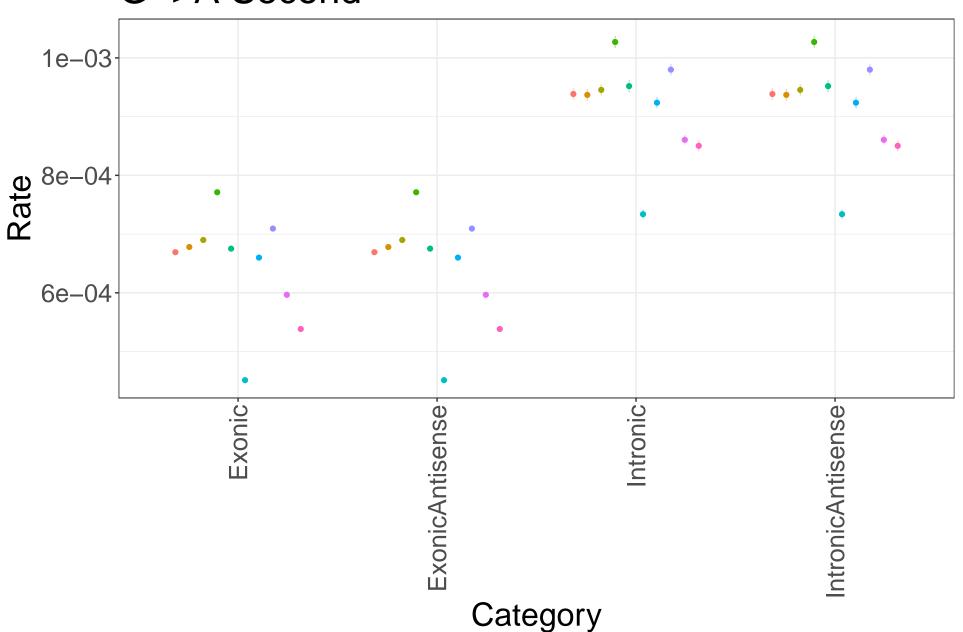


- 107307A\_SLAM\_0h\_no4sU 107309A\_SLAM\_1h

- 107311A\_SLAM\_2h 107313A\_SLAM\_4h 107315A\_SLAM\_8h 107337B\_SLAM\_0h\_no4sU 107339B\_SLAM\_1h
- 107341B\_SLAM\_2h 107343B\_SLAM\_4h
- 107345B\_SLAM\_8h



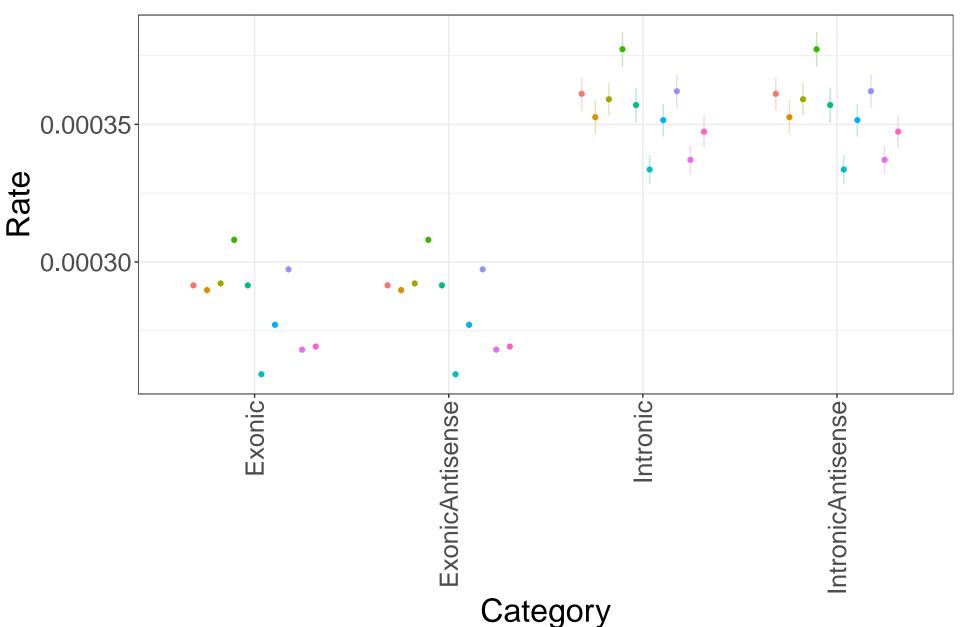
#### G->A Second



- 107307A\_SLAM\_0h\_no4sU 107309A\_SLAM\_1h

- 107311A\_SLAM\_2h 107313A\_SLAM\_4h 107315A\_SLAM\_8h
- 107337B\_SLAM\_0h\_no4sU 107339B\_SLAM\_1h
- 107341B\_SLAM\_2h 107343B\_SLAM\_4h
- 107345B\_SLAM\_8h

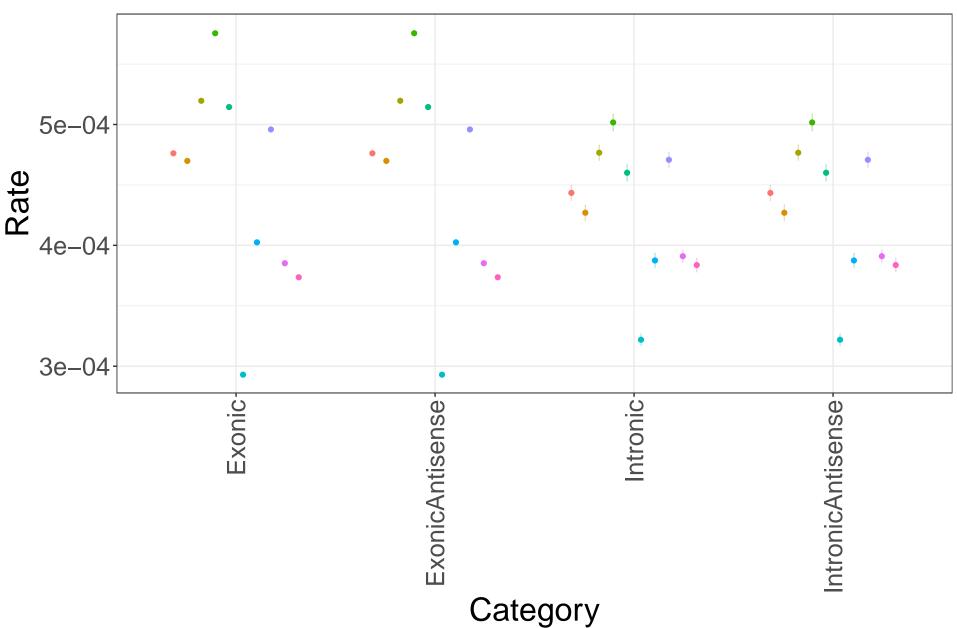
#### G->C First



- 107307A\_SLAM\_0h\_no4sU 107309A\_SLAM\_1h 107311A\_SLAM\_2h 107313A\_SLAM\_4h 107315A\_SLAM\_8h 107337B\_SLAM\_0h\_no4sU 107339B\_SLAM\_1h

- 107341B\_SLAM\_2h 107343B\_SLAM\_4h
- 107345B\_SLAM\_8h

#### G->C Second

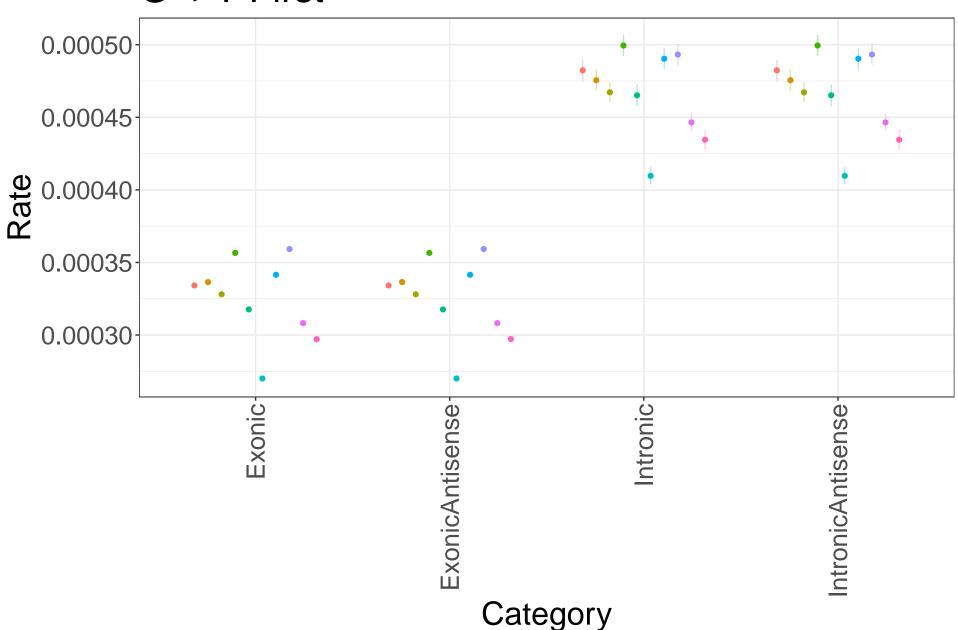


- 107307A\_SLAM\_0h\_no4sU 107309A\_SLAM\_1h

- 107311A\_SLAM\_2h 107313A\_SLAM\_4h 107315A\_SLAM\_8h 107337B\_SLAM\_0h\_no4sU 107339B\_SLAM\_1h

- 107341B\_SLAM\_2h 107343B\_SLAM\_4h
- 107345B\_SLAM\_8h

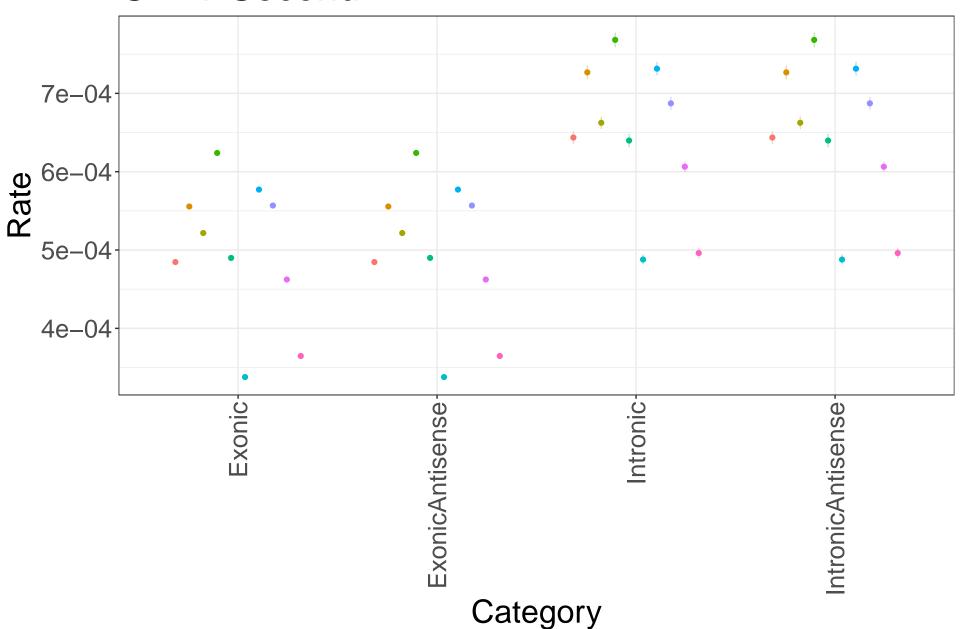
#### G->T First



- 107307A\_SLAM\_0h\_no4sU 107309A\_SLAM\_1h 107311A\_SLAM\_2h 107313A\_SLAM\_4h 107315A\_SLAM\_8h 107337B\_SLAM\_0h\_no4sU 107339B\_SLAM\_1h

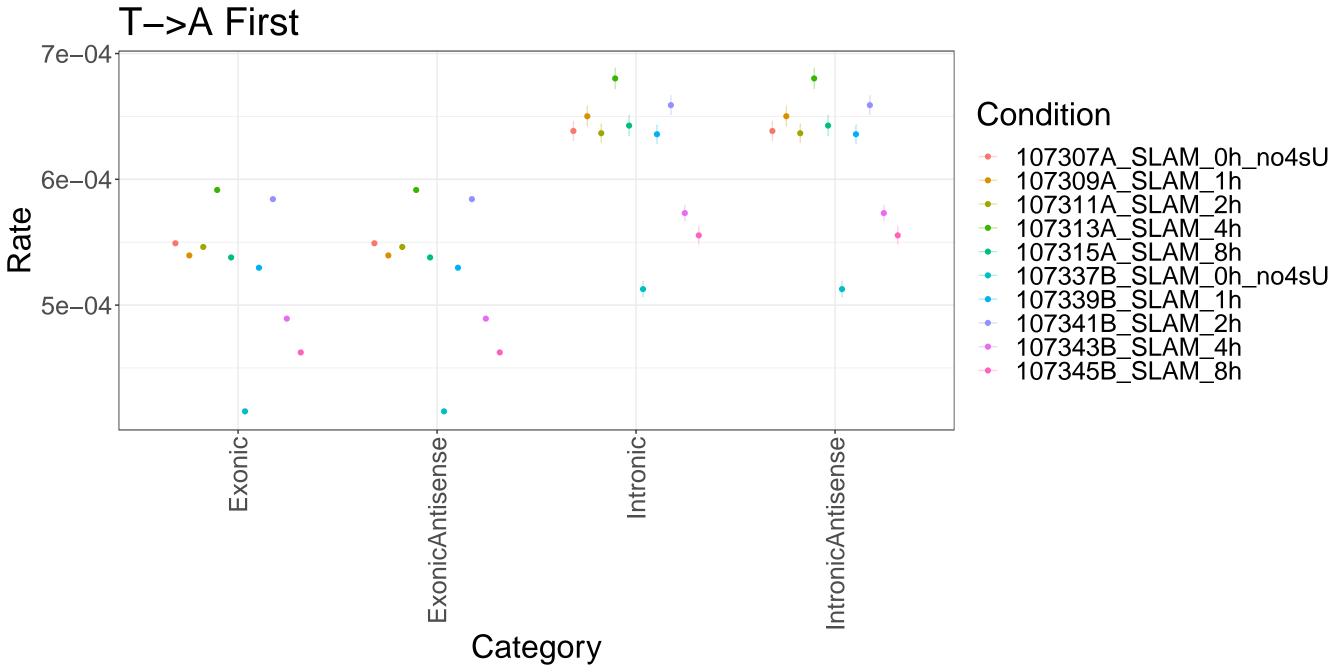
- 107341B\_SLAM\_2h 107343B\_SLAM\_4h
- 107345B\_SLAM\_8h

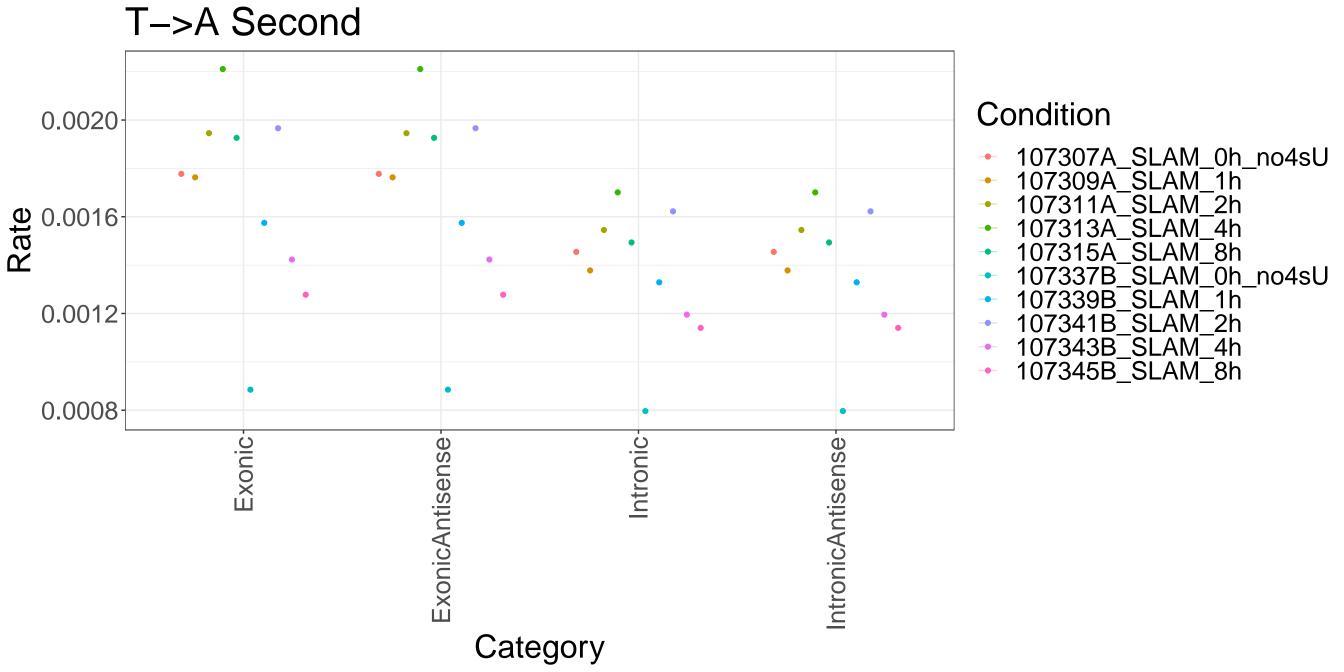
#### G->T Second

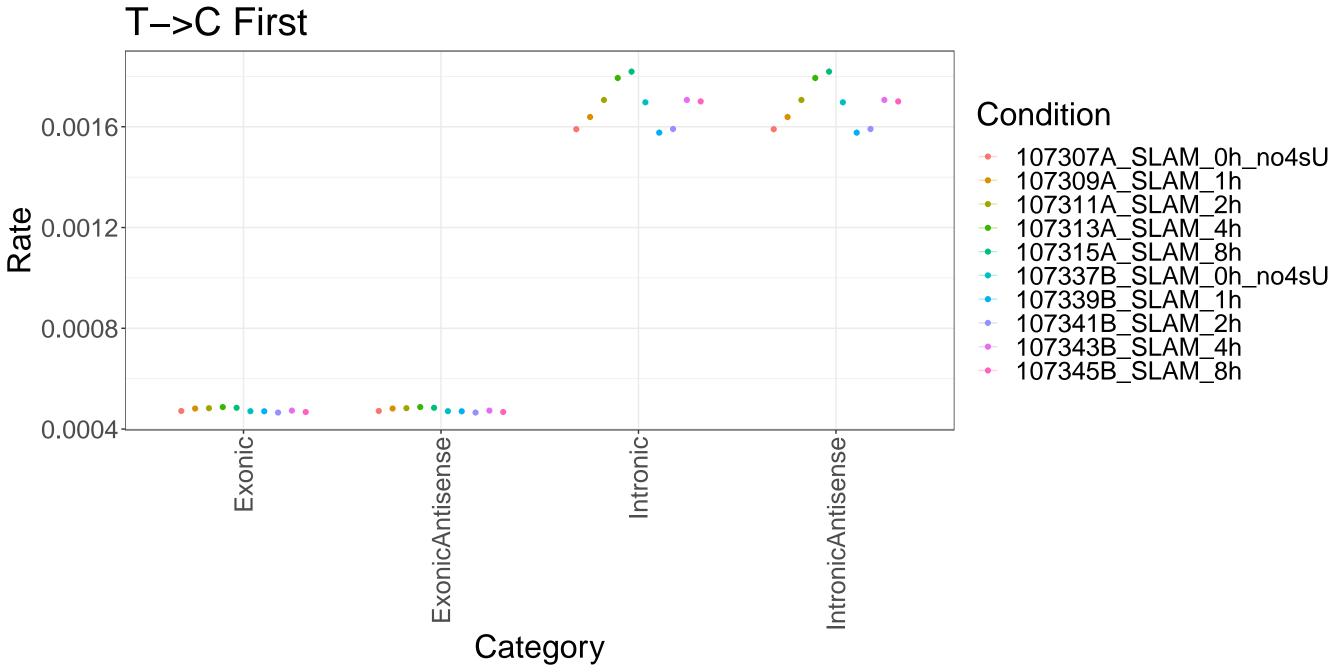


- 107307A\_SLAM\_0h\_no4sU 107309A\_SLAM\_1h

- 107311A\_SLAM\_2h 107313A\_SLAM\_4h 107315A\_SLAM\_8h 107337B\_SLAM\_0h\_no4sU 107339B\_SLAM\_1h
- 107341B\_SLAM\_2h 107343B\_SLAM\_4h
- 107345B\_SLAM\_8h

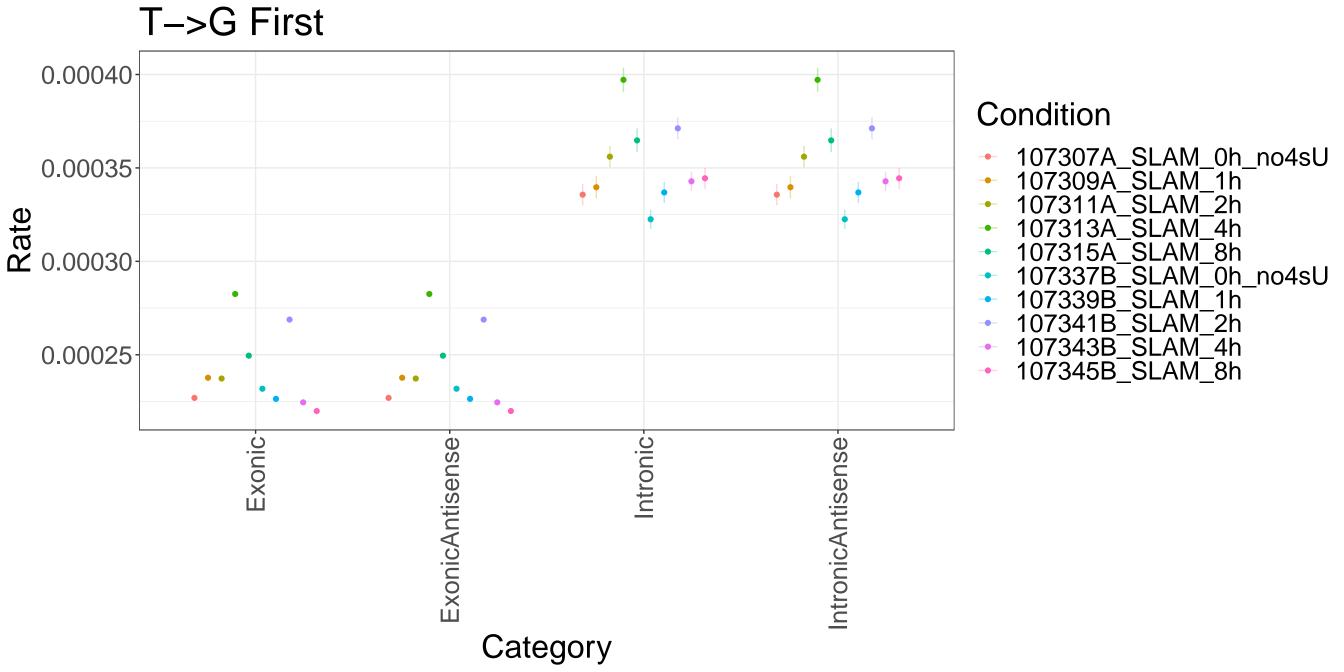




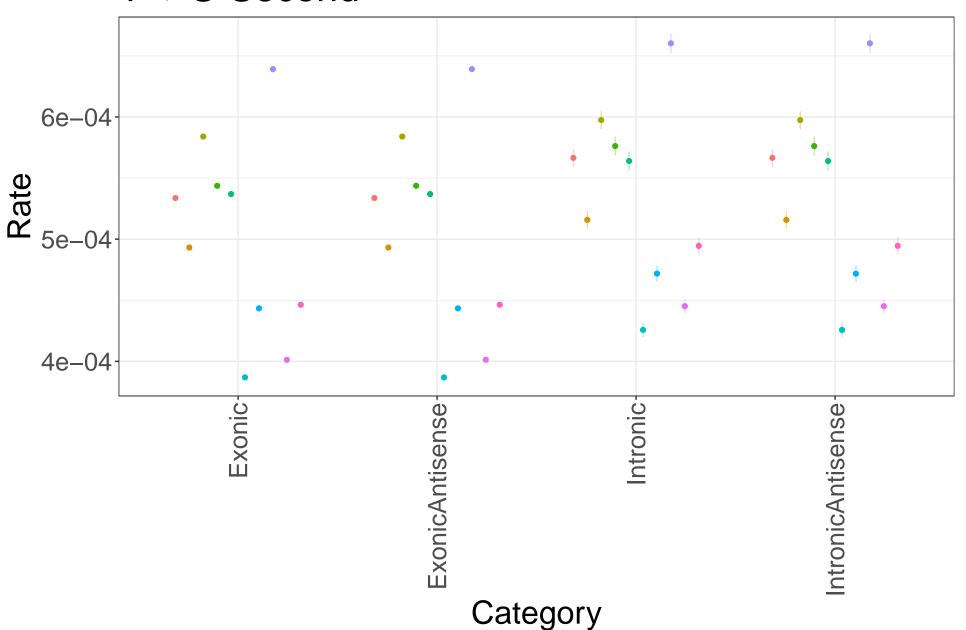


### T->C Second Condition 107307A\_SLAM\_0h\_no4sU 107309A\_SLAM\_1h 0.02 107311A\_SLAM\_2h 107313A\_SLAM\_4h 107315A\_SLAM\_8h 107337B\_SLAM\_0h\_no4sU 107339B\_SLAM\_1h Rate 0.01 107341B\_SLAM\_2h 107343B\_SLAM\_4h 107345B\_SLAM\_8h 0.00 Exonic-Intronic IntronicAntisense ExonicAntisense

Category



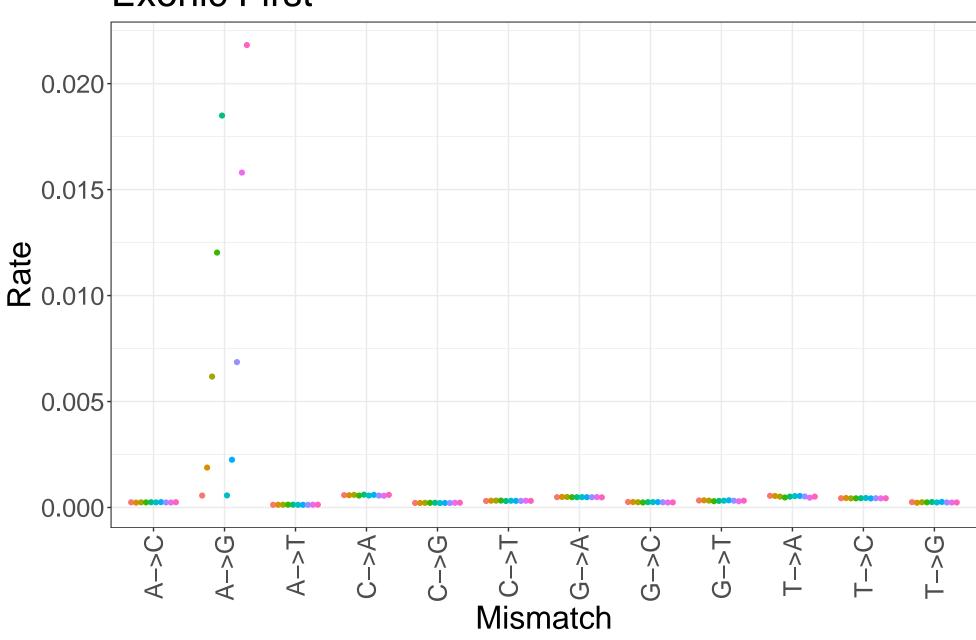
#### T->G Second



- 107307A\_SLAM\_0h\_no4sU 107309A\_SLAM\_1h

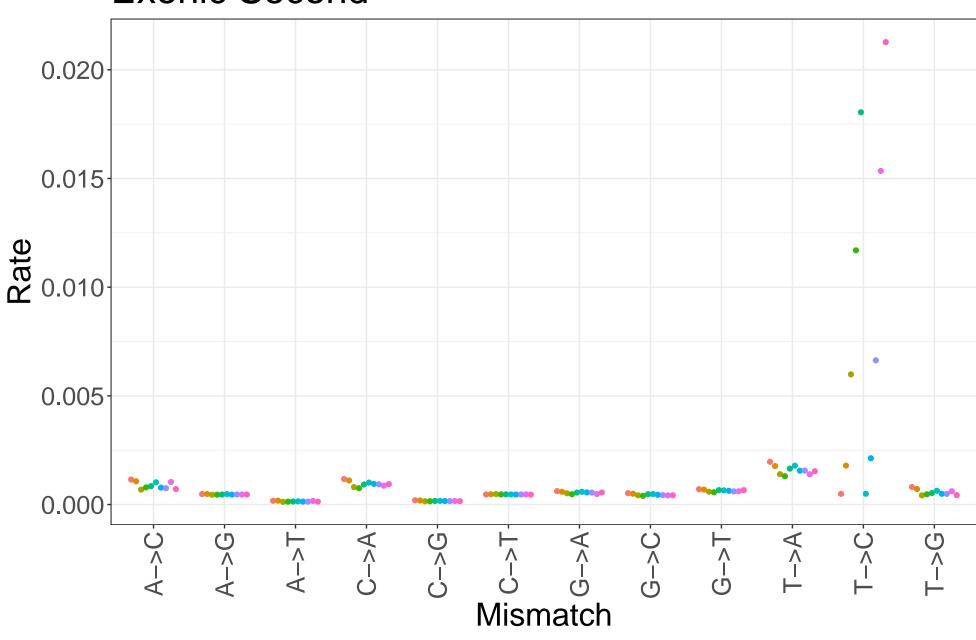
- 107311A\_SLAM\_2h 107313A\_SLAM\_4h 107315A\_SLAM\_8h 107337B\_SLAM\_0h\_no4sU 107339B\_SLAM\_1h
- 107341B\_SLAM\_2h 107343B\_SLAM\_4h
- 107345B\_SLAM\_8h

#### **Exonic First**



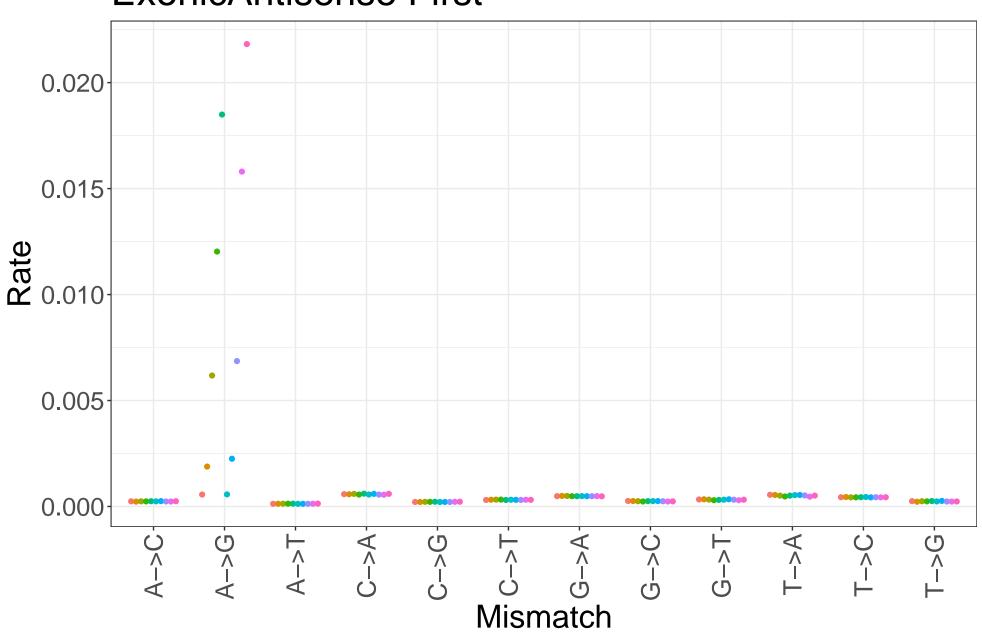
- 107317A\_TLS\_0h\_no4sU
  107319A\_TLS\_1h
  107321A\_TLS\_2h
  107323A\_TLS\_4h
  107325A\_TLS\_8h
  107347B\_TLS\_0h\_no4sU
  107349B\_TLS\_1h
  107351B\_TLS\_2h
- 107351B\_TLS\_2h 107353B\_TLS\_4h 107355B\_TLS\_8h

#### **Exonic Second**



- 107317A\_TLS\_0h\_no4sU
  107319A\_TLS\_1h
  107321A\_TLS\_2h
  107323A\_TLS\_4h
  107325A\_TLS\_8h
  107347B\_TLS\_0h\_no4sU
  107349B\_TLS\_1h
  107351B\_TLS\_2h
- 107351B\_TLS\_2h 107353B\_TLS\_4h 107355B\_TLS\_8h

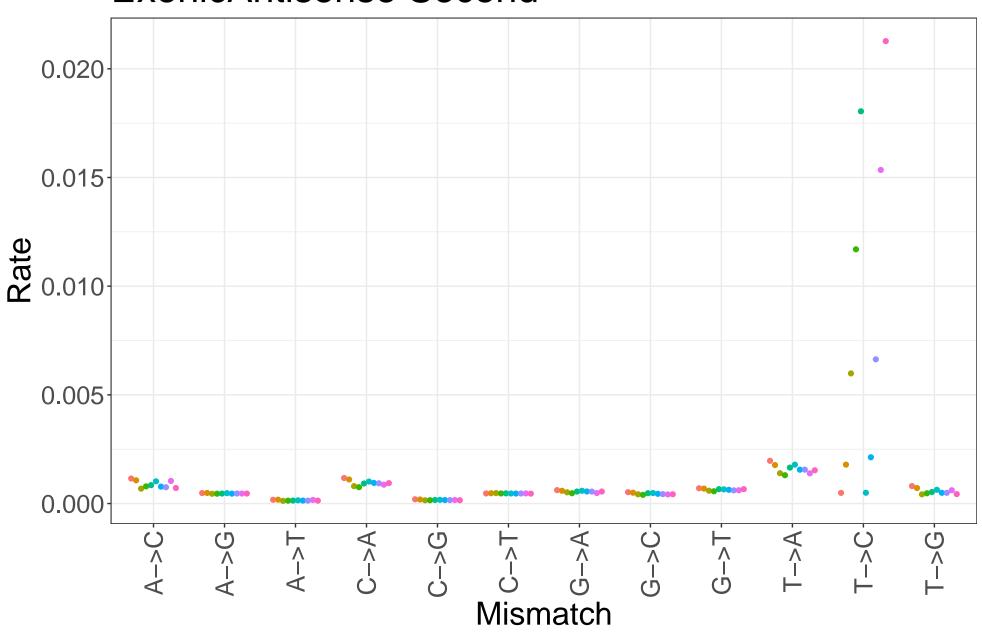
#### **ExonicAntisense First**



- 107317A\_TLS\_0h\_no4sU 107319A\_TLS\_1h 107321A\_TLS\_2h 107323A\_TLS\_4h 107325A\_TLS\_8h 107347B\_TLS\_0h\_no4sU 107349B\_TLS\_1h

- 107351B\_TLS\_2h 107353B\_TLS\_4h 107355B\_TLS\_8h

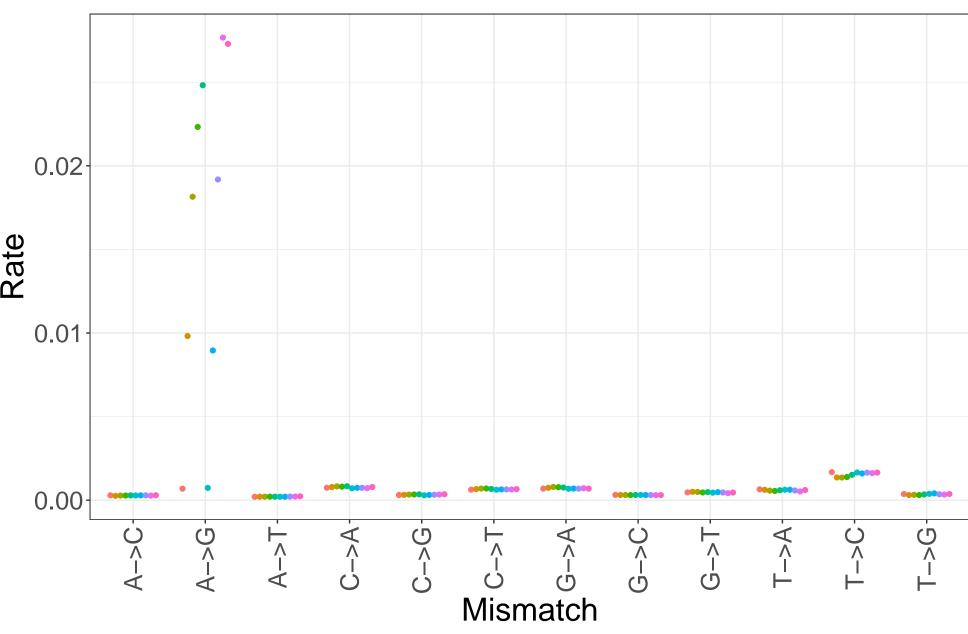
# **ExonicAntisense Second**



- 107317A\_TLS\_0h\_no4sU 107319A\_TLS\_1h 107321A\_TLS\_2h 107323A\_TLS\_4h 107325A\_TLS\_8h 107347B\_TLS\_0h\_no4sU 107349B\_TLS\_1h

- 107351B\_TLS\_2h 107353B\_TLS\_4h 107355B\_TLS\_8h

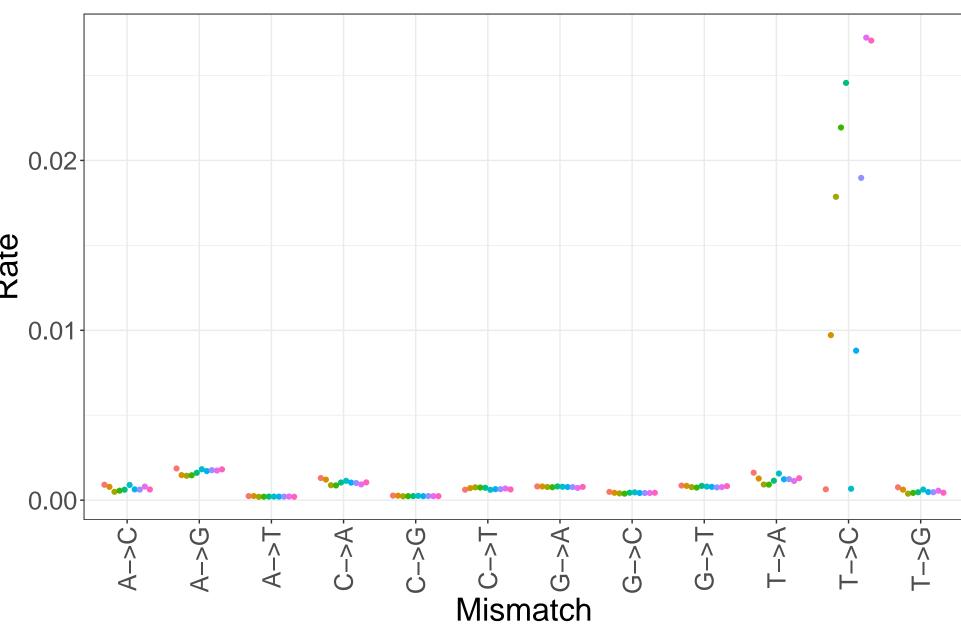
# Intronic First



- 107317A\_TLS\_0h\_no4sU 107319A\_TLS\_1h 107321A\_TLS\_2h 107323A\_TLS\_4h 107325A\_TLS\_8h 107347B\_TLS\_0h\_no4sU 107349B\_TLS\_1h

- 107351B\_TLS\_2h 107353B\_TLS\_4h 107355B\_TLS\_8h

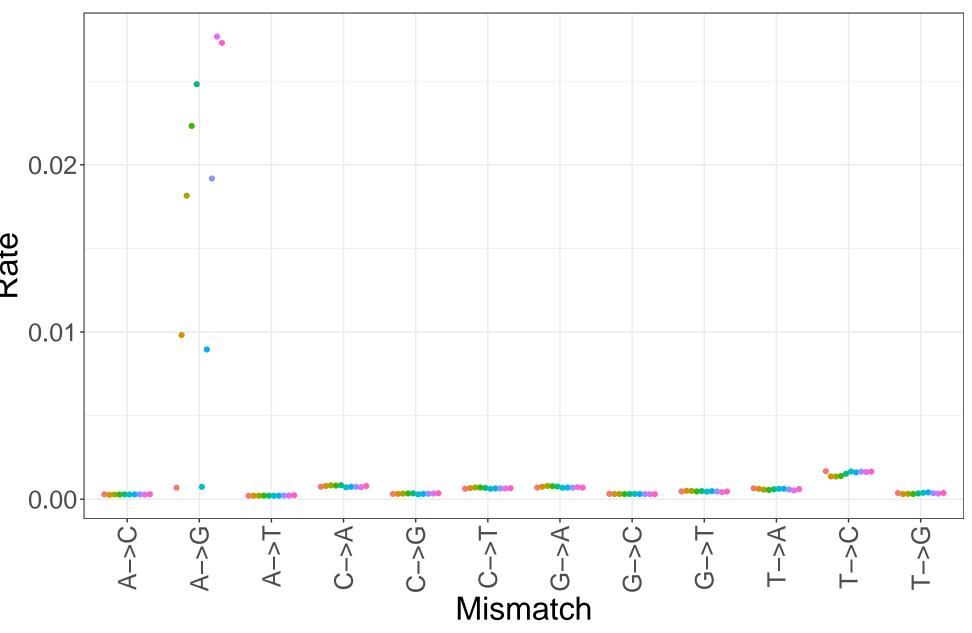
# Intronic Second



- 107317A\_TLS\_0h\_no4sU 107319A\_TLS\_1h 107321A\_TLS\_2h 107323A\_TLS\_4h 107325A\_TLS\_8h 107347B\_TLS\_0h\_no4sU 107349B\_TLS\_1h

- 107351B\_TLS\_2h 107353B\_TLS\_4h 107355B\_TLS\_8h

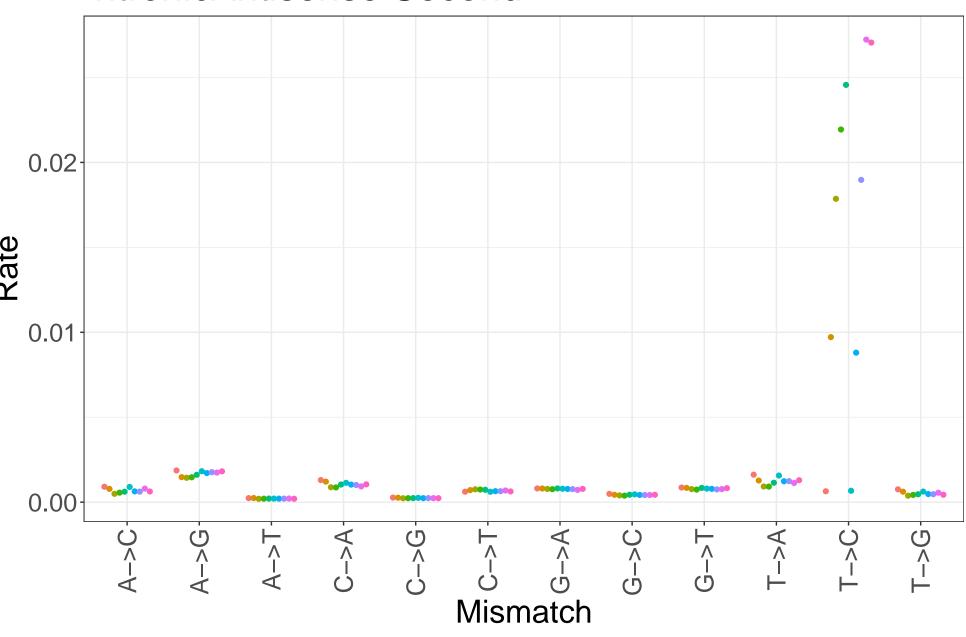
# IntronicAntisense First



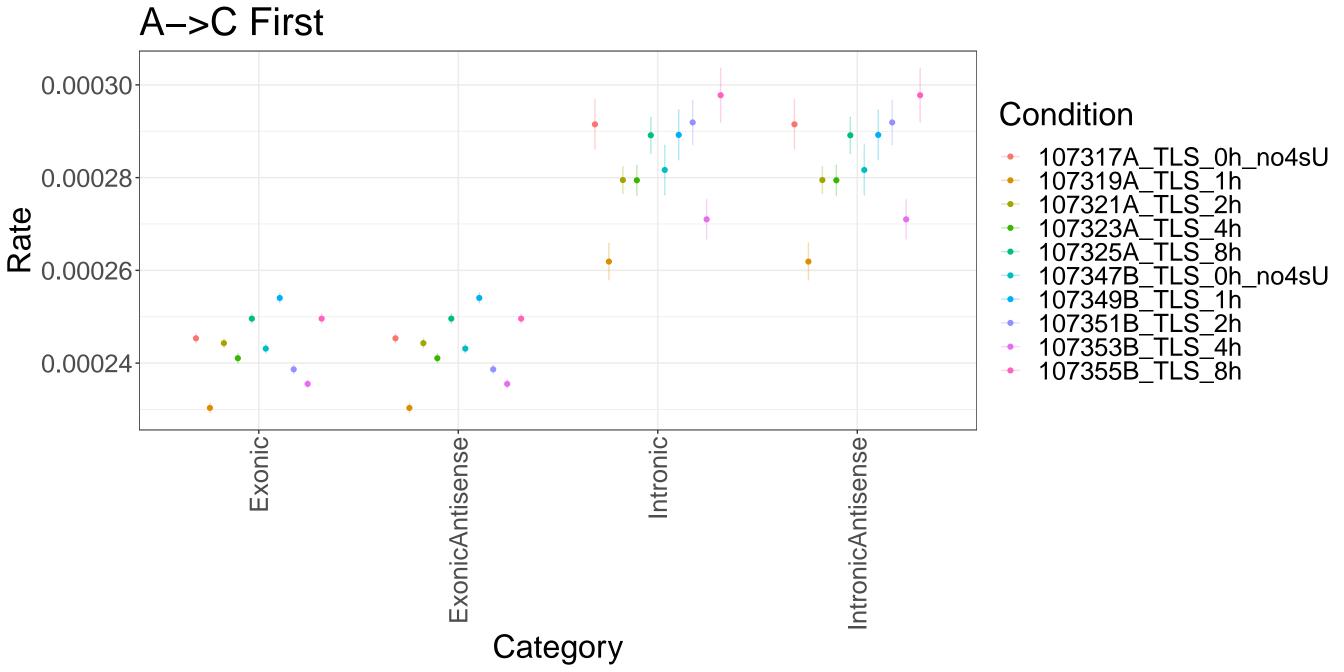
- 107317A\_TLS\_0h\_no4sU 107319A\_TLS\_1h 107321A\_TLS\_2h 107323A\_TLS\_4h 107325A\_TLS\_8h 107347B\_TLS\_0h\_no4sU 107349B\_TLS\_1h

- 107351B\_TLS\_2h 107353B\_TLS\_4h 107355B\_TLS\_8h

# IntronicAntisense Second



- 107317A\_TLS\_0h\_no4sU 107319A\_TLS\_1h 107321A\_TLS\_2h 107323A\_TLS\_4h 107325A\_TLS\_8h 107347B\_TLS\_0h\_no4sU 107349B\_TLS\_1h
- 107351B\_TLS\_2h 107353B\_TLS\_4h 107355B\_TLS\_8h

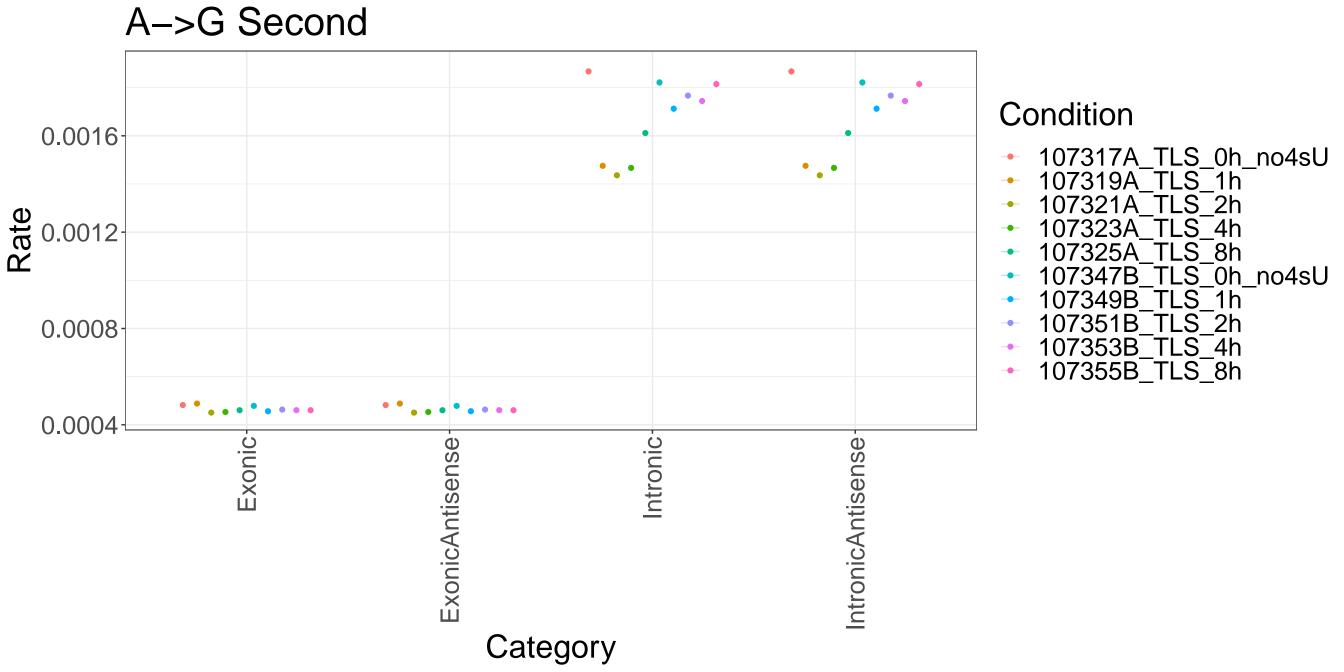


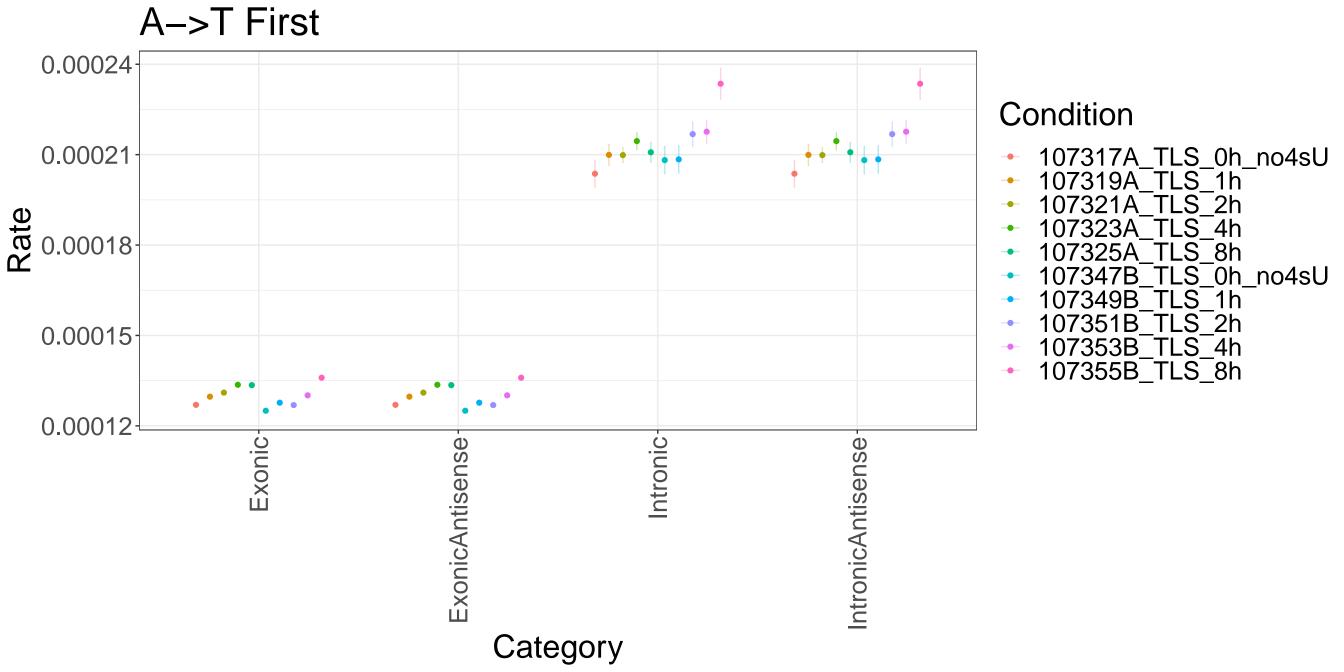
# A->C Second Condition 1e-03 107317A\_TLS\_0h\_no4sU 107319A\_TLS\_1h 107321A\_TLS\_2h 107323A\_TLS\_4h 8e-04-107325A\_TLS\_8h 107347B\_TLS\_0h\_no4sU 107349B\_TLS\_1h 107351B\_TLS\_2h 107353B\_TLS\_4h 107355B\_TLS\_8h 6e-04 Exonic ExonicAntisense IntronicAntisense

Category

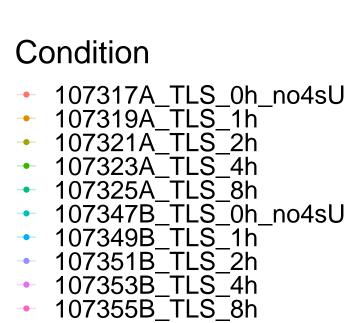
#### A->G First • • Condition 107317A\_TLS\_0h\_no4sU 107319A\_TLS\_1h 0.02 107321A\_TLS\_2h 107323A\_TLS\_4h Rate 107325A\_TLS\_8h 107347B\_TLS\_0h\_no4sU 107349B\_TLS\_1h 0.01 107351B\_TLS\_2h 107353B\_TLS\_4h 107355B\_TLS\_8h 0.00 Exonic-Intronic ExonicAntisense IntronicAntisense

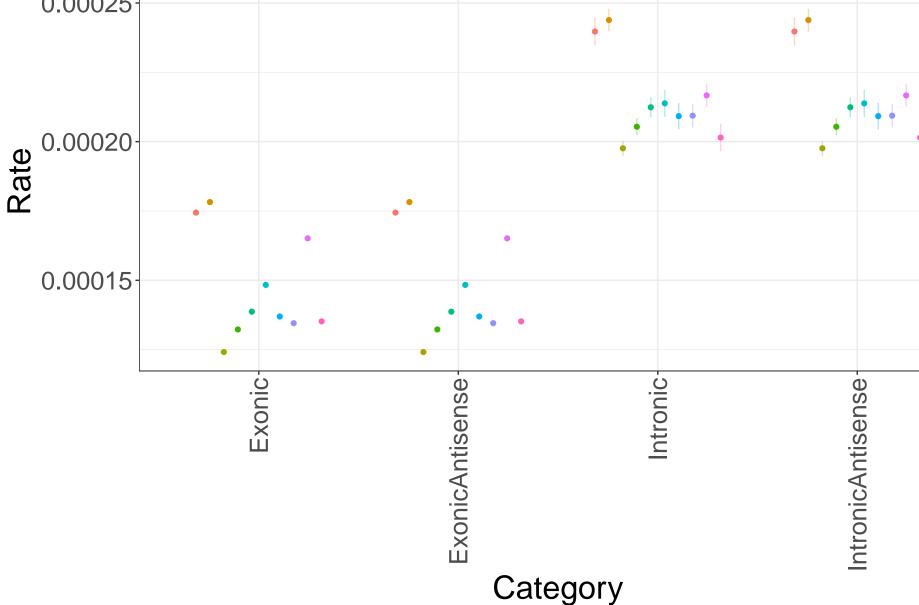
Category





# A->T Second 0.00025

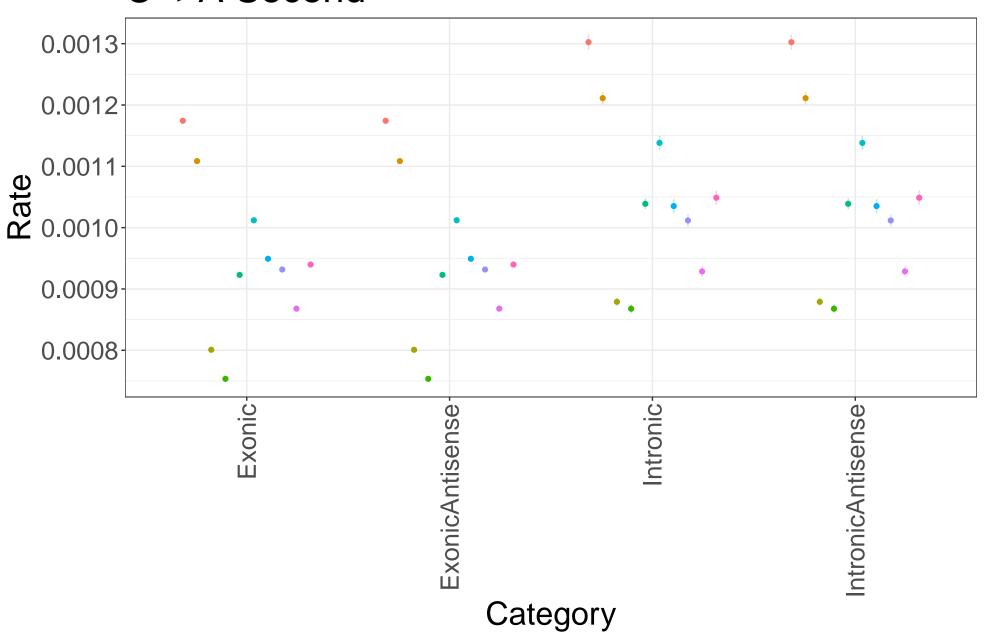




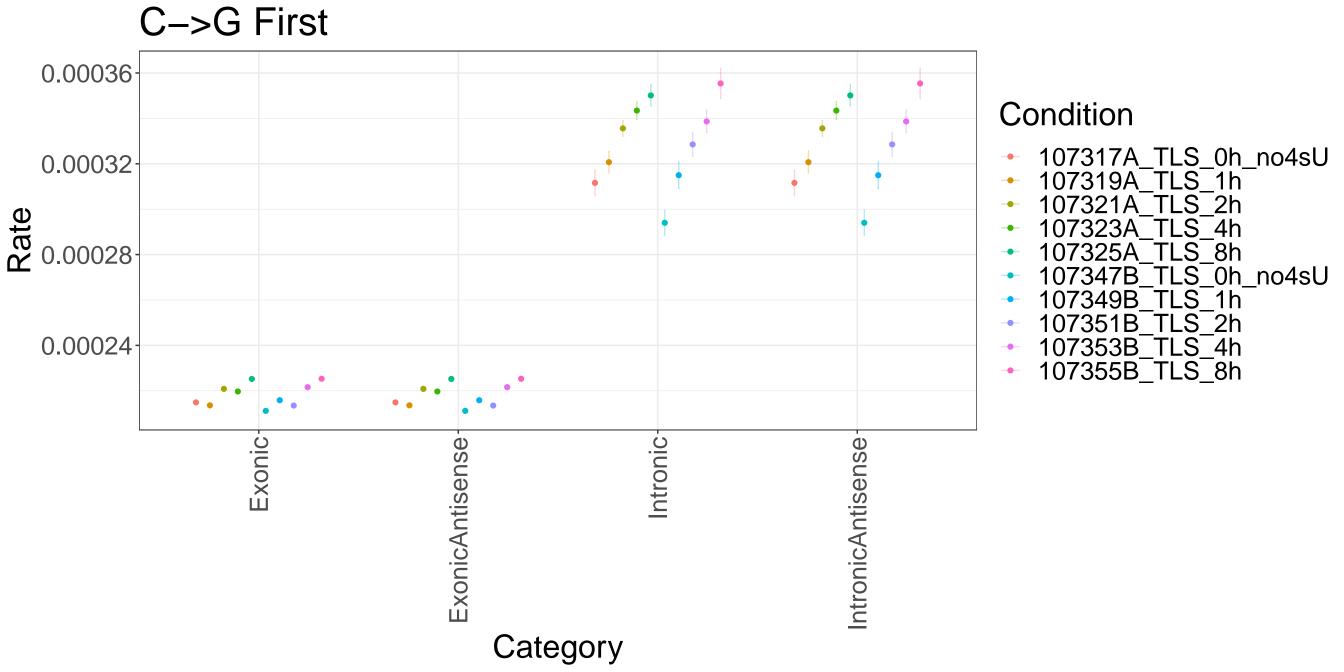
# C->A First 0.00085Condition 0.00080 107317A\_TLS\_0h\_no4sU 107319A\_TLS\_1h 0.00075 107321A\_TLS\_2h 107323A\_TLS\_4h **Rate** 0.00070 107325A\_TLS\_8h 107347B\_TLS\_0h\_no4sU 107349B\_TLS\_1h 0.00065 107351B\_TLS\_2h 107353B\_TLS\_4h 107355B\_TLS\_8h 0.00060 0.00055 Exonic-Intronic IntronicAntisense ExonicAntisense

Category

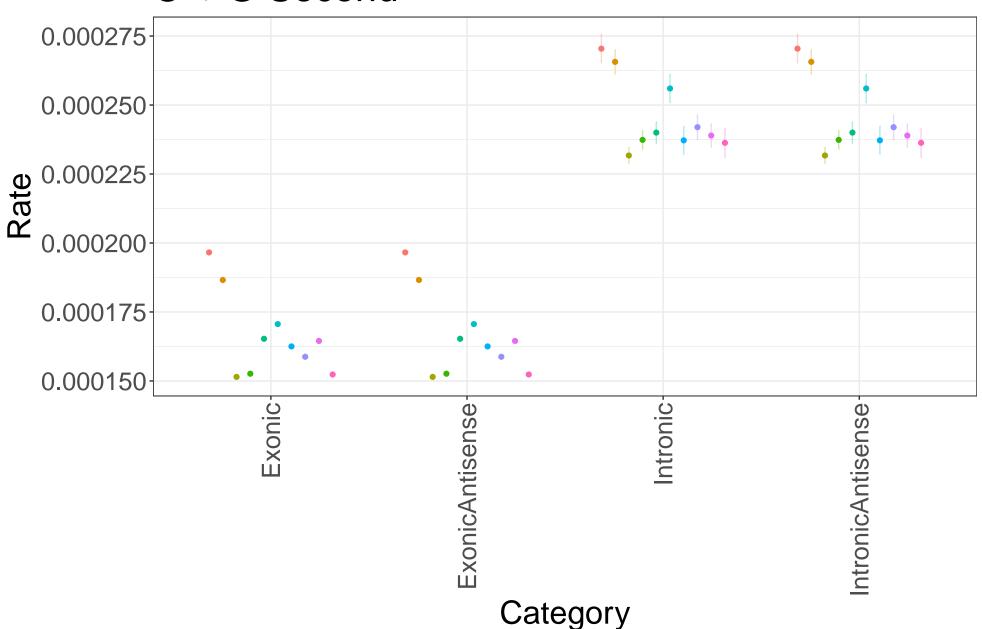
# C->A Second



- 107317A\_TLS\_0h\_no4sU 107319A\_TLS\_1h
- 107321A\_TLS\_2h
- 107325A
- 107347B\_TLS\_0h\_no4sU
- 107349B TLS 1h
- 107351B\_
- 107353B\_TLS\_4h 107355B\_TLS\_8h

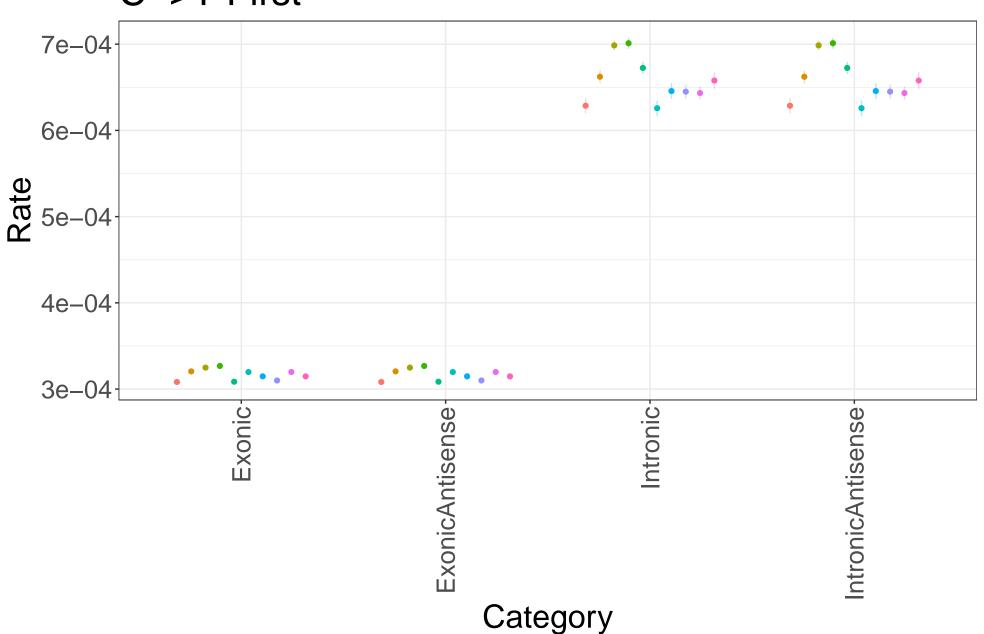


# C->G Second



- 107317A\_TLS\_0h\_no4sU 107319A\_TLS\_1h
- 107321A\_TLS\_2h 107323A\_TLS\_4h
- 107325A\_TLS\_8h
- 107347B\_TLS\_0h\_no4sU 107349B\_TLS\_1h
- 107351B\_TLS\_2h
- 107353B\_TLS\_4h 107355B\_TLS\_8h

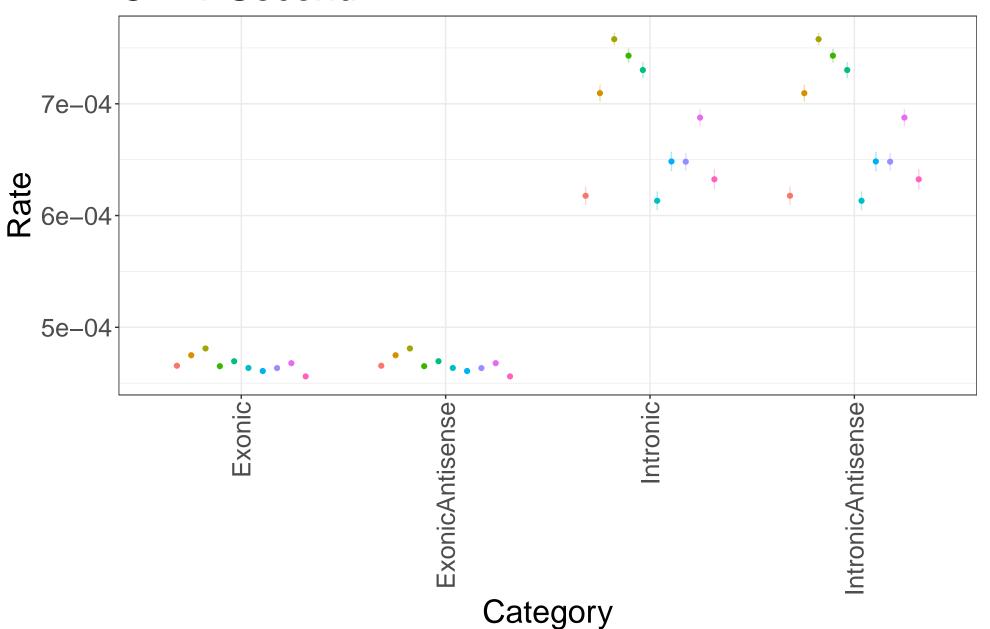
# C->T First



- 107317A\_TLS\_0h\_no4sU 107319A\_TLS\_1h
- 107321A\_TLS\_2h 107323A\_TLS\_4h
- 107325A\_TLS\_8h
- 107347B\_TLS\_0h\_no4sU 107349B\_TLS\_1h

- 107351B\_TLS\_2h 107353B\_TLS\_4h
- 107355B\_TLS\_8h

# C->T Second



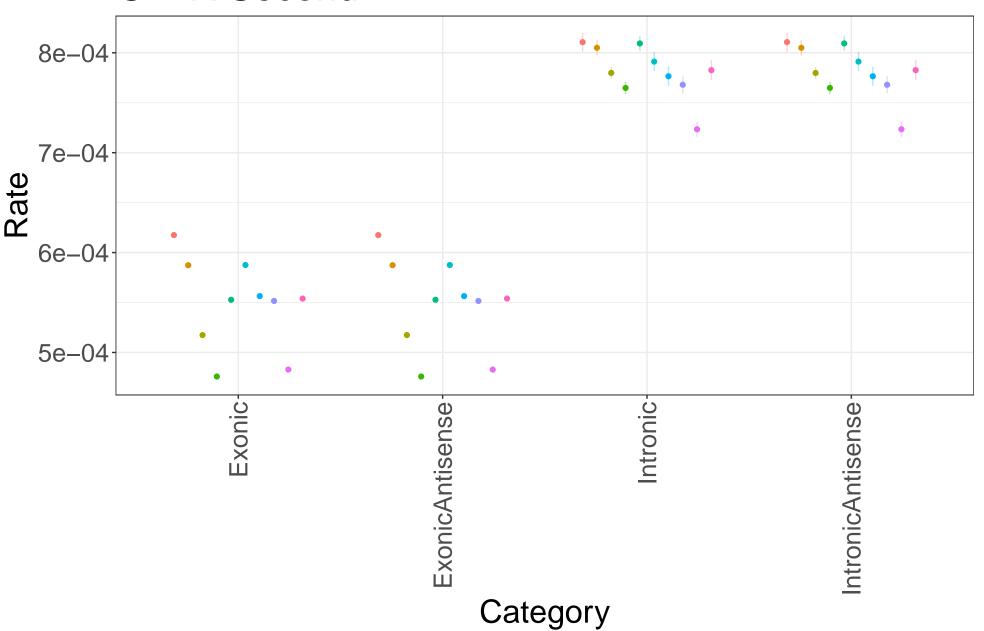
- 107317A\_TLS\_0h\_no4sU 107319A\_TLS\_1h
- 107321A\_TLS\_2h 107323A\_TLS\_4h
- 107325A\_TLS\_8h
- 107347B\_TLS\_0h\_no4sU 107349B\_TLS\_1h

- 107351B\_TLS\_2h 107353B\_TLS\_4h 107355B\_TLS\_8h

# G->A First 8e-04 Condition 107317A\_TLS\_0h\_no4sU 7e-04 107319A\_TLS\_1h 107321A\_TLS\_2h 107323A\_TLS\_4h Rate 107325A\_TLS\_8h 107347B\_TLS\_0h\_no4sU 107349B\_TLS\_1h 6e-04 107351B\_TLS\_2h 107353B\_TLS\_4h 107355B\_TLS\_8h 5e-04 Exonic Intronic IntronicAntisense ExonicAntisense

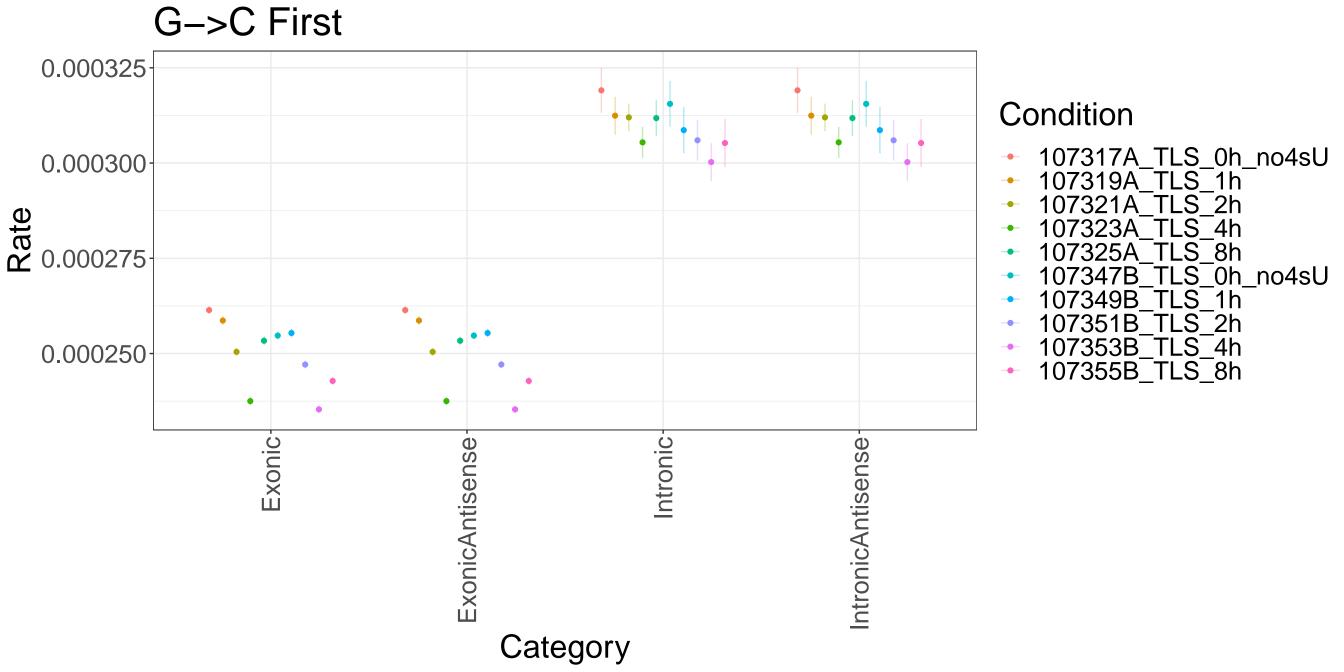
Category

#### G->A Second



- 107317A\_TLS\_0h\_no4sU 107319A\_TLS\_1h
- 107321A\_TLS\_2h 107323A\_TLS\_4h
- 107325A\_TLS\_8h
- 107347B\_TLS\_0h\_no4sU 107349B\_TLS\_1h

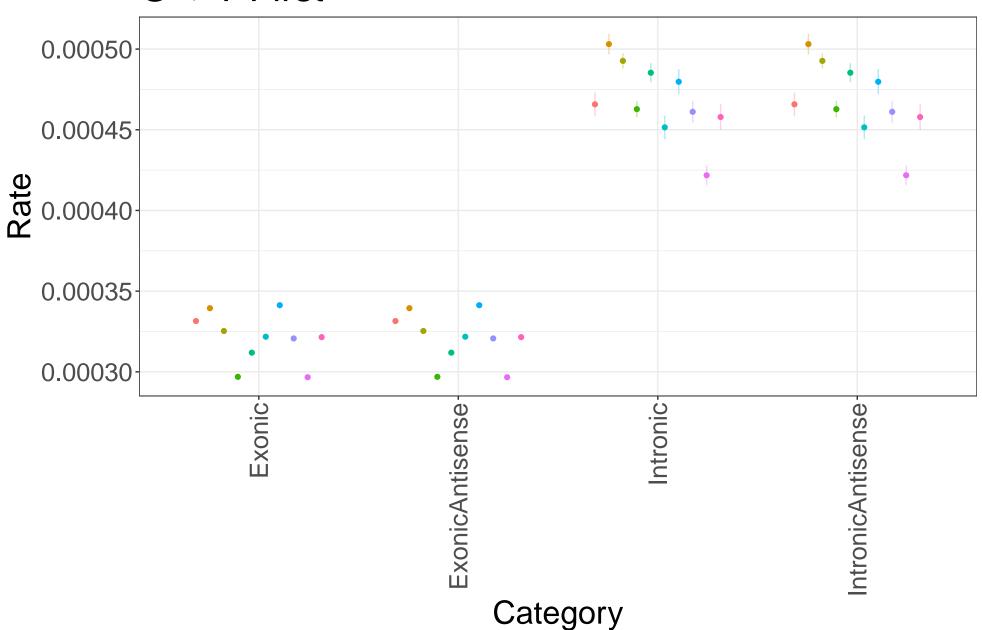
- 107351B\_TLS\_2h 107353B\_TLS\_4h 107355B\_TLS\_8h



# G->C Second 0.00052 Condition 107317A\_TLS\_0h\_no4sU 107319A\_TLS\_1h 0.00048107321A\_TLS\_2h Rate 9.00044 107323A\_TLS\_4h 107325A\_TLS\_8h 107347B\_TLS\_0h\_no4sU 107349B TLS 1h 107351B\_TLS\_2h 107353B\_TLS\_4h 107355B\_TLS\_8h 0.00040 **Exonic**-Intronic ExonicAntisense

Category

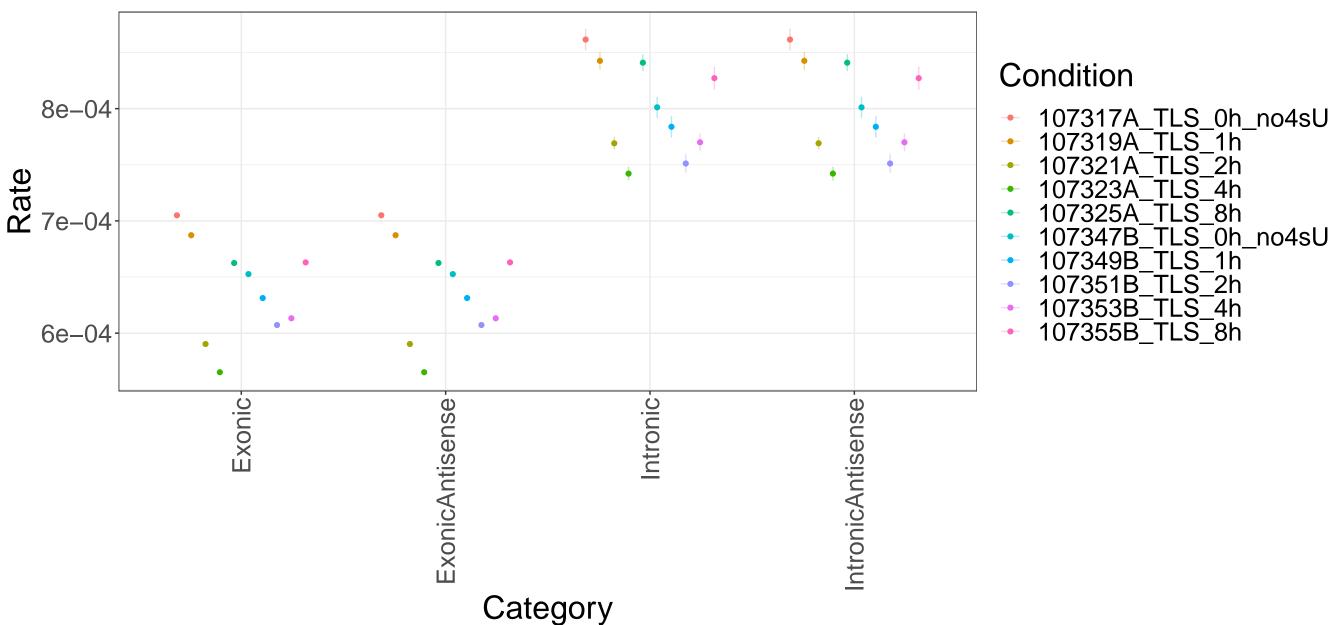
# G->T First

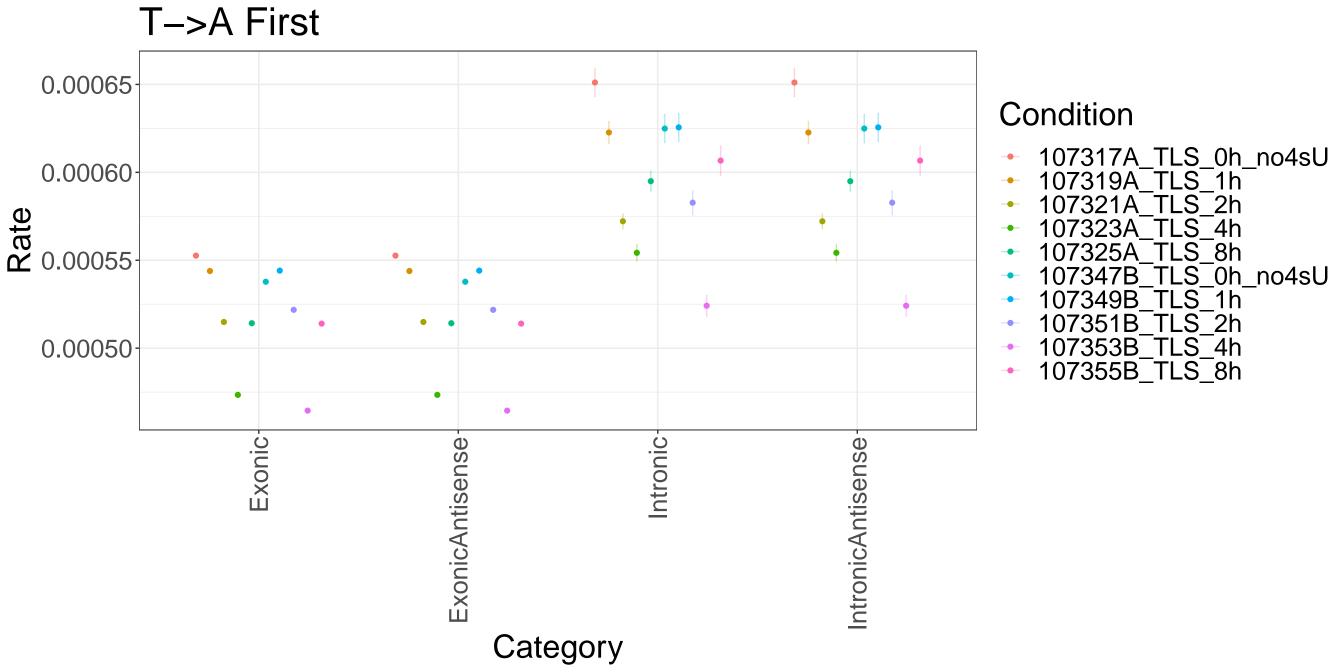


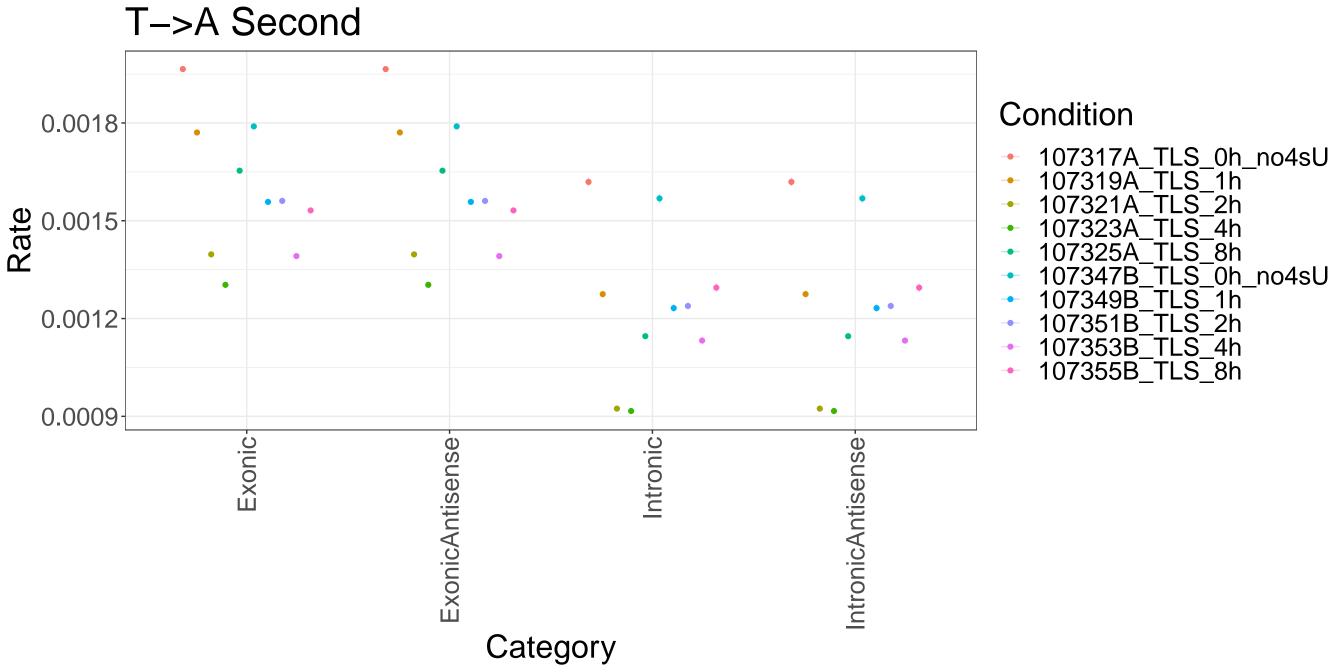
- 107317A\_TLS\_0h\_no4sU 107319A\_TLS\_1h
- 107321A\_TLS\_2h 107323A\_TLS\_4h
- 107325A\_TLS\_8h
- 107347B\_TLS\_0h\_no4sU 107349B\_TLS\_1h

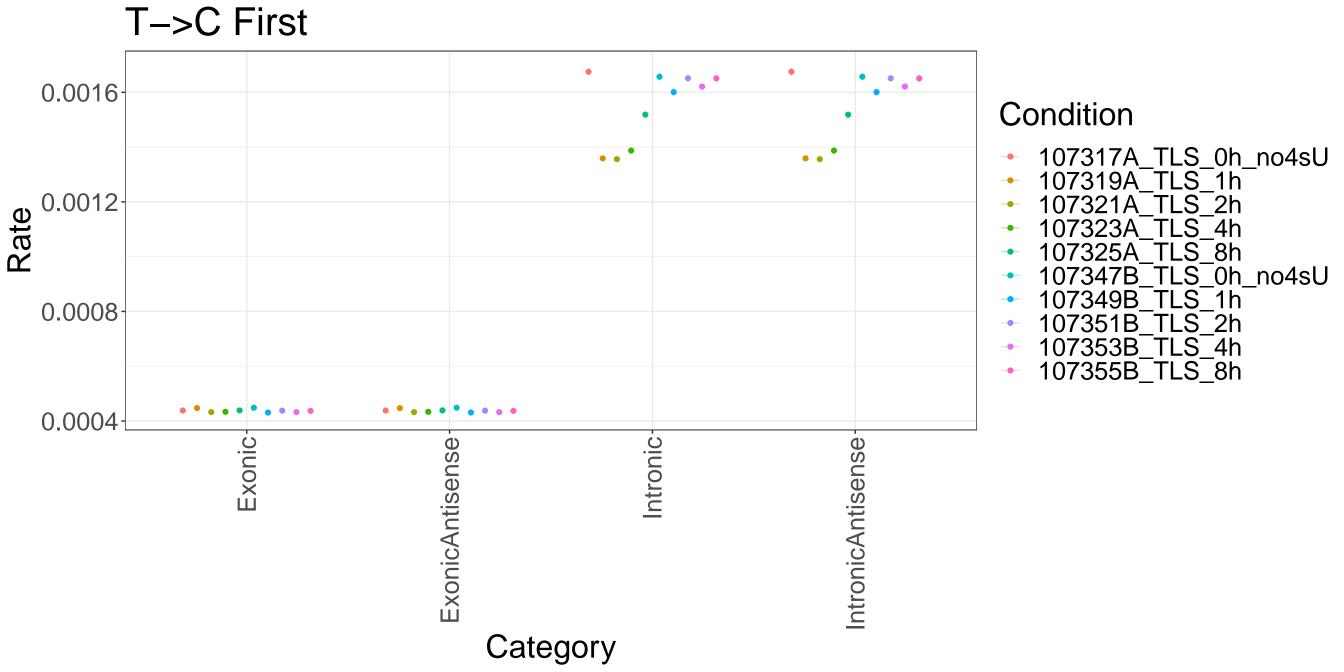
- 107351B\_TLS\_2h 107353B\_TLS\_4h 107355B\_TLS\_8h

# G->T Second





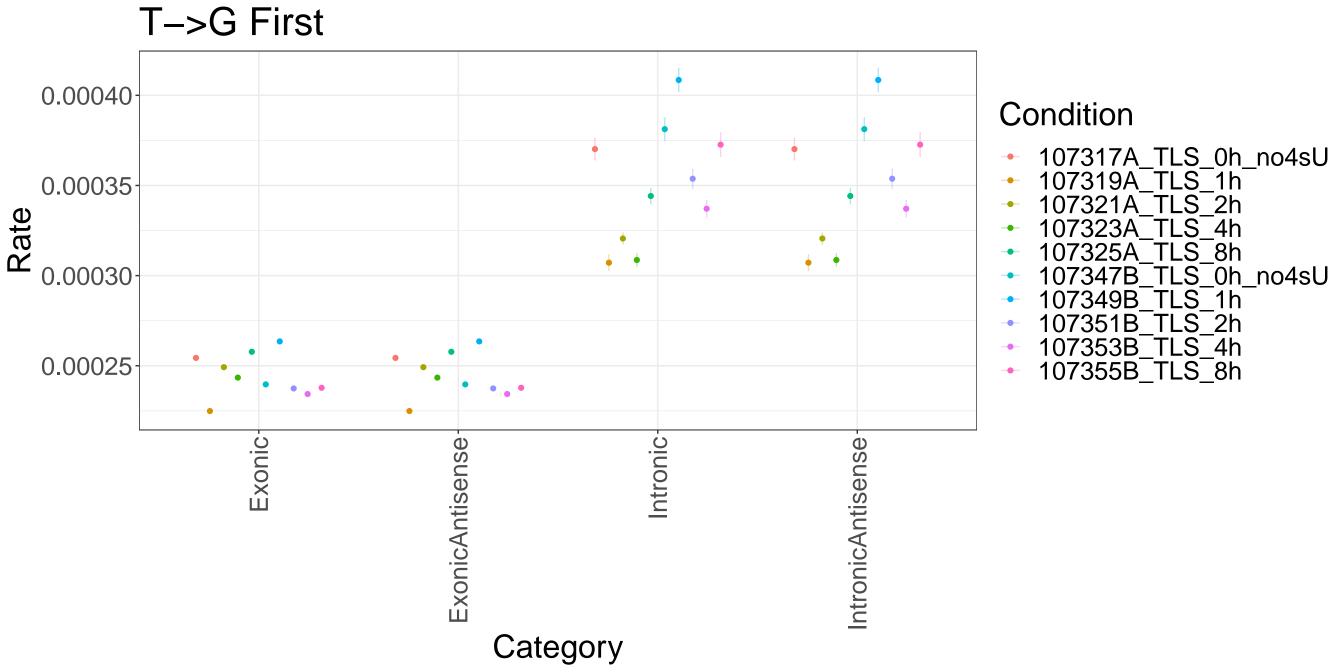




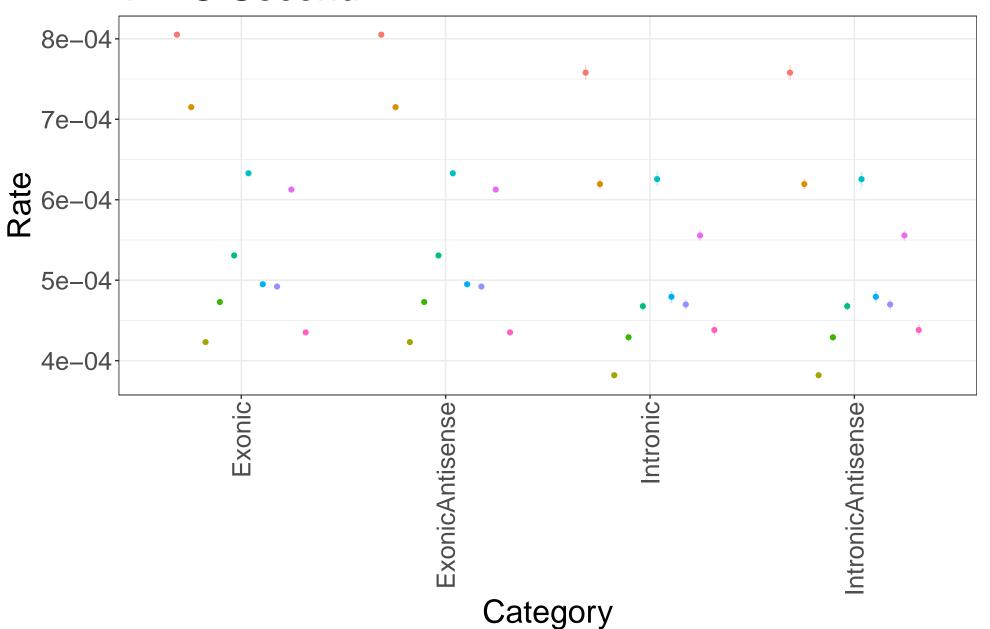
# T->C Second • • Condition 107317A\_TLS\_0h\_no4sU 107319A\_TLS\_1h 0.02 107321A\_TLS\_2h 107323A\_TLS\_4h 107325A\_TLS\_8h 107347B\_TLS\_0h\_no4sU 0.01 107349B\_TLS\_1h 107351B\_TLS\_2h 107353B\_TLS\_4h 107355B\_TLS\_8h 0.00 Exonic-Intronic ExonicAntisense IntronicAntisense

Category

Rate

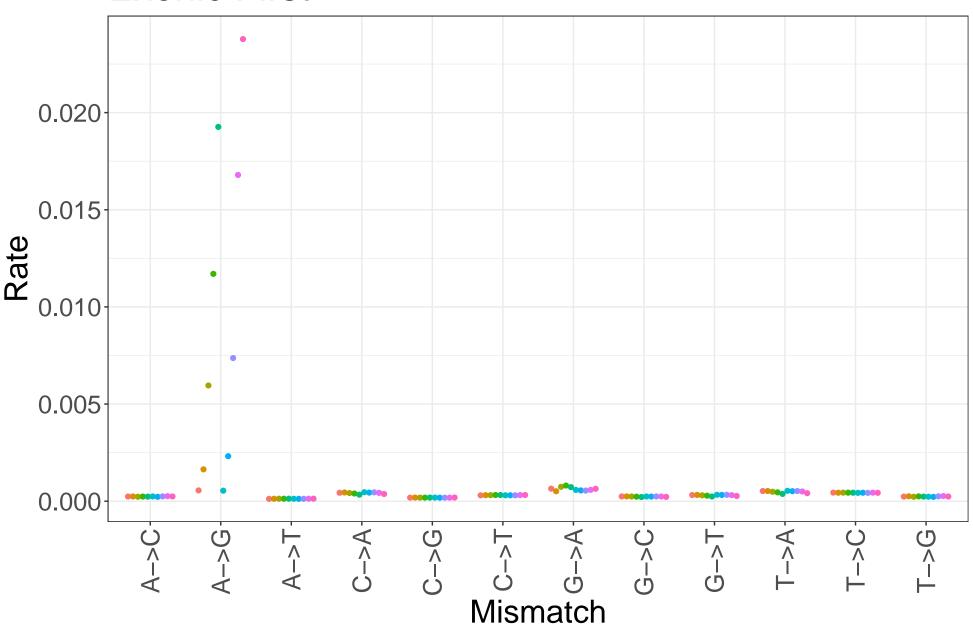


# T->G Second



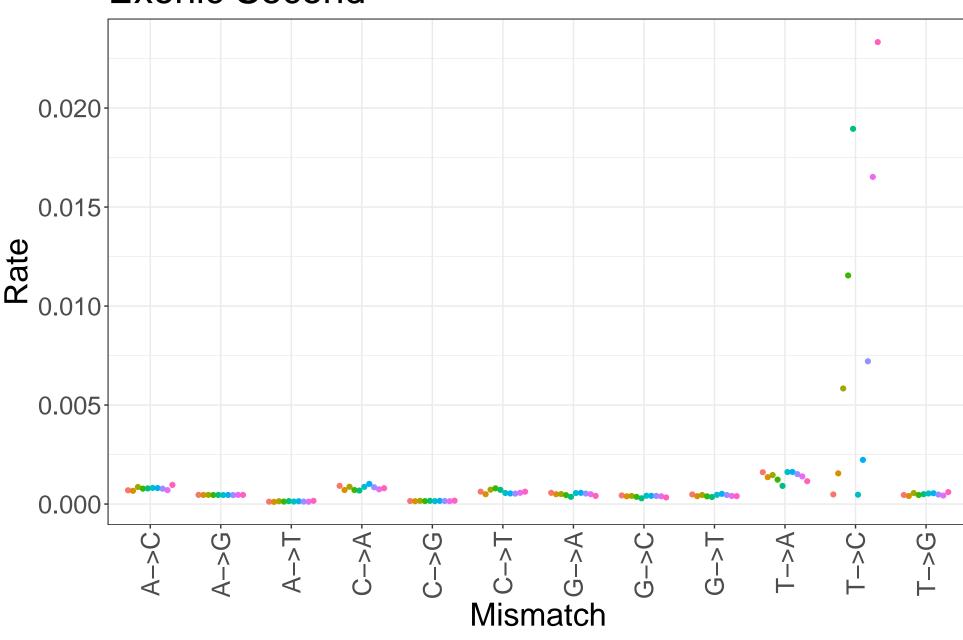
- 107317A\_TLS\_0h\_no4sU
- 107319A\_TLS\_1h
- 107321A\_TLS\_2h
- 107323A\_TLS\_4h
- 107325A\_TLS\_8h
- 107347B\_TLS\_0h\_no4sU
- 107349B TLS 1h
- 107351B\_TLS\_2h
- 107353B\_TLS\_4h
- 107355B\_TLS\_8h

# **Exonic First**



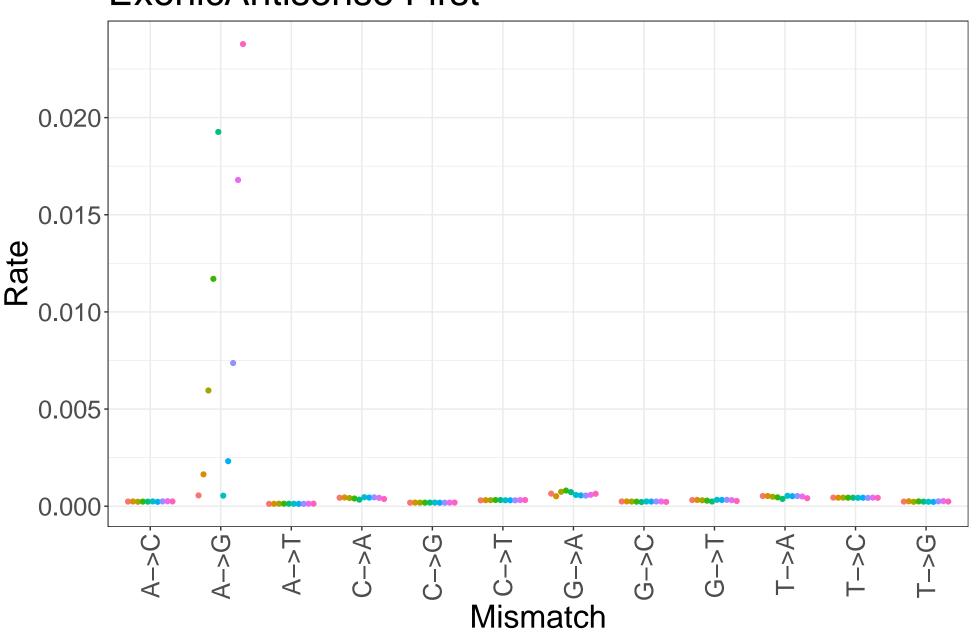
- 107327A\_TUC\_0h\_no4sU 107329A\_TUC\_1h 107331A\_TUC\_2h 107333A\_TUC\_4h 107335A\_TUC\_8h 107357B\_TUC\_0h\_no4sU 107359B\_TUC\_1h 107361B\_TUC\_2h 107363B\_TUC\_4h 107365B\_TUC\_8h

# **Exonic Second**



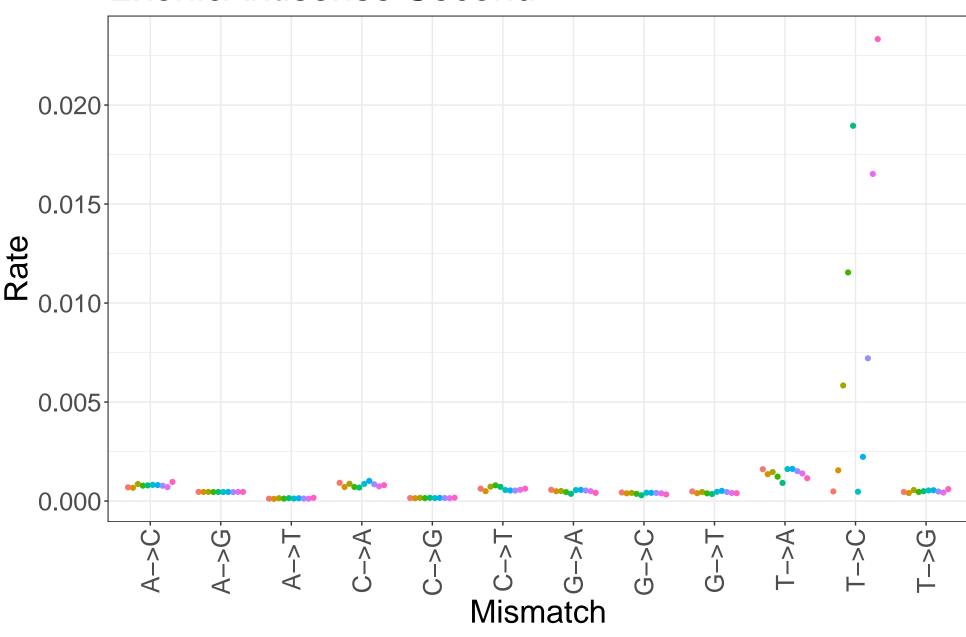
- 107327A\_TUC\_0h\_no4sU 107329A\_TUC\_1h 107331A\_TUC\_2h 107333A\_TUC\_4h 107335A\_TUC\_8h 107357B\_TUC\_0h\_no4sU 107359B\_TUC\_1h 107361B\_TUC\_2h 107363B\_TUC\_4h 107365B\_TUC\_8h

# **ExonicAntisense First**



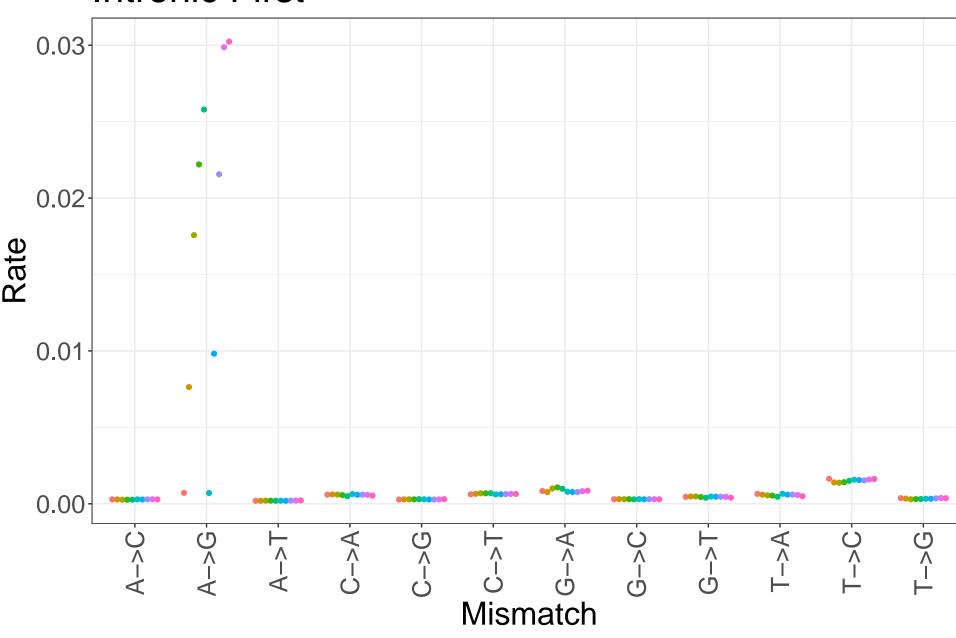
- 107327A\_TUC\_0h\_no4sU
  107329A\_TUC\_1h
  107331A\_TUC\_2h
  107333A\_TUC\_4h
  107335A\_TUC\_8h
  107357B\_TUC\_0h\_no4sU
  107359B\_TUC\_1h
  107361B\_TUC\_2h
  107365B\_TUC\_8h

# **ExonicAntisense Second**



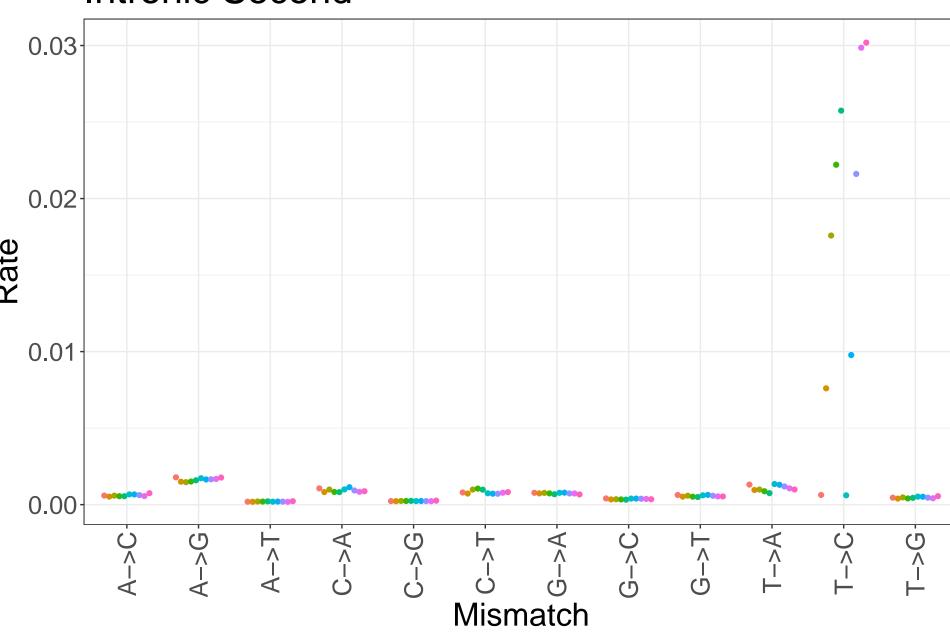
- 107327A\_TUC\_0h\_no4sU 107329A\_TUC\_1h 107331A\_TUC\_2h 107333A\_TUC\_4h 107335A\_TUC\_8h 107357B\_TUC\_0h\_no4sU 107359B\_TUC\_1h 107361B\_TUC\_2h 107363B\_TUC\_4h 107365B\_TUC\_8h

# Intronic First



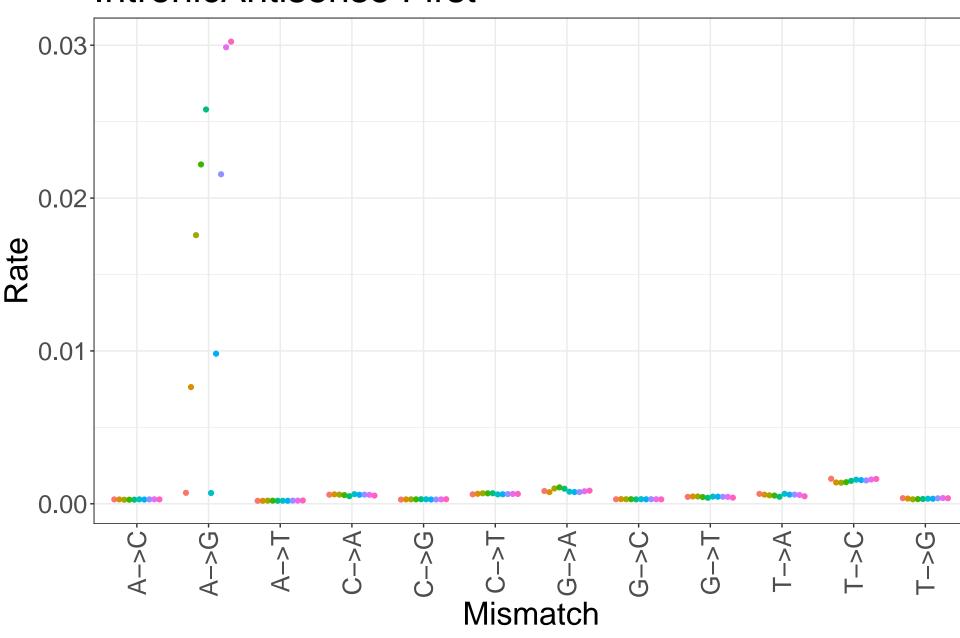
- 107327A\_TUC\_0h\_no4sU
  107329A\_TUC\_1h
  107331A\_TUC\_2h
  107333A\_TUC\_4h
  107335A\_TUC\_8h
  107357B\_TUC\_0h\_no4sU
  107359B\_TUC\_1h
  107361B\_TUC\_2h
  107365B\_TUC\_8h

# Intronic Second



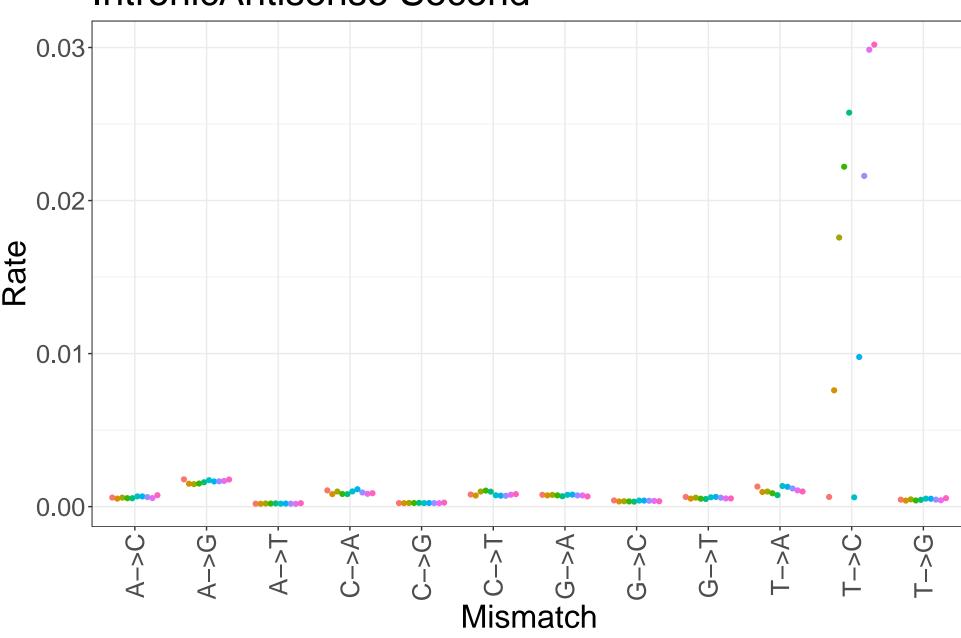
- 107327A\_TUC\_0h\_no4sU 107329A\_TUC\_1h 107331A\_TUC\_2h 107333A\_TUC\_4h 107335A\_TUC\_8h 107357B\_TUC\_0h\_no4sU 107359B\_TUC\_1h 107361B\_TUC\_2h 107363B\_TUC\_4h 107365B\_TUC\_8h

# IntronicAntisense First

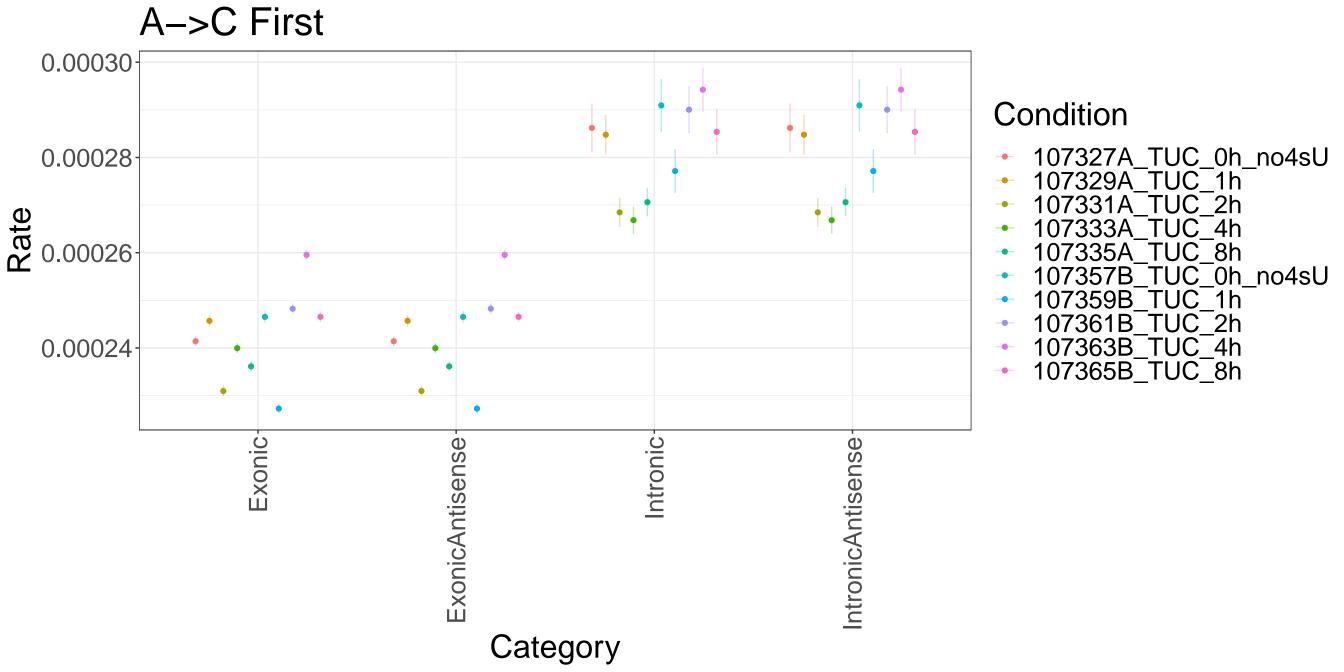


- 107327A\_TUC\_0h\_no4sU 107329A\_TUC\_1h 107331A\_TUC\_2h 107333A\_TUC\_4h 107335A\_TUC\_8h 107357B\_TUC\_0h\_no4sU 107359B\_TUC\_1h 107361B\_TUC\_2h 107363B\_TUC\_4h 107365B\_TUC\_8h

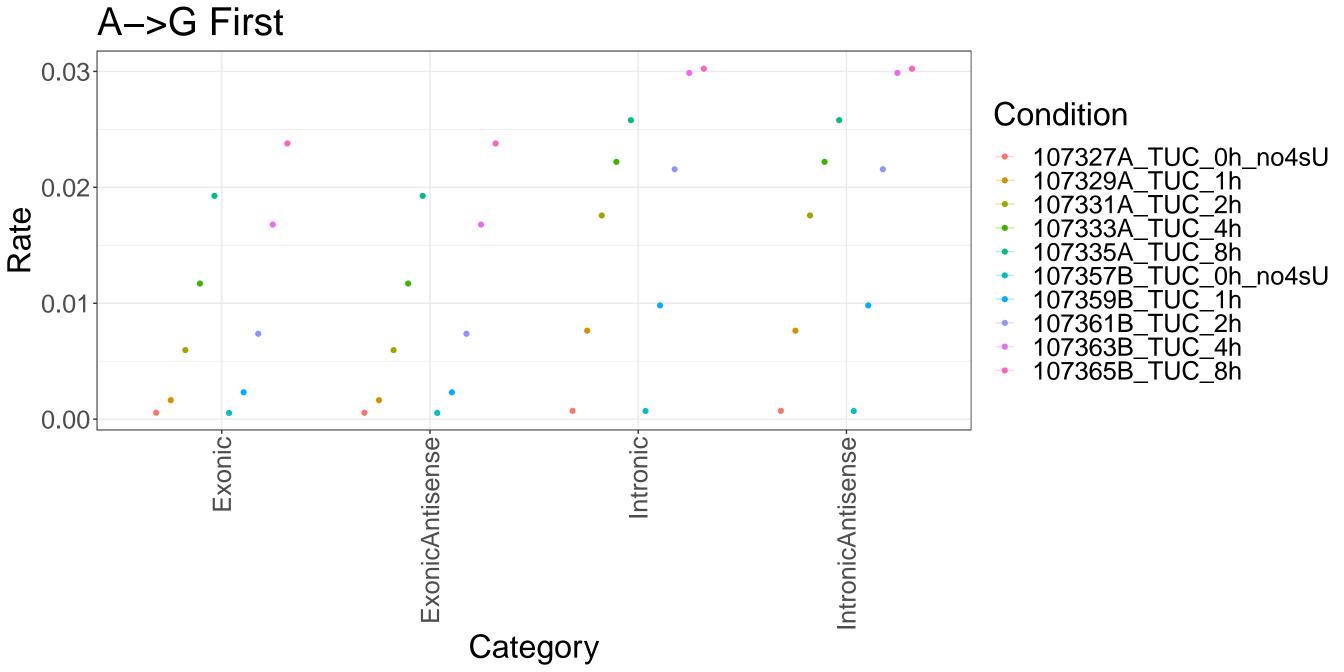
# IntronicAntisense Second

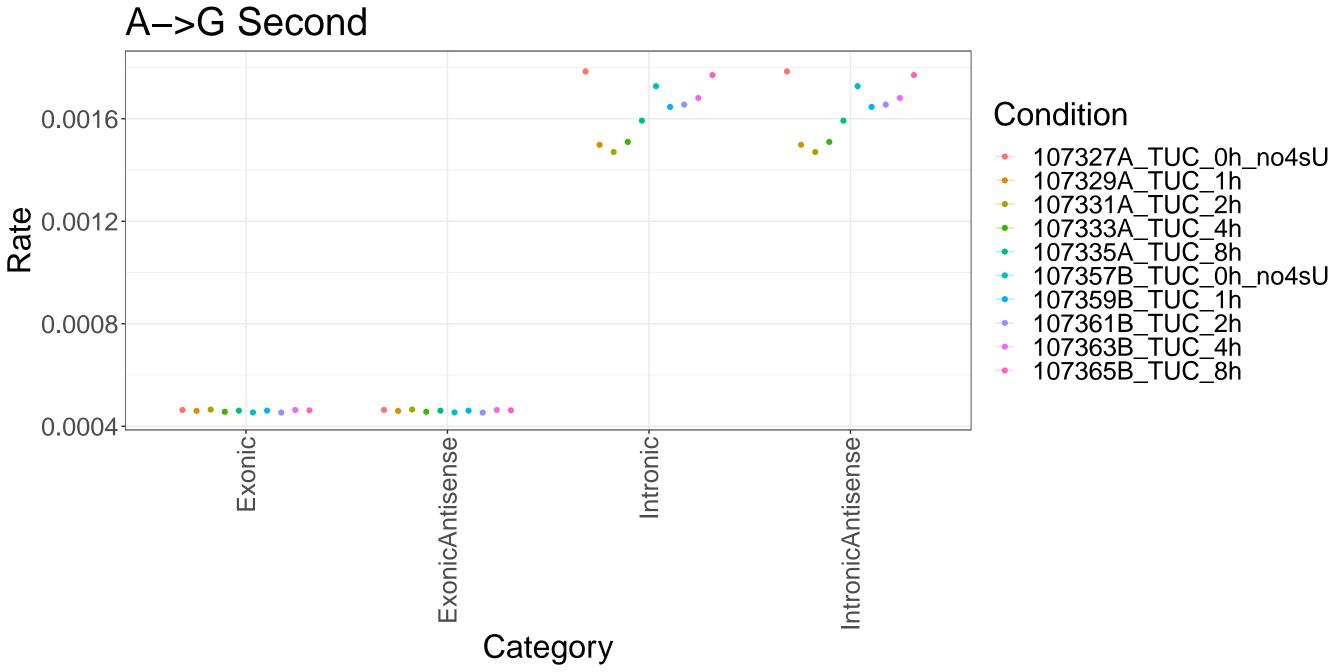


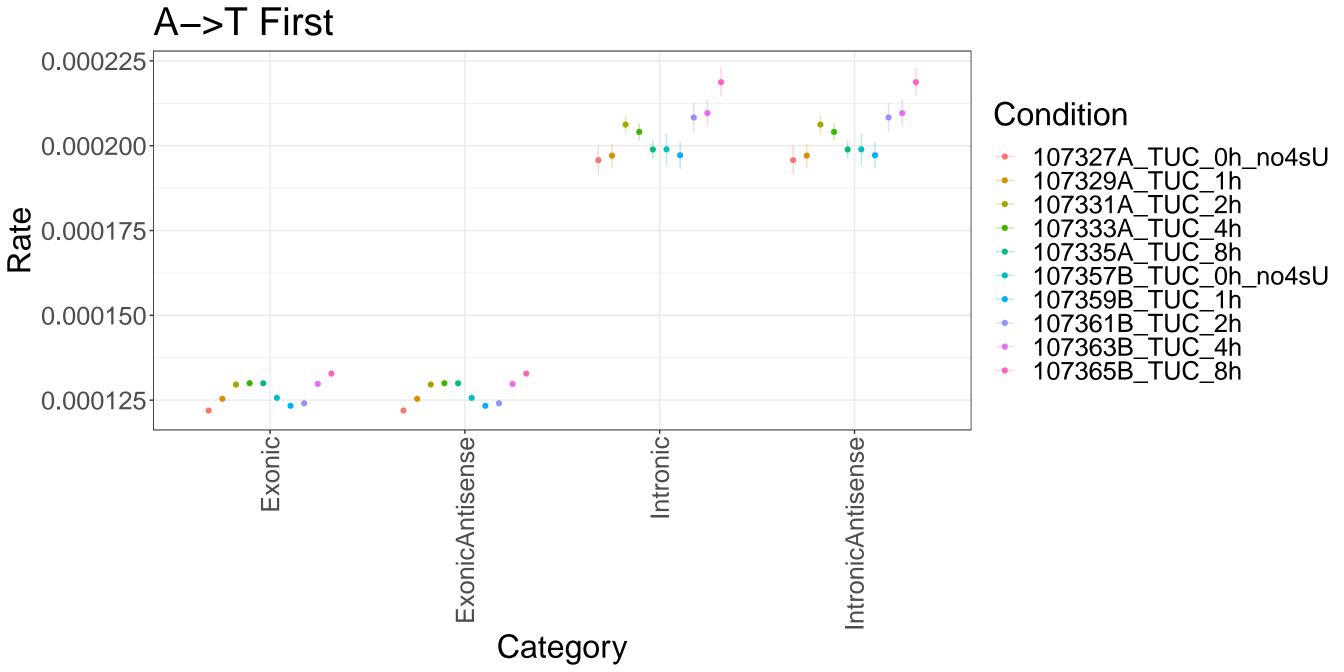
- 107327A\_TUC\_0h\_no4sU 107329A\_TUC\_1h 107331A\_TUC\_2h 107333A\_TUC\_4h 107335A\_TUC\_8h 107357B\_TUC\_0h\_no4sU 107359B\_TUC\_1h 107361B\_TUC\_2h 107363B\_TUC\_4h 107365B\_TUC\_8h

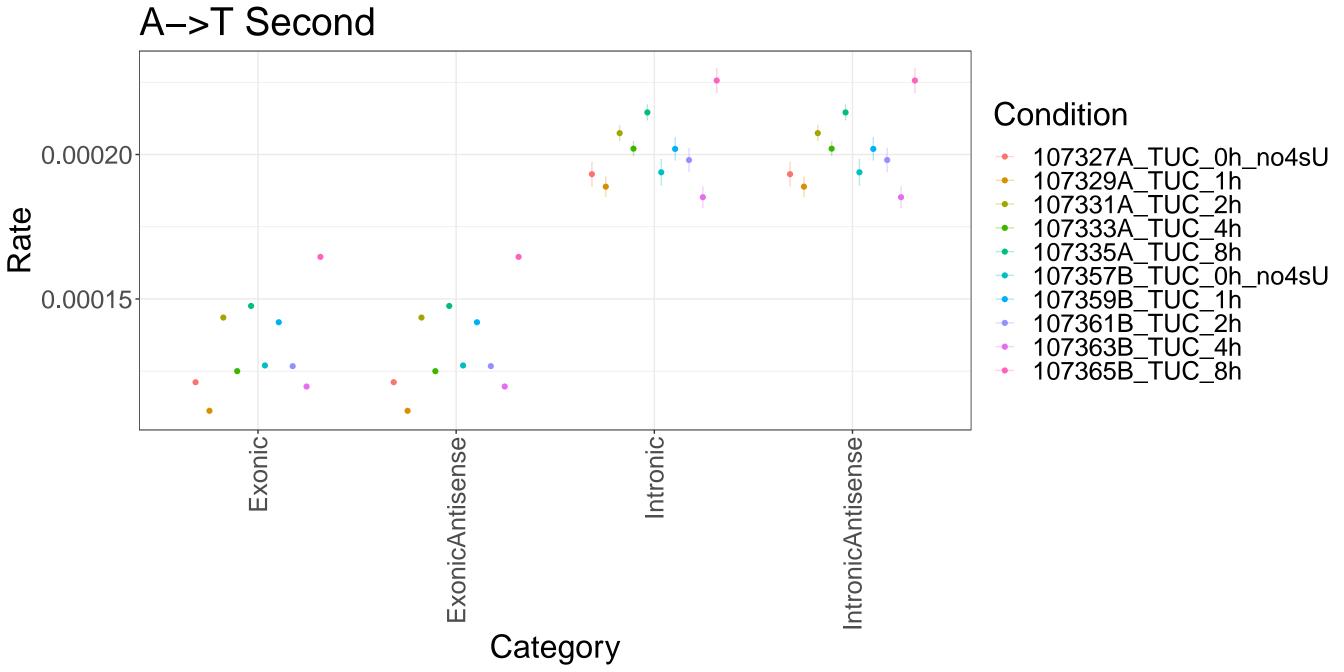


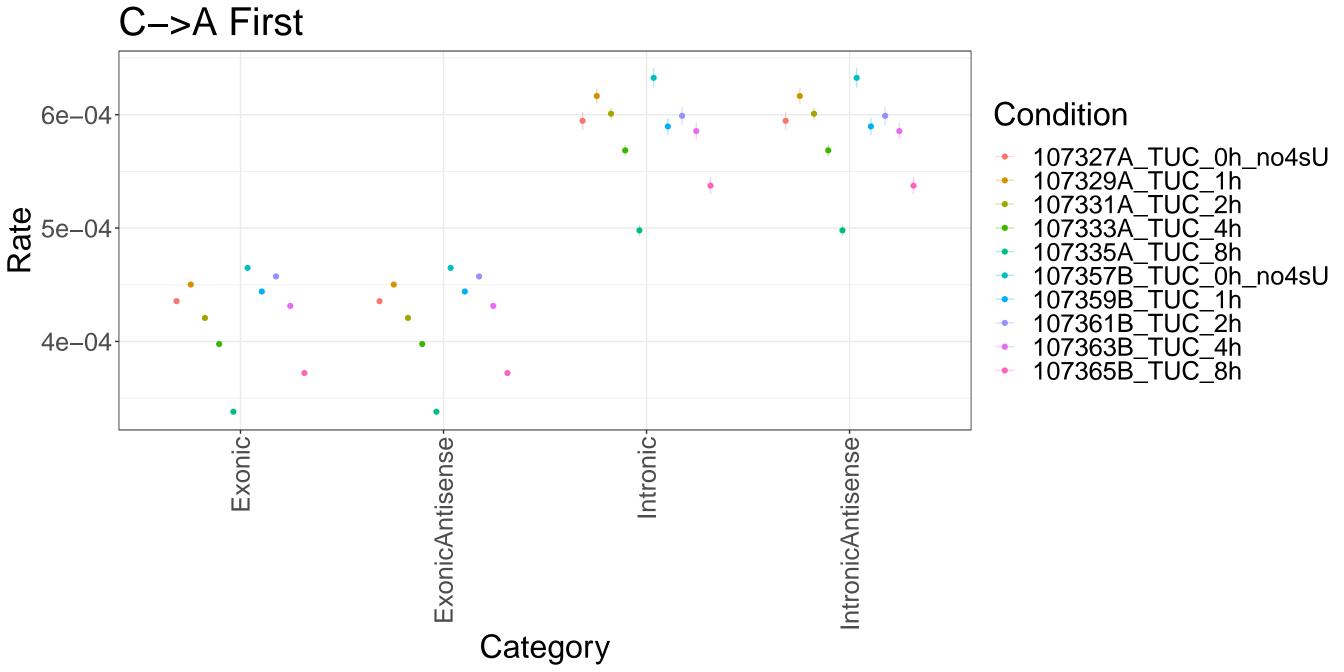
# A->C Second Condition 9e-04107327A\_TUC\_0h\_no4sU 107329A\_TUC\_1h 107331A\_TUC\_2h 107333A\_TUC\_4h • • 8e-04-107335A\_TUC\_8h 107357B\_TUC\_0h\_no4sU 107359B\_TUC\_1h 107361B\_TUC\_2h 107363B\_TUC\_4h 6e-04 107365B\_TUC\_8h 5e-04 **Exonic** Intronic-ExonicAntisense IntronicAntisense Category





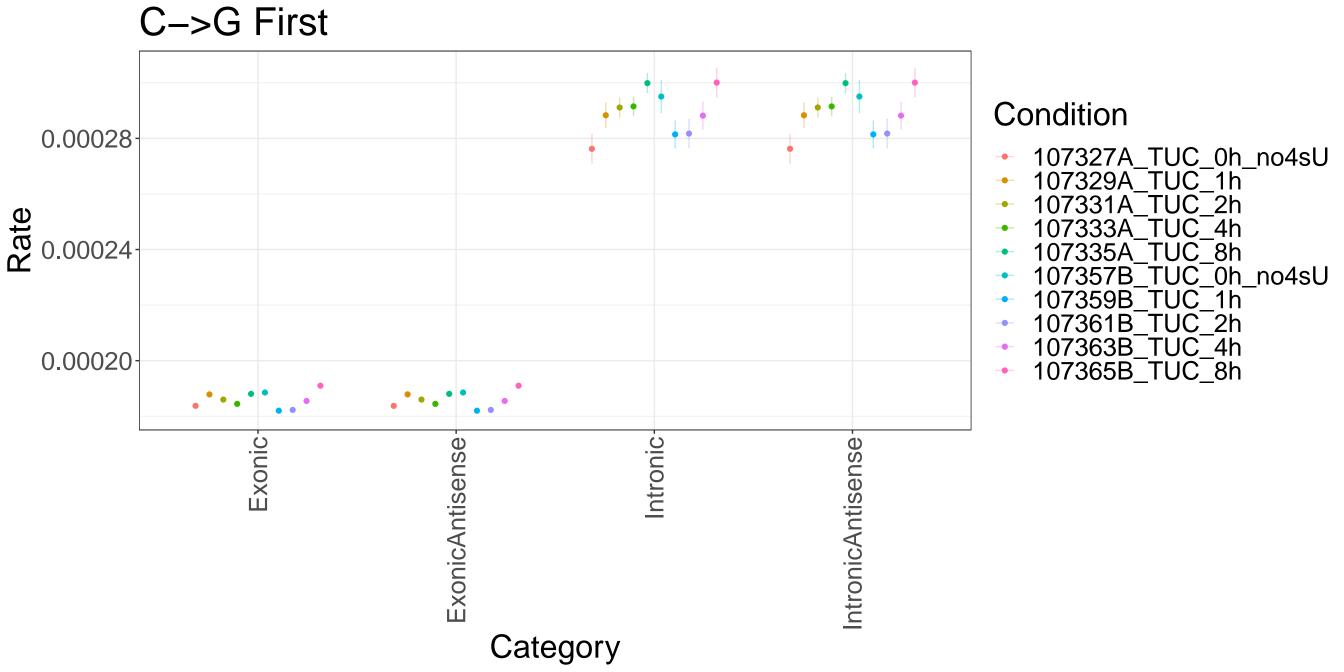




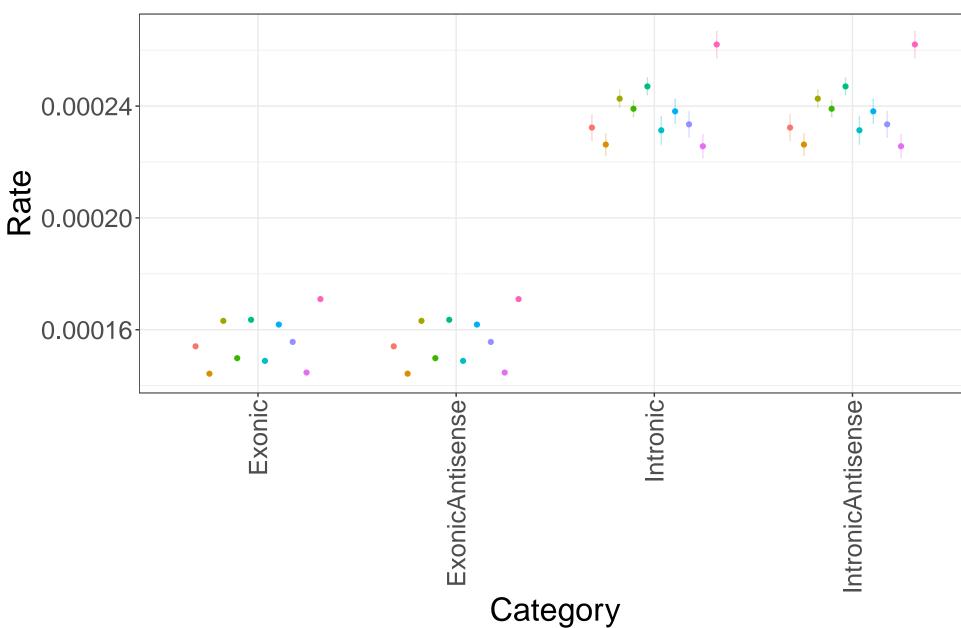


# C->A Second 0.0011 Condition 107327A\_TUC\_0h\_no4sU 107329A\_TUC\_1h 107331A\_TUC\_2h 107333A\_TUC\_4h 0.0010-107335A\_TUC\_8h .0009-107357B\_TUC\_0h\_no4sU 107359B\_TUC\_1h 107361B\_TUC\_2h 0.0008 107363B\_TUC\_4h 107365B\_TUC\_8h 0.0007 Exonic ExonicAntisense Intronic IntronicAntisense

Category



# C->G Second

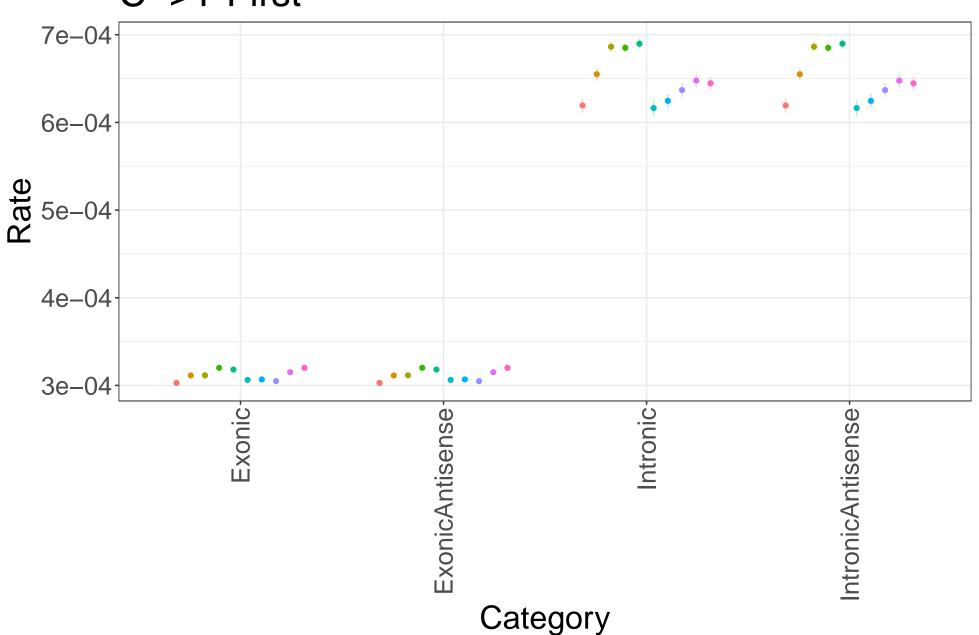


- 107327A\_TUC\_0h\_no4sU 107329A\_TUC\_1h 107331A\_TUC\_2h 107333A\_TUC\_4h

- 107335A\_TUC\_8h
- 107357B\_TUC\_0h\_no4sU 107359B\_TUC\_1h

- 107361B\_TUC\_2h 107363B\_TUC\_4h 107365B\_TUC\_8h

# C->T First

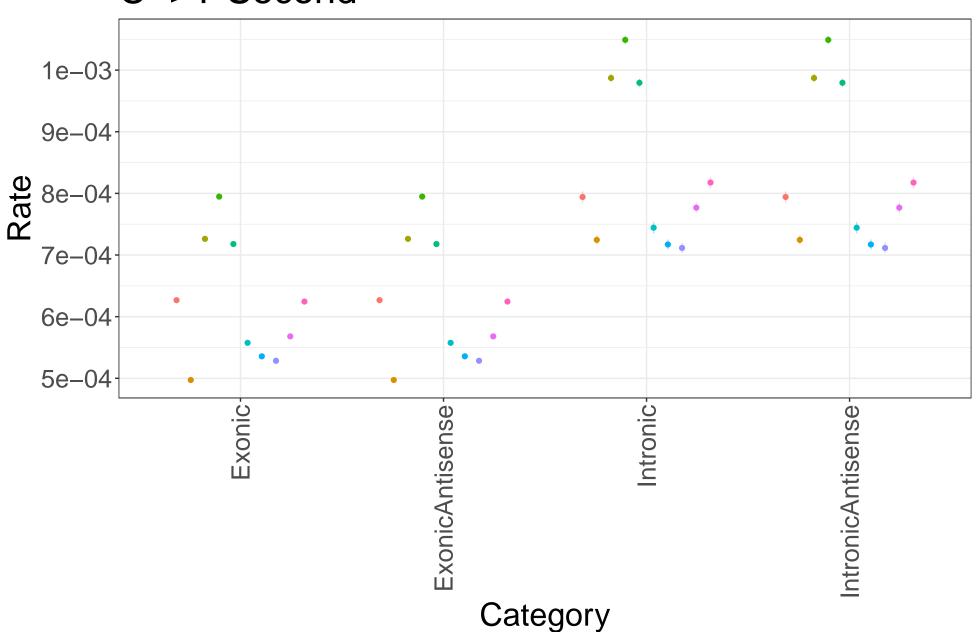


- 107327A\_TUC\_0h\_no4sU 107329A\_TUC\_1h 107331A\_TUC\_2h 107333A\_TUC\_4h

- 107335A\_TUC\_8h 107357B\_TUC\_0h\_no4sU 107359B\_TUC\_1h

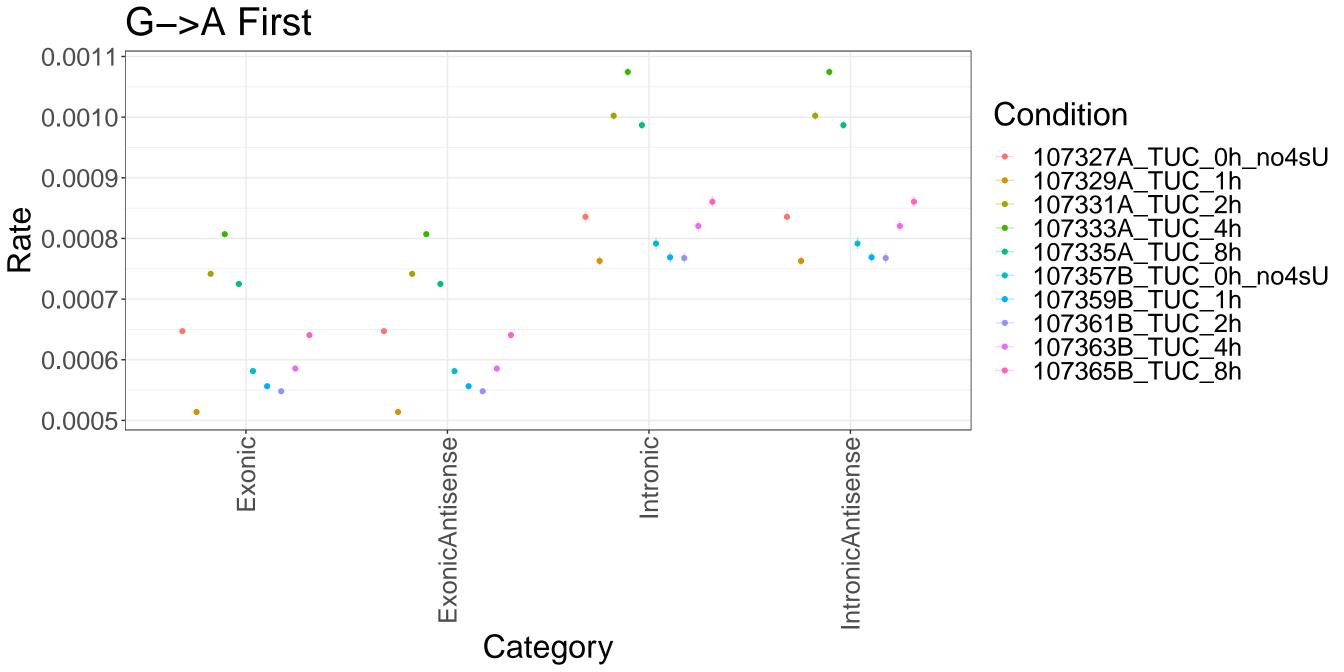
- 107361B\_TUC\_2h 107363B\_TUC\_4h
- 107365B\_TUC\_8h

# C->T Second

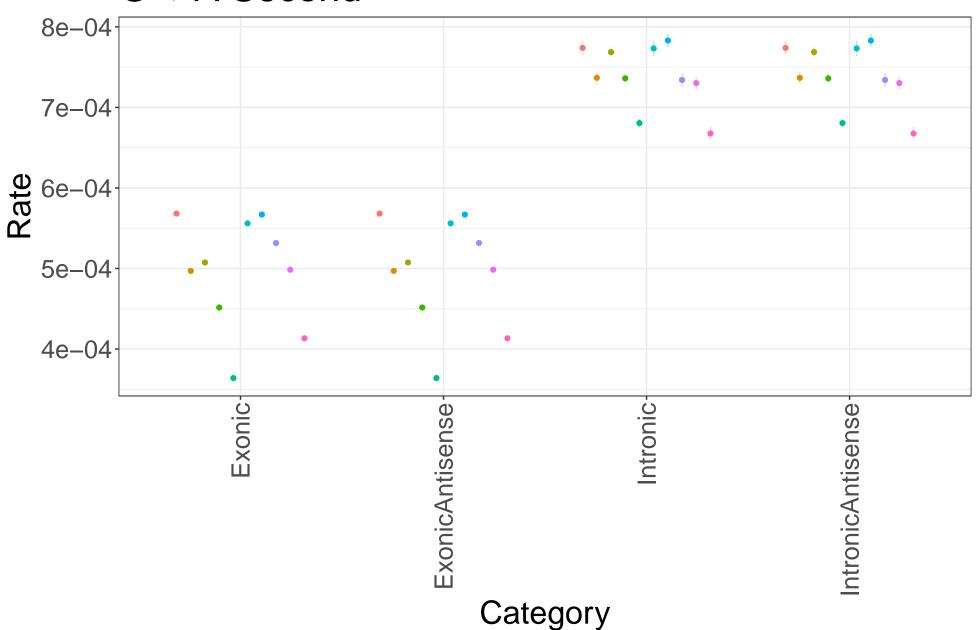


- 107327A\_TUC\_0h\_no4sU 107329A\_TUC\_1h
- 107331A\_TUC\_2h
- 107333A\_TUC\_4h
- 107335A\_TUC\_8h
- 107357B\_TUC\_0h\_no4sU 107359B\_TUC\_1h

- 107361B\_TUC\_2h
- 107363B\_TUC\_4h
- 107365B\_TUC\_8h



## G->A Second



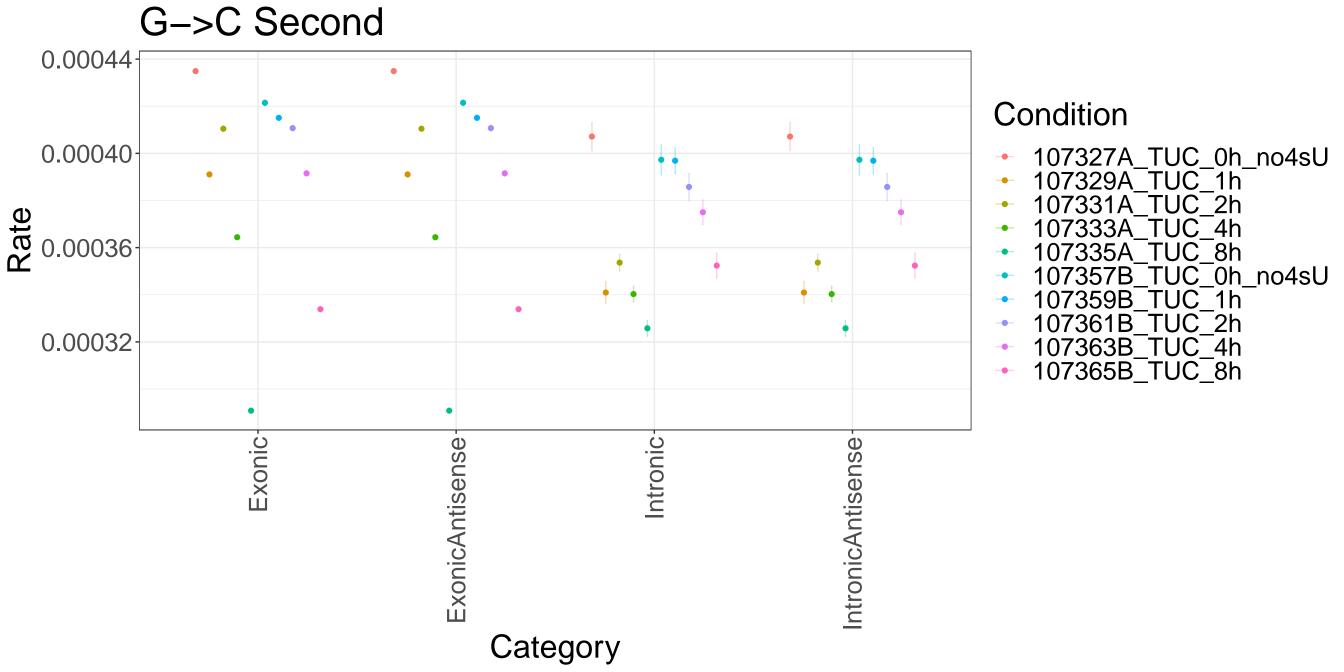
- 107327A\_TUC\_0h\_no4sU 107329A\_TUC\_1h 107331A\_TUC\_2h 107333A\_TUC\_4h

- 107335A\_TUC\_8h
- 107357B\_TUC\_0h\_no4sU 107359B\_TUC\_1h

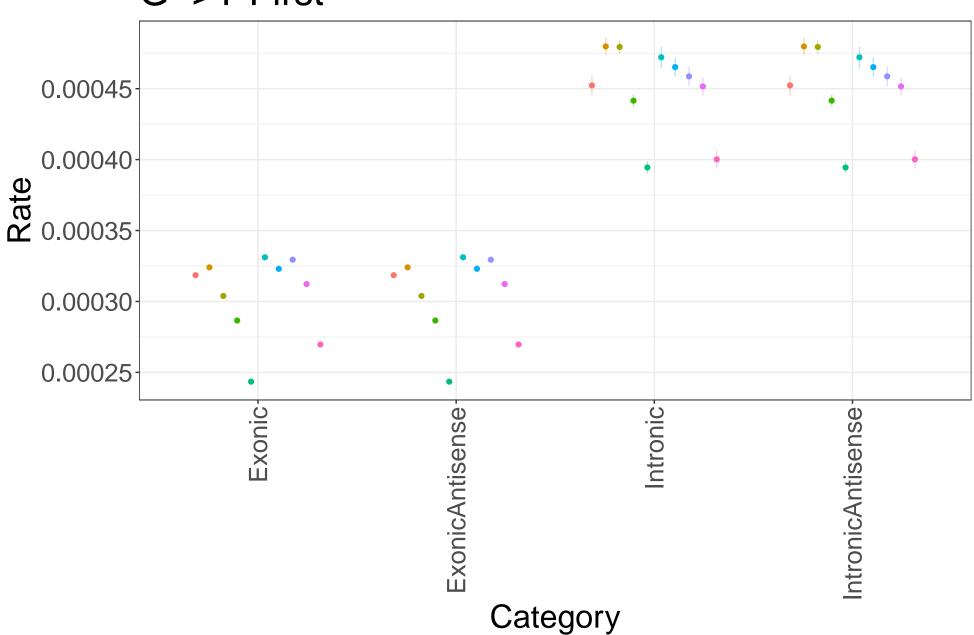
- 107361B\_TUC\_2h 107363B\_TUC\_4h
- 107365B\_TUC\_8h

# G->C First 0.00030 Condition 107327A\_TUC\_0h\_no4sU 107329A\_TUC\_1h 107331A\_TUC\_2h 107333A\_TUC\_4h 0.00028 107335A\_TUC\_8h .00026 107357B\_TUC\_0h\_no4sU 107359B\_TUC\_1h 107361B\_TUC\_2h 0.00024107363B\_TUC\_4h 107365B\_TUC\_8h 0.00022 Exonic Intronic IntronicAntisense ExonicAntisense

Category



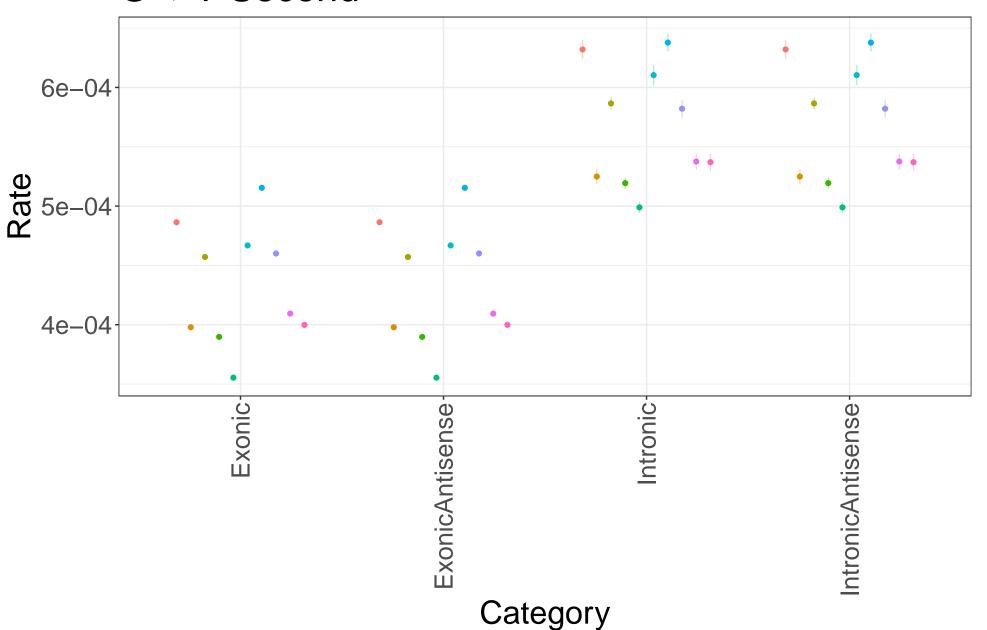
# G->T First



- 107327A\_TUC\_0h\_no4sU 107329A\_TUC\_1h 107331A\_TUC\_2h 107333A\_TUC\_4h

- 107335A\_TUC\_8h
- 107357B\_TUC\_0h\_no4sU 107359B\_TUC\_1h
- 107361B\_TUC\_2h
- 107363B\_TUC\_4h
- 107365B\_TUC\_8h

# G->T Second



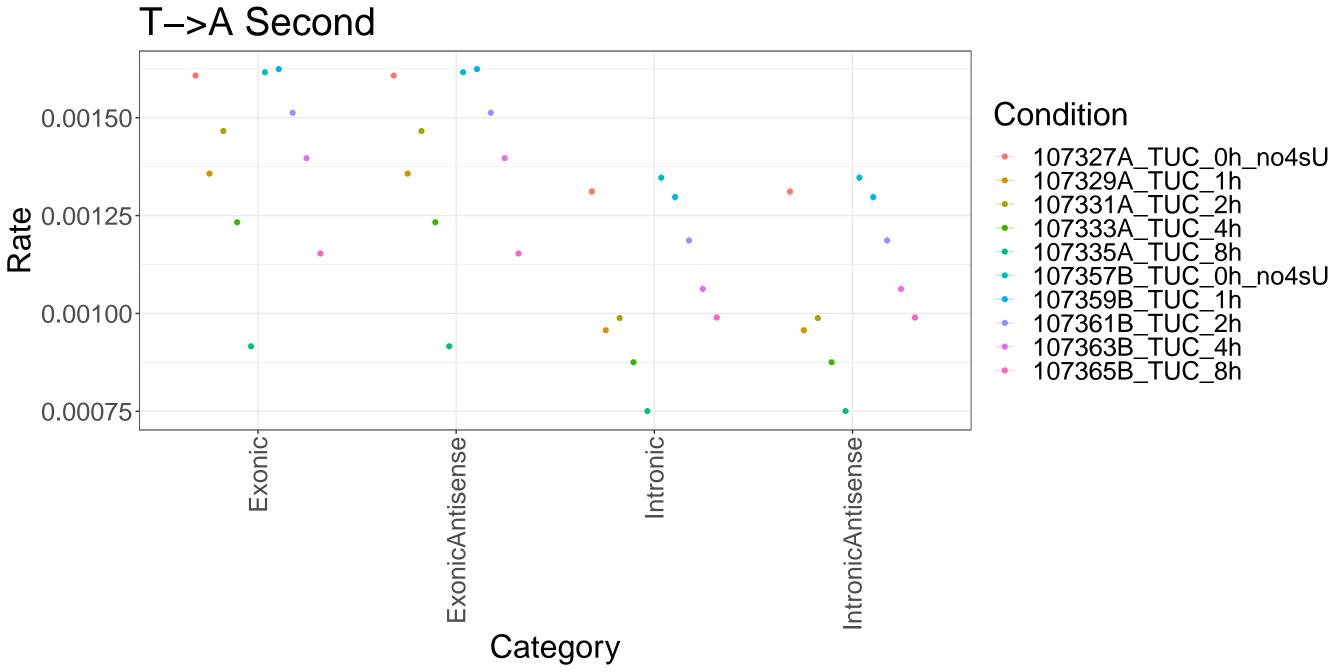
- 107327A\_TUC\_0h\_no4sU 107329A\_TUC\_1h 107331A\_TUC\_2h 107333A\_TUC\_4h

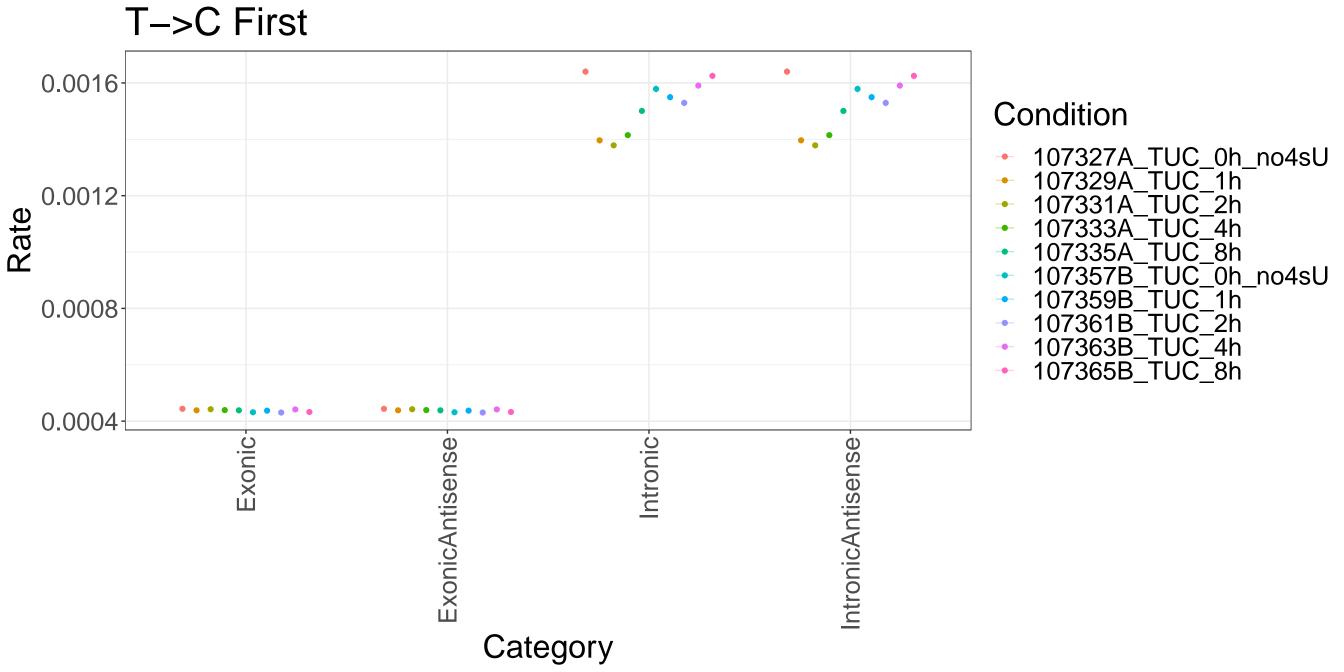
- 107335A\_TUC\_8h
- 107357B\_TUC\_0h\_no4sU 107359B\_TUC\_1h

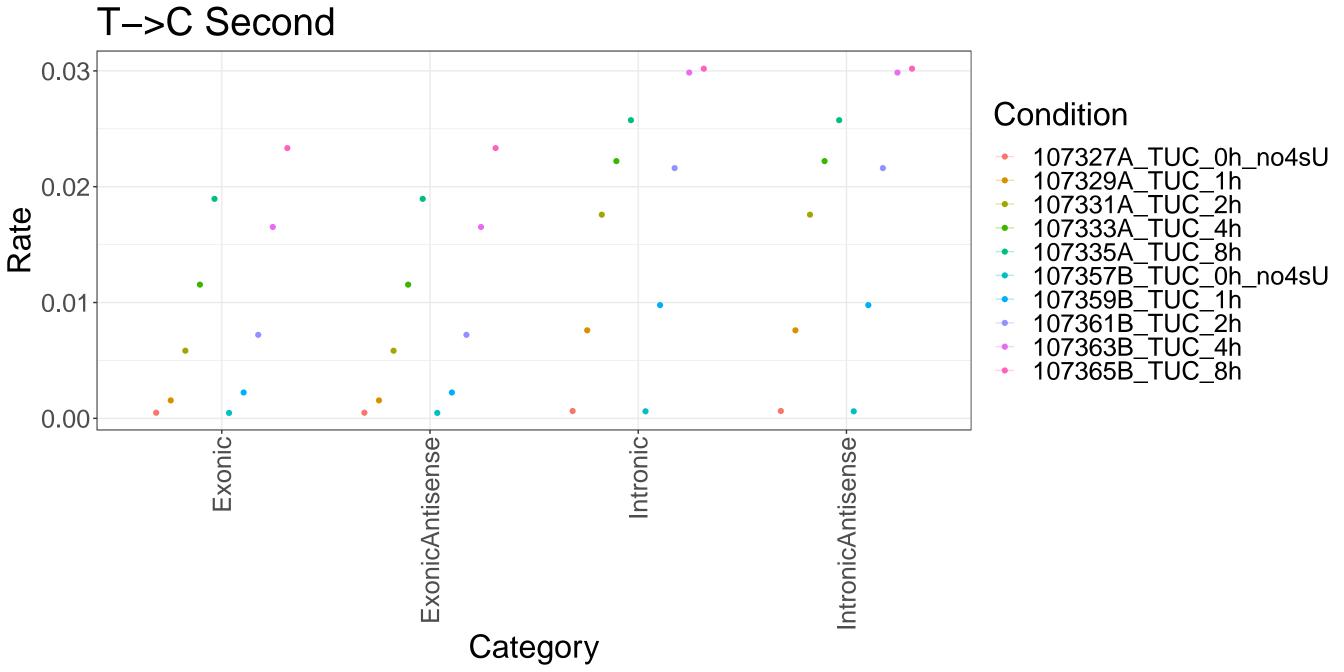
- 107361B\_TUC\_2h 107363B\_TUC\_4h
- 107365B\_TUC\_8h

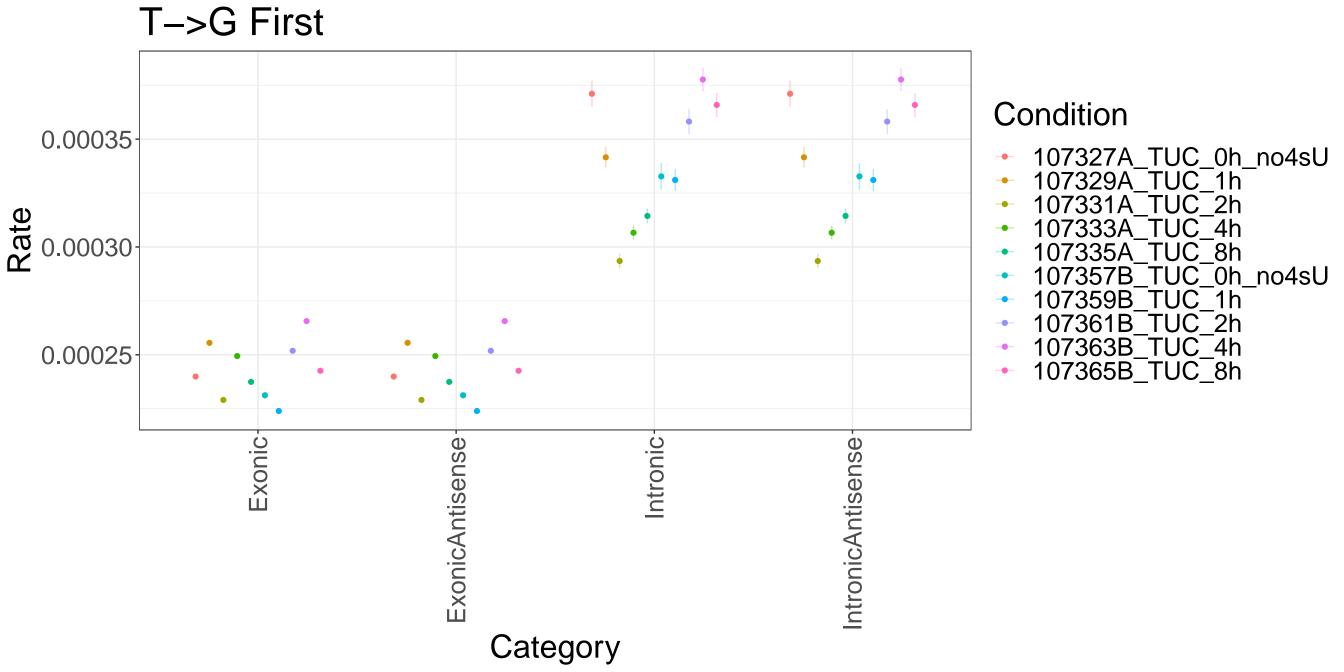
T->A First Condition 6e-04 107327A\_TUC\_0h\_no4sU 107329A\_TUC\_1h 107331A\_TUC\_2h 107333A\_TUC\_4h **Rate** (40) 107335A\_TUC\_8h 107357B\_TUC\_0h\_no4sU 107359B\_TUC\_1h 107361B\_TUC\_2h 107363B\_TUC\_4h 107365B\_TUC\_8h 4e-04 ExonicAntisense Intronic IntronicAntisense

Category







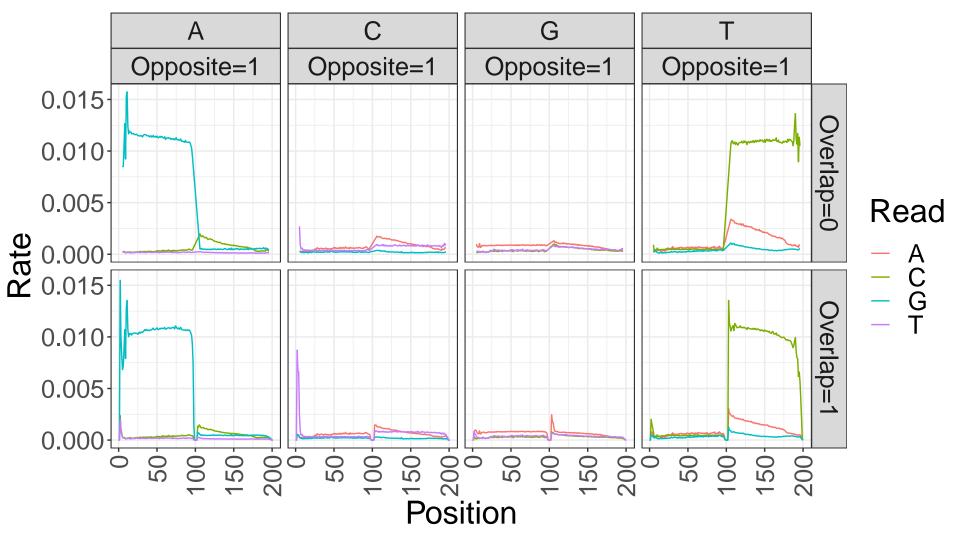


# T->G Second 0.00060 Condition 107327A\_TUC\_0h\_no4sU 107329A\_TUC\_1h 107331A\_TUC\_2h 107333A\_TUC\_4h 0.00055 **Rate** 0.00050-107335A\_TUC\_8h 107357B\_TUC\_0h\_no4sU 107359B\_TUC\_1h 0.00045 107361B\_TUC\_2h 107363B\_TUC\_4h 107365B\_TUC\_8h 0.00040 Intronic ExonicAntisense IntronicAntisense

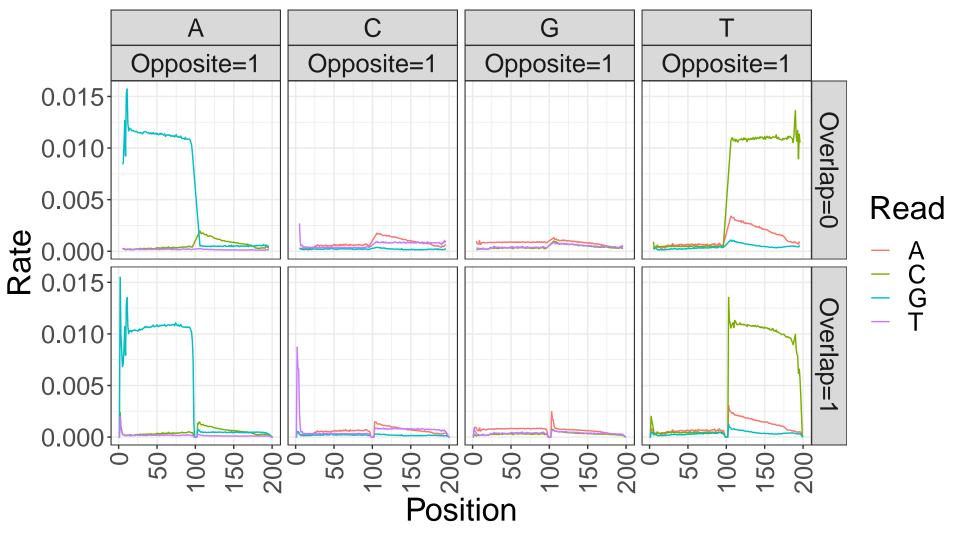
Category

## SLAM

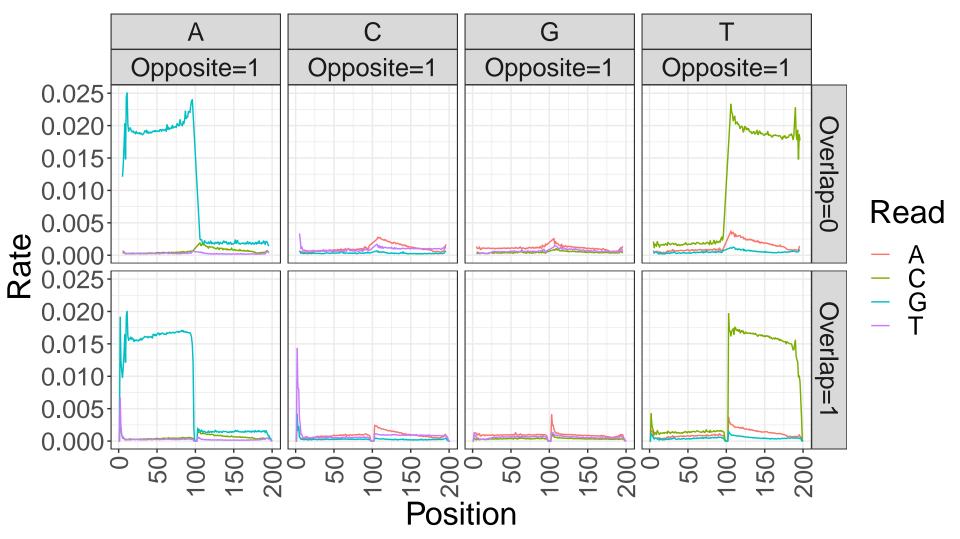
# Exonic



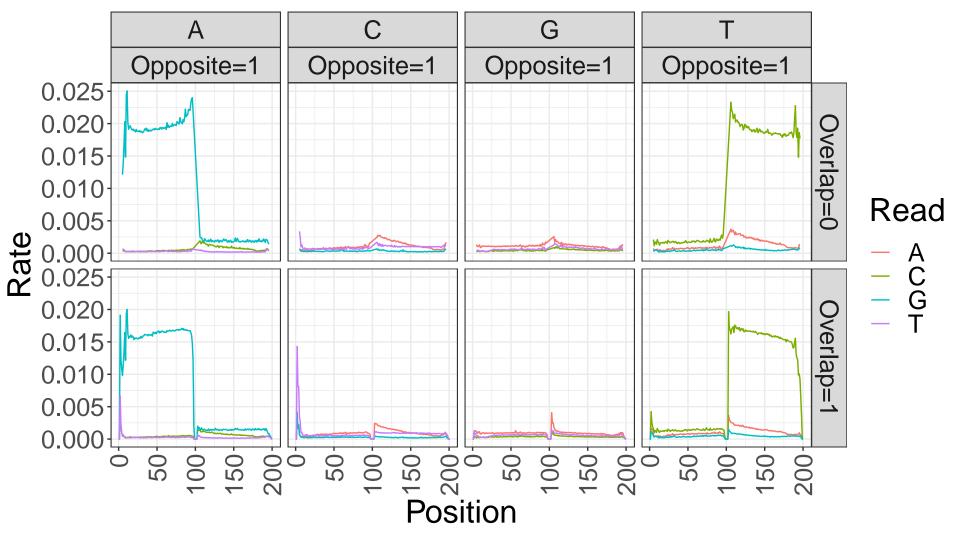
# ExonicAntisense



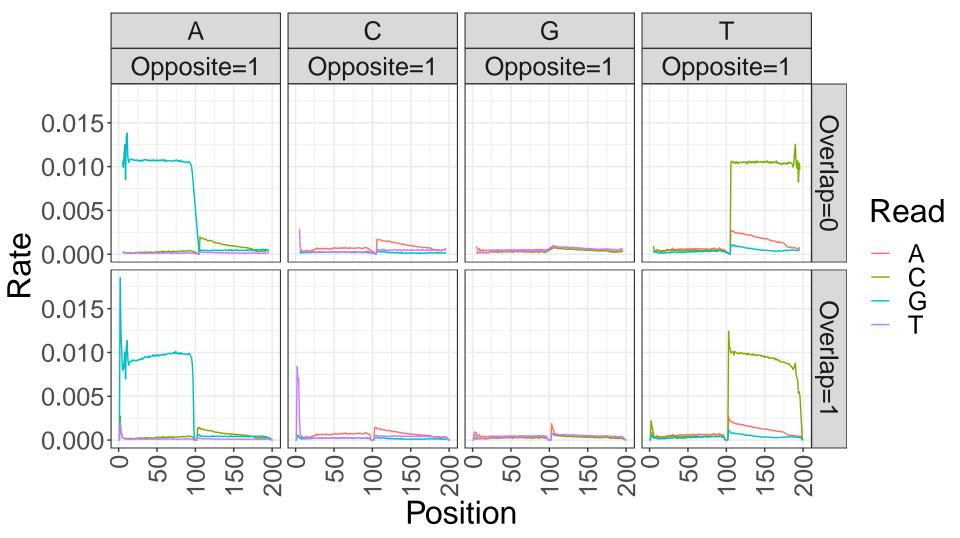
## Intronic



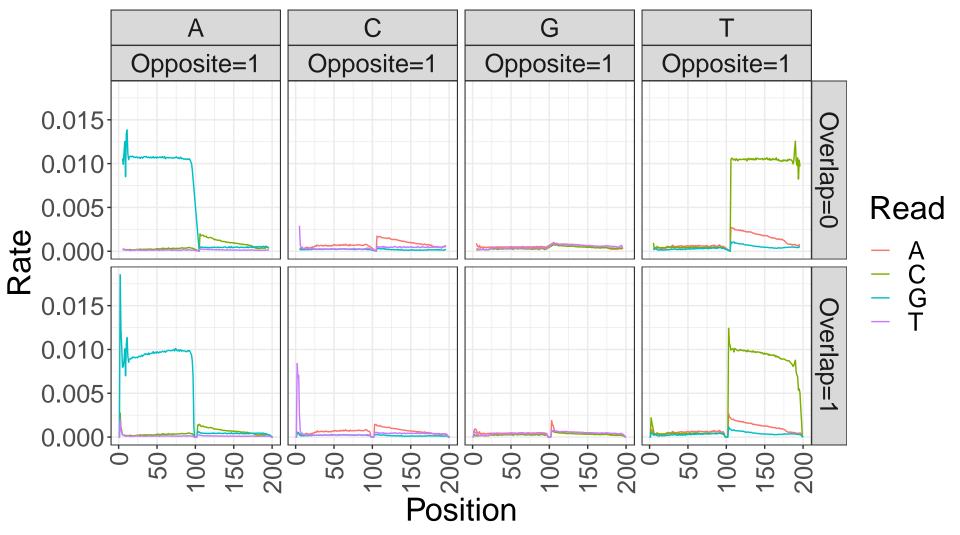
# IntronicAntisense



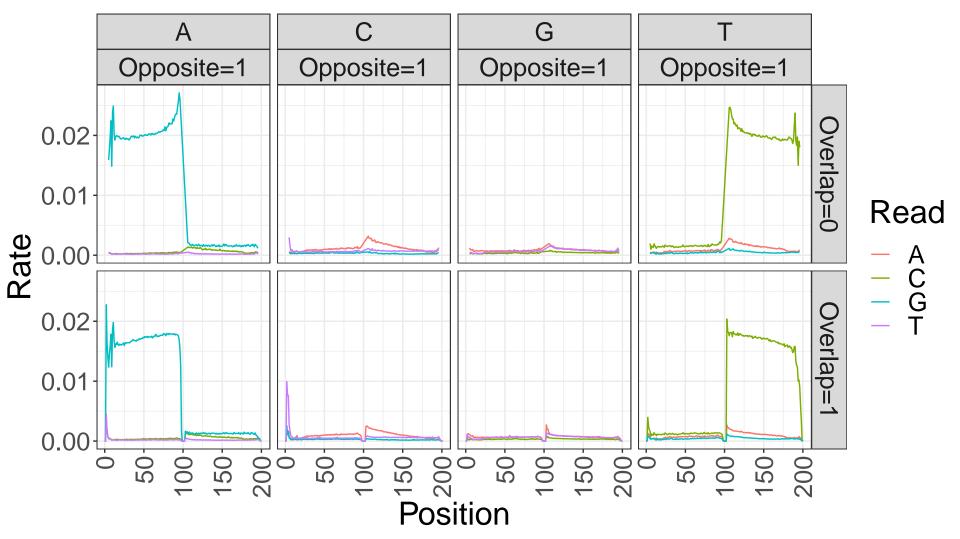
# Exonic



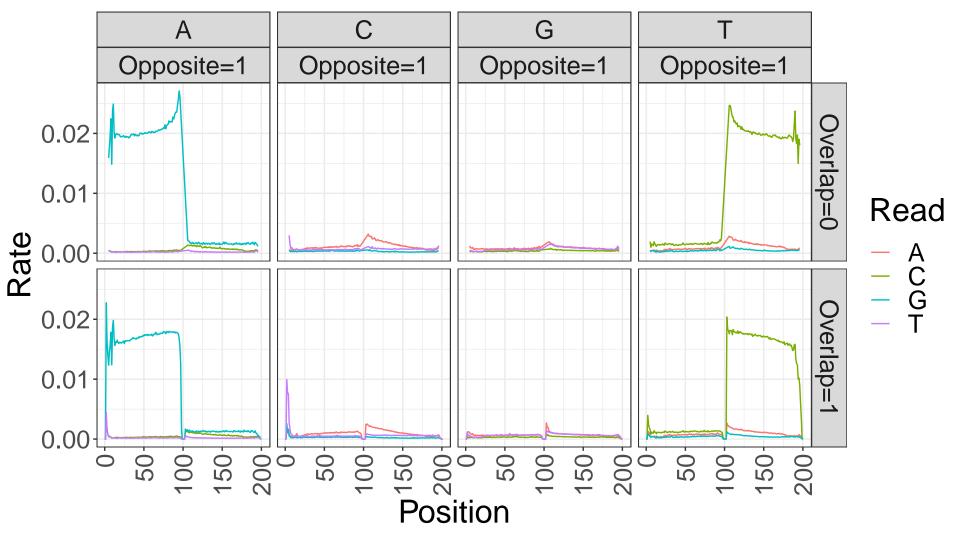
# ExonicAntisense



## Intronic

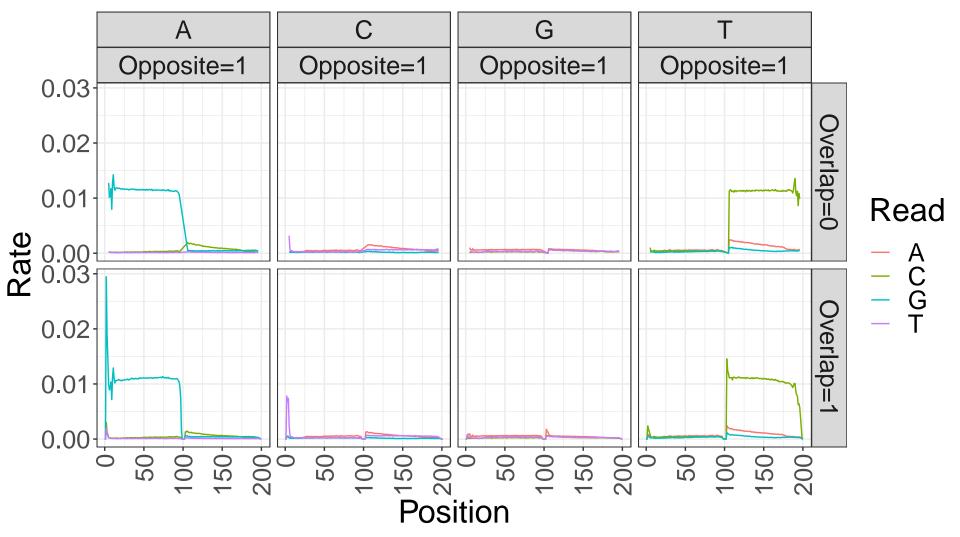


# IntronicAntisense

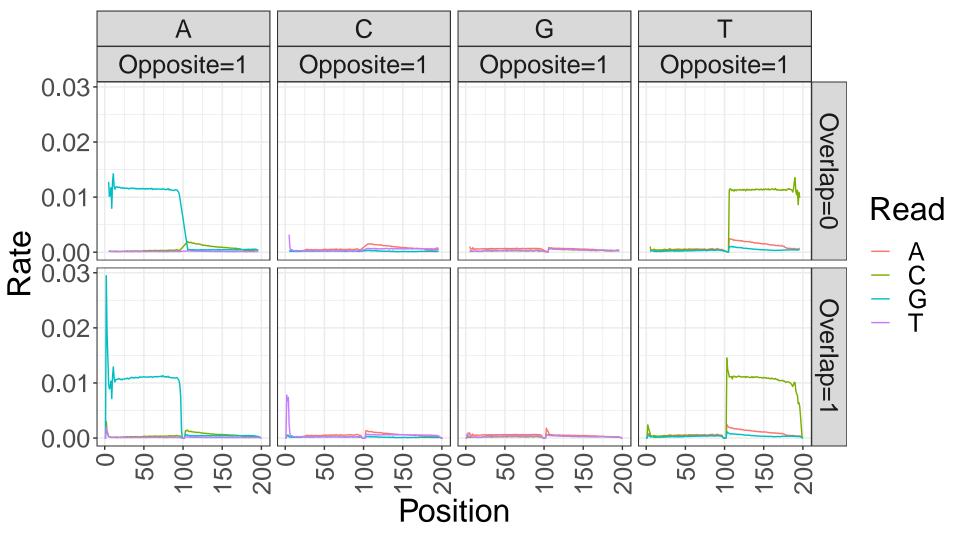


## TUC

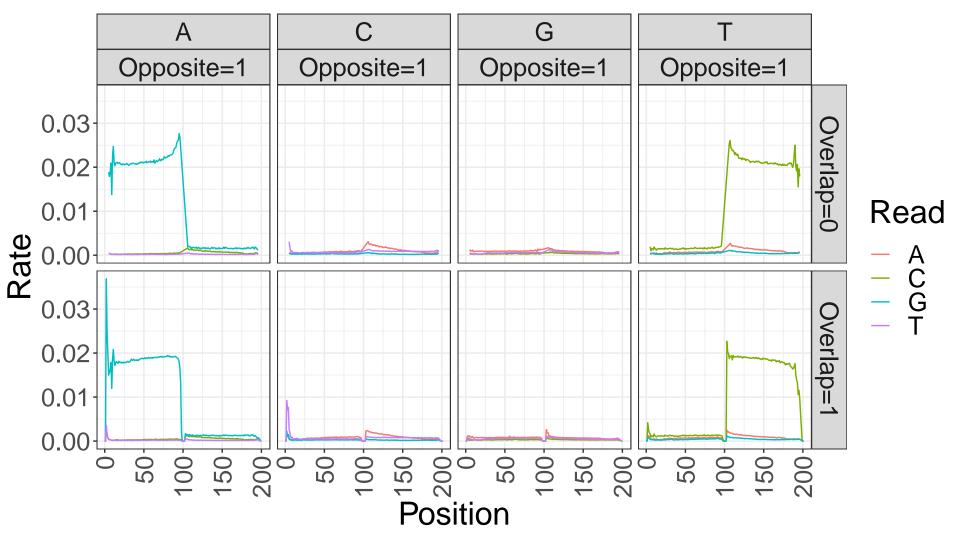
# Exonic



# ExonicAntisense



# Intronic



# IntronicAntisense

