

20V P-Channel Power MOSFET



SOT-23



Pin Definition:

- 1. Gate
- 2. Source
- 3. Drain

Key Parameter Performance

Parameter		Value	Unit	
V_{DS}		-20	V	
R _{DS(on)} (max)	$V_{GS} = -4.5V$	50		
	$V_{GS} = -2.5V$	65	m	
	V _{GS} = -1.8V	85		
Q_g		9.6	nC	

Features

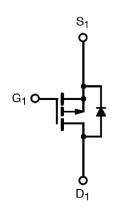
Fast Switching Suited for -1.8V Gate Drive Applications Halogen-free

Ordering Information

Part No.	Package	Packing			
TSM500P02CX RFG	SOT-23	3Kpcs / 7+Reel			

Note: %G+denotes for Halogen- and Antimony-free as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds

Block Diagram



P-Channel MOSFET

Absolute Maximum Ratings (T_C = 25°C unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V _{DS}	-20	V
Gate-Source Voltage		V_{GS}	±10	V
Continuous Prais Current	T _C = 25°C		-4.7	Α
Continuous Drain Current	T _C = 100°C	l _D	-3	Α
Pulsed Drain Current (Note 1)		I _{DM}	-18.8	А
Power Dissipation @ T _C = 25°C		P _D	1.56	W
Operating Junction Temperature		T _J	150	°C
Storage Temperature Range	·	T _{STG}	-55 to +150	°C

Thermal Performance

Parameter	Symbol	Limit	Unit	
Thermal Resistance - Junction to Ambient	R _{JA}	80	°C/W	

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Electrical Specifications (T_C = 25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Тур	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = -250\mu A$	BV _{DSS}	-20			V
Drain-Source On-State Resistance	$V_{GS} = -4.5V, I_D = -3A$	R _{DS(ON)}		42	50	m
	$V_{GS} = -2.5V, I_D = -2A$			57	65	
	$V_{GS} = -1.8V, I_D = -1A$			75	85	
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	V _{GS(TH)}	-0.3	-0.6	-0.8	V
Zero Gate Voltage Drain Current	$V_{DS} = -20V, V_{GS} = 0V$	I _{DSS}			-1	μΑ
	V _{DS} = -16V, T _J = 125°C				-10	
Gate Body Leakage	$V_{GS} = \pm 10V, V_{DS} = 0V$	I _{GSS}			±100	nA
Forward Transconductance (Note 2)	$V_{DS} = -10V, I_{D} = -3A$	g fs		7		S
Dynamic						
Total Gate Charge (Note 2,3)		Q_g		9.6		nC
Gate-Source Charge (Note 2,3)	$V_{DS} = -10V, I_{D} = -3A,$ $V_{GS} = -4.5V$	Q_{gs}		1.6		
Gate-Drain Charge (Note 2,3)		Q_{gd}		2		
Input Capacitance		C _{iss}		850		
Output Capacitance	$V_{DS} = -10V, V_{GS} = 0V,$ f = 1.0MHz	C _{oss}		70		pF
Reverse Transfer Capacitance	A = 1.0	C _{rss}		55		
Switching						
Turn-On Delay Time (Note 2,3)		t _{d(on)}		6		
Turn-On Rise Time (Note 2,3)	$V_{DD} = -10V, I_{D} = -1A,$	t _r		21.6		
Turn-Off Delay Time (Note 2,3)	V _{GS} = -4.5V, R _{GEN} =25	t _{d(off)}		51		ns
Turn-Off Fall Time (Note 2,3)		t _f		13.8		1
Source-Drain Diode Ratings and Ch	aracteristic					
Maximum Continuous Drain-Source	Integral reverse diode in the MOSFET	ı			-4.7	А
Diode Forward Current		Is		- -		
Maximum Pulse Drain-Source Diode		I _{SM}			-18.8	Α
Forward Current					4	
Diode-Source Forward Voltage	$V_{GS} = 0V, I_{S} = -1A$	V_{SD}			-1	V

Note:

- 1. Pulse width limited by safe operating area
- 2. Pulse test: pulse width m300µs, duty cycle m2%
- 3. Switching time is essentially independent of operating temperature.

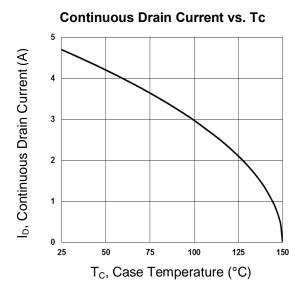
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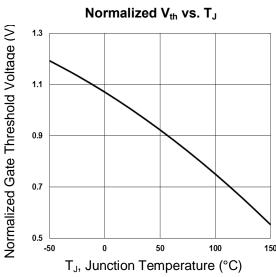


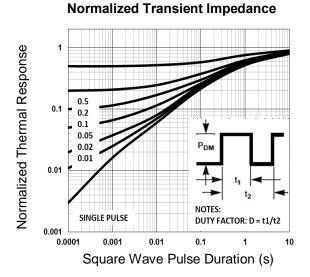
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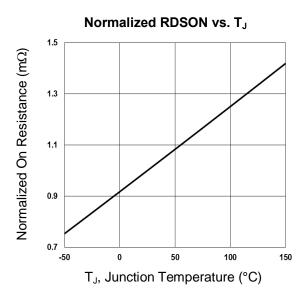
Pb ROHS COMPLIANT

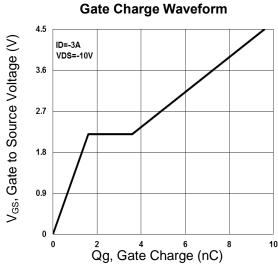
Electrical Characteristics Curves

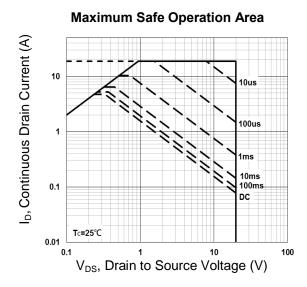












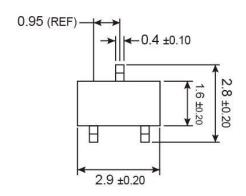
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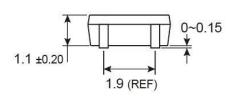


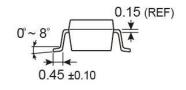
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SOT-23 Mechanical Drawing







Unit: Millimeters

Marking Diagram



50 = Device Code

Y = Year Code

M = Month Code for Halogen Free Product
(O=Jan, P=Feb, Q=Mar, R=Apl, S=May, T=Jun, U=Jul, V=Aug, W=Sep, X=Oct, Y=Nov, Z=Dec)

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L = Lot Code

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TSM500P02CX 20V P-Channel Power MOSFET



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