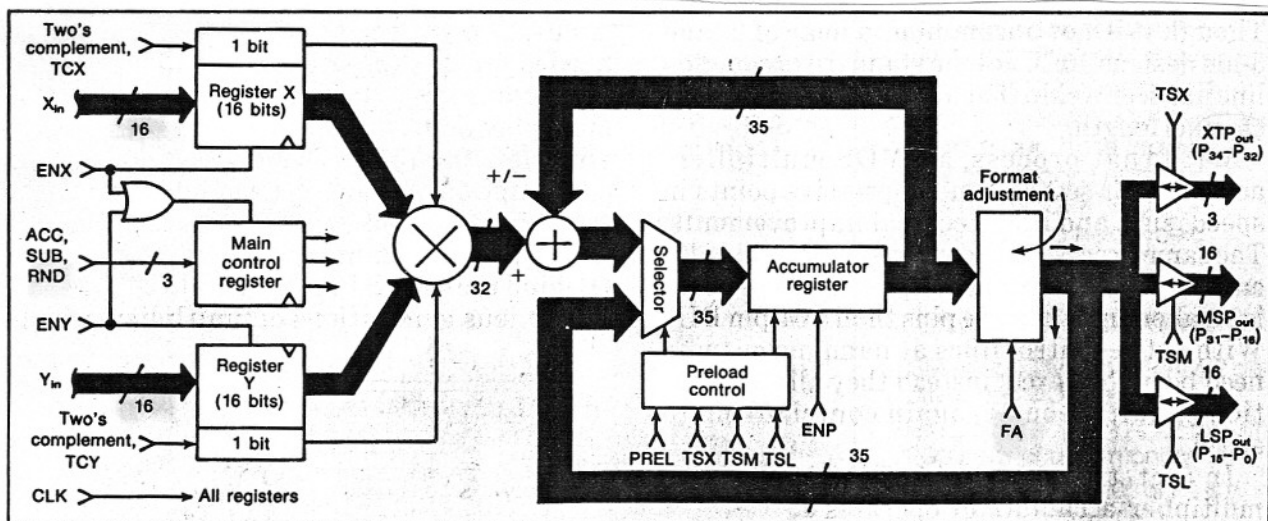
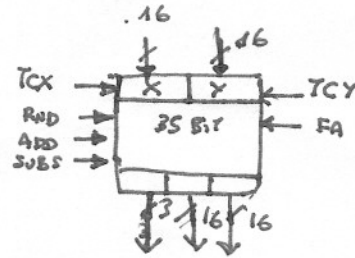


- 3 PORT MAC
  - 88 Pin
  - MASTER CLOCK (instead of signal edge)
  - Preloading a constant into the o/p register.
- It increases the bandwidth for the  $(A \times B + C)$  operations.



1. Capable of multiplying two 16-bit numbers, the TMC2160 multiplier-accumulator accepts inputs in either two's complement or unsigned magnitude format as determined by its two's complement control lines, TCX and TCY. The unique format adjustment section allows a designer to position the binary point to match system requirements. The three-state output lines are under the control of the TSX, TSM and TSL inputs, which respectively deal with the extended, most significant, and least significant products.