The case for HCM instead of dual mac to do Complex mul

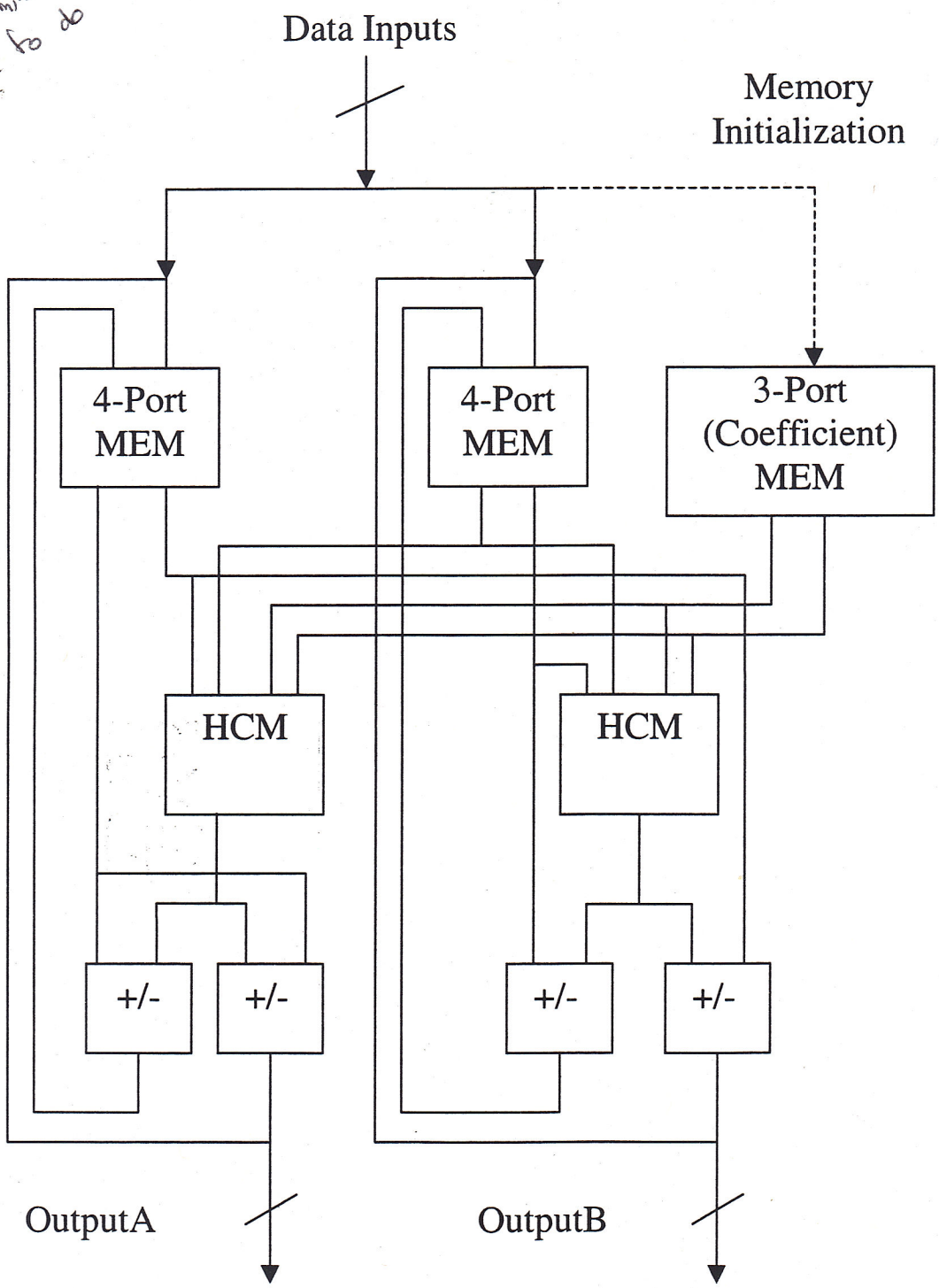

Figure 6. Quad MAC Single-Cycle Complex Multiply Architecture using Dual HCM-MACs

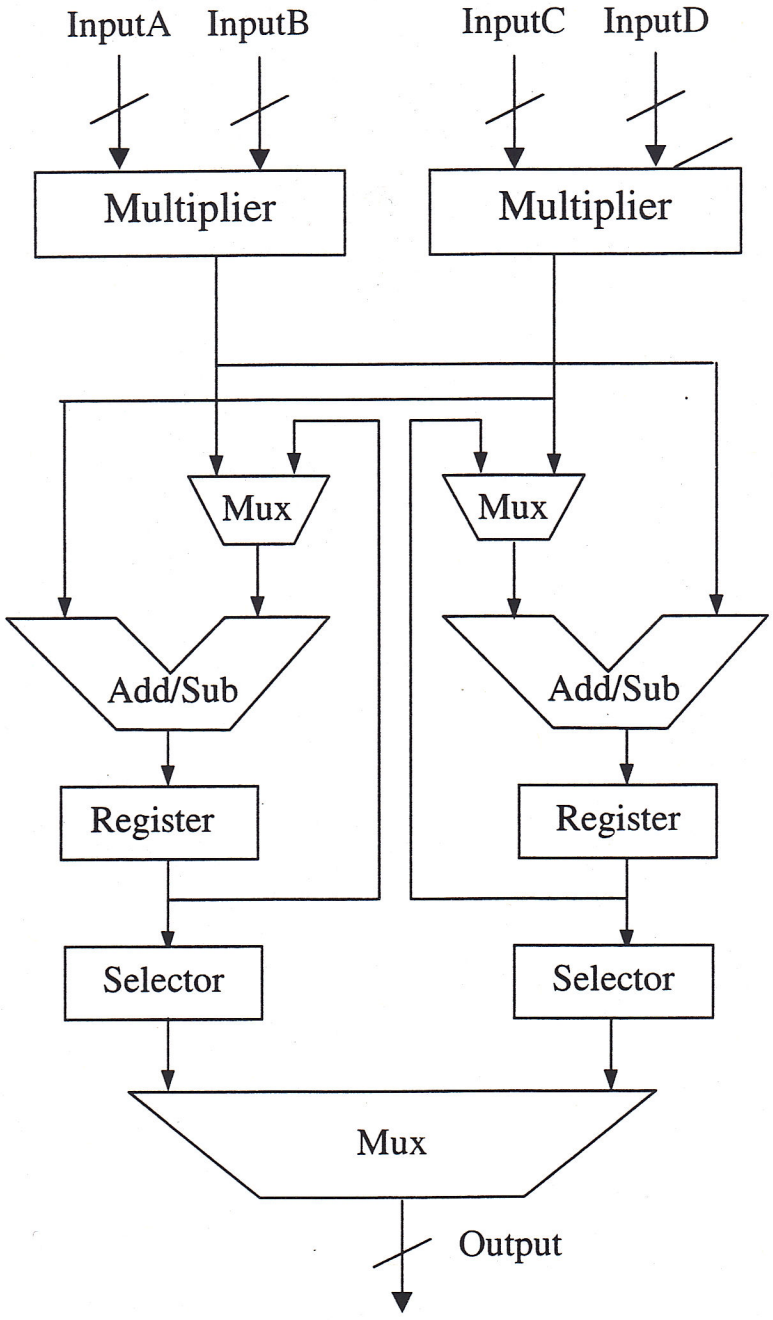

Figure 5. Dual Multiplier / Dual Accumulator (D/D-MAC) Execution Unit

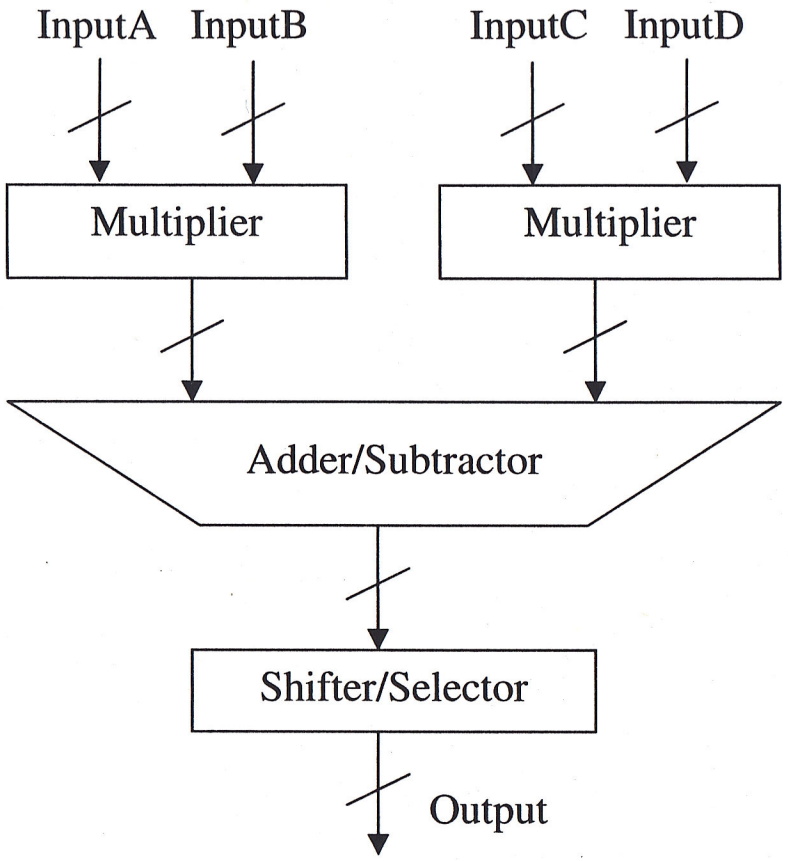

Figure 4. Half Complex Multiply (HCM-MAC) MAC Execution Unit