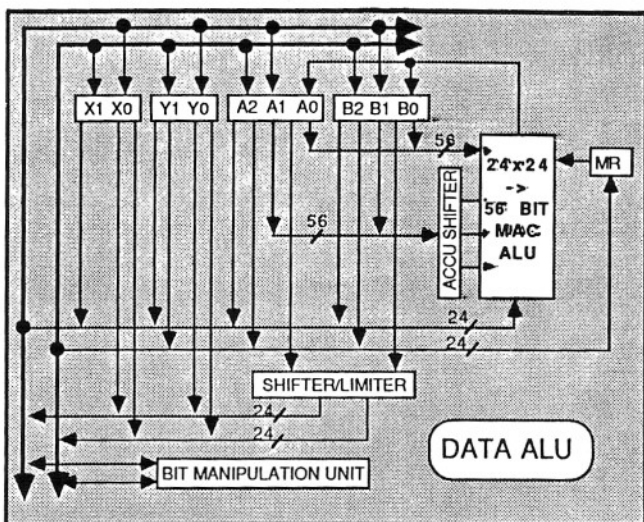


DATA ALU FEATURES



- 24X24 ->56 Bit single-cycle multiply-accumulation unit,
- Two 56 Bit Independent Accumulators With Extension Adder,
- Four 24 bit data registers,

mac x0,y0,a x:(r0)+,x0 y:(r4)-,y0

- Bit manipulation unit
JSCLR #n,x:<addr>

- Shifter/limiter unit

Flexible overflow & scaling control