ARM System-on-Chip Architecture (2nd Revised edition)

By Steve Furber

Pearson Education (US). Paperback. Book Condition: new. BRAND NEW, ARM System-on-Chip Architecture (2nd Revised edition), Steve Furber, The future of the computer and communications industries is converging on mobile information appliances - phones, PDAs, laptops and other devices. The ARM is at the heart of this trend, leading the way in system-on-chip (SoC) development and becoming the processor core of choice for many embedded applications. System-on-chip technology is changing the way we use computers, but it also sets designers the very challenging problem of getting a complex SoC design right first time. ARM System-on-Chip Architecture introduces the concepts and methodologies employed in designing a system-on-chip based around a microprocessor core, and in designing the core itself. Extensive illustrations, based on the ARM, give practical substance to the design principles set out in the book, reinforcing the reader's understanding of how and why SoCs and microprocessors are designed as they are. ARM System-on-Chip Architecture: - presents and discusses the major issues of system-on-chip design, including memory hierarchy, caches, memory management, on-chip buses, on-chip debug and production test - provides an overview of the ARM processor family, enabling the reader to decide which ARM is best for the job in hand -...

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