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Lab 05

Submission Date - October 3, 2016

Deadline - Oct 03, 11:59 PM

1 In the Helgrind tutorial link, familiarize yourself with the use for:

1. Data race condition

A data race occurs when multiple threads access a shared memory location with undetermined accessing order and at least one access is to write a new data into the shared memory location. In the program data race condition occurs when we want to increment same count with multiple threads.

2. Ordering in POSIX locks

`-track-lockorders=no—yes` [default: yes]

When enabled (the default), Helgrind performs lock order consistency checking. For some buggy programs, the large number of lock order errors reported can become annoying, particularly if you're only interested in race errors. You may therefore find it helpful to disable lock order checking.

3. POSIX misuses detection

there are many events when threads run on invalid time such as:

1. unlocking an invalid mutex
2. unlocking a not-locked mutex
3. unlocking a mutex held by a different thread
4. destroying an invalid or a locked mutex
5. recursively locking a non-recursive mutex

2 - refer to the attached file

1 verify the following

1. the put runs faster with 4 cores than 2 cores, but not 4 times as fast as a single core.

```
hemant@hemant:~$ cd Downloads
hemant@hemant:~/Downloads$ gcc -g -o valgrind_1 valgrind_1.c -pthread
hemant@hemant:~/Downloads$ ./valgrind_1 2
completion time for put phase = 1.835714
0: 22 keys missing
1: 22 keys missing
completion time for get phase = 3.561289
hemant@hemant:~/Downloads$ ./valgrind_1 4
completion time for put phase = 1.407124
1: 26 keys missing
2: 29 keys missing
3: 28 keys missing
0: 29 keys missing
completion time for get phase = 3.762213
hemant@hemant:~/Downloads$
```

as we can see in the figure that is the result when the threads are run for 2 threads and 4 threads, the time taken on 4 threads is less but not the half of the time taken when 4 threads are used. this is happening when there occurs the data race condition as multiple threads tries to get the lock but the threads have to wait for the other thread to unlock the mutex lock. so more the threads more will be the race condition occurred.

2. the get phase ran even slower

because of the more missing keys it took more time to get the access to the hash table and read the data. Also there were times when the mutex lock could not happen. Because of this mutex lock more conflicts are coming with the threads. The application inserted 22+22 keys in phase 1 that phase 2 couldn't find.

2 Use Valgrind to find race condition. Using mutex lock to fix the race condition. Argue that your changes ensures correctness

```
==7280== This conflicts with a previous write of size 4 by thread #2
==7280== Locks held: none
==7280==    at 0x400CAB: put (valgrind_1.c:65)
==7280==    by 0x400E6F: put_thread (valgrind_1.c:100)
==7280==    by 0x4C30FA6: ??? (in /usr/lib/valgrind/vgpreload_helgrind-amd
ux.so)
==7280==    by 0x4E45181: start_thread (pthread_create.c:312)
==7280==    by 0x515630C: clone (clone.S:111)
==7280== Address 0x33c8d28 is 48000008 bytes inside data symbol "table"
==7280==
```

As we can see here that the two threads are conflicting to get the access to the table to put the data to the hash table but because there is no ordering sequence that is causing no access to both the threads.

We can solve this problem by putting the lock for access before each thread that gets access to the table to update the data and then unlock the access for the other threads to get access.

```
put_thread(void *xa)
{
    assert(pthread_mutex_lock(&lock) == 0);
    long n = (long) xa;
    int i;
    int b = NKEYS/nthread;

    for (i = 0; i < b; i++) {
        put(keys[b*n + i], n);
    }
    assert(pthread_mutex_unlock(&lock) == 0);
}
```

Here we have taken mutex lock for the process that i have described above the figure.

3 Using “cachegrind” and “kcachegrind”, identify the most expensive function calls in terms of cycles spent. Instructions for the tools are available in Lab 4. Attach screen-shots whenever necessary.

1. cycles in the put function

put				
Types	Callers	All Callers	Callee Map	Source Code
#	CEst	CEst	Source ('/home/hemant/Downloads/valgrind_1.c')	
54			// Insert (key, value) pair into hash table.	
55			static	
56			void put(int key, int value)	
57	0.00	0.00	{	
58	0.00	0.00	int b = key % NBUCKET;	
59			int i;	
60			// Loop up through the entries in the bucket to find an unused one:	
61	7.94	7.94	for (i = 0; i < NENTRY; i++) {	
62	42.01	42.01	if (!table[b][i].inuse) {	
63	0.01	0.01	table[b][i].key = key;	
64	0.00	0.00	table[b][i].value = value;	
65	0.00	0.00	table[b][i].inuse = 1;	
66	0.00	0.00	return;	
67			}	
68			}	

This is the screen shot showing that the if statement after the for loop is taking the maximum cycles as it has to check on every thread call whether the particular location is available or not to read the data.

2. cycles in the get function

get			
Types	Callers	All Callers	Source Code
#	CEst	CEst	Source ('/home/hemant/Downloads/valgrind_1.c')
81	42.01	42.01	if (table[b][i].key == key && table[b][i].inuse) {
80	7.94	7.94	for (i = 0; i < NENTRY; i++) {
77	0.00	0.00	int b = key % NBUCKET;
82	0.00	0.00	v = table[b][i].value;
75	0.00	0.00	{
76	0.00	0.00	assert(pthread_mutex_lock(&lock) == 0);
87	0.00	0.00	assert(pthread_mutex_unlock(&lock) == 0);
89	0.00	0.00	}
88	0.00	0.00	return v;
79	0.00	0.00	int v = -1;
83	0.00	0.00	break;
72			// Lookup key in hash table. The lock serializes the lookups.
73			static int
74			get(int key)
78			int i;

In put function also the if condition which is running after the for loop is taking the maximum cycles of the other functions.

But both the places are taking estimated cycles and are equal. So we can not distinguish between these positions.

Thank You