

DILIP TEJA

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ACADEMIC QUALIFICATION

National Institute of Technology, Warangal

Bachelor of Technology in Electronics and Communication - 7.4/10

Warangal, India

Jun 2016 – May 2020

Sri Chaitanya Institutions

Intermediate (+1 and +2) – 98.4/100

Hyderabad, India

May 2016

Sri Chaitanya School

Class X – 9.7/10

Hyderabad, India

May 2014

RELEVANT COURSES

• Computer Programming • Data Structures and Algorithms • Computer Networks • Computer Architecture and Organisation • Object-Oriented Programming • Operating systems • Optimization Techniques • Engineering Mathematics • Digital System Design • Probability Theory • Digital Signal Processing • Microprocessors

WORK EXPERIENCE

Qualcomm India Private Limited

Software Engineer

Hyderabad, India

Dec 2021 – Present

- Responsible for validating Linux Kernel and Linux Security components for Qualcomm IoT chipsets (Wearables, Connected Cameras, Industrial IoT products) and part of five successful product releases.
- Developed and integrated logging mechanism for stability test framework using Python, which provides MTBF reports.
- Created more than 10 process improvement designs out of which four are in execution phase.

Associate Software Engineer

Jun 2020 – Nov 2021

- Built Kernel Automation Framework and automated more than 200 test cases (Scheduler, Memory) using **Python, OOPS, and Design Patterns**.
- Developed Web Portal having more than 100 active users using **Django, Javascript, Ajax, Bootstrap, PostgreSQL** and deployed using Apache server.
- The Web App provides data analytics and product information improving the team's productivity.

INTERNSHIP EXPERIENCE

Qualcomm India Private Limited

Interim Engineering Intern

Hyderabad, India

Apr 2019 – Jun 2019

- Developed end-to-end Automation for a nightly trigger of test jobs, which improved the resource utilization by 30%.
- Developed a Web App as a one stop solution for the team to check and analyze test reports, improving efficiency.
- Implemented the App using **Python Flask, Ajax, Bootstrap, PostgreSQL** and deployed using Apache server.

Indian Institute of Science, Bangalore (IISc)

Research Intern — Guide: Dr. Amarjot Singh, Founder & CEO at SkyLark Labs Inc.

Bangalore, India

Apr 2018 – Jun 2018

- Research on Transfer Learning and its applications.
- Trained a ResNet model with augmented data for detecting and classifying gender and age.

RELEVANT PROJECTS

Underage Kids Identification | Deep Learning, Keras

Aug 2018

- Developed a Compact Stagewise **Regression Network** and Face Recognition using **Incremental Classifier**.
- This model detects or recognizes underage kids to prevent them from performing restricted activities.
- Performed fine-tuning of a pre-trained model with augmented data and could achieve around 89% accuracy.

Online Voting System | Blockchain

Oct 2019

- Developed Blockchain and implemented **Smart Contracts** for voting in elections to prevent malpractices.
- Accomplished as part of Microsoft CodeFunDo++ competition and secured **second place** across the college.

Line Follower | *Arduino*

Oct 2017

- Designed a line follower using Arduino UNO as part of the Hackathon.
- The prototype follows a particular line detected using an Arduino board and its attached sensors.

A Novel OFDM scheme for Visible Light Communications | *Matlab*

Nov 2019

- Implemented a spectral efficient OFDM scheme using the **Index Modulation** technique.
- Monitored a spectral gain of 0.875 bits/s/Hz with OFDM-IM compared to conventional OFDM.
- Spectral gain of OFDM-IM improved data transfer rates compared to conventional VLC techniques.
- Worked under the guidance of Prof V.V Mani, NIT Warangal

TECHNICAL SKILLS

Languages:	Python, C, C++, Java(basics), Matlab(Basics)
Web Development:	Django, HTML/CSS, Javascript, Ajax, JQuery, PostgreSQL
Developer Tools:	VS Code, Jupyter Notebook, Pycharm, Google Colab
Technologies/Frameworks:	Linux Kernel, Keras, Git, GitHub

ACHIEVEMENTS/CO-CURRICULAR/EXTRACURRICULAR

- Recipient of "Orion Insta Award" from Senior Director at Qualcomm for outstanding contribution and efforts. 2021
- Three time recipient of "Passionate Executor Award" at Qualcomm for driving execution within deadlines. 2021
- Achieved **All India Rank 602** in IIT-JEE Mains among over 1.2 million candidates. 2016
- Secured **State Rank 512** in TS-EAMCET organized under Ministry of HRD, **Govt. of Telangana**. 2016
- Secured **State Rank 1206** in AP-EAMCET organized under Ministry of HRD, **Govt. of Andhra Pradesh**. 2016
- Professional Badminton Player, participated in state level tournaments. 2012 - Present

LEADERSHIP ROLES

National Institute of Technology, Warangal

Warangal, India

Inter-NIT Championships

Jun 2018 - Jun 2020

- Led college Badminton Team as a captain to quarter-finals.

Batch Representative

Aug 2019 - Mar 2020

- Represented the ECE Class of 2020 in the Department UG Council and other department events.

Sponsorship and Public Relations Sub-Coordinator

Aug 2018 - Mar 2019

- Responsible for organizing the college's cultural festival 'Spring Spree' and dealing with various sponsors, notably Harvard Ascend and GameRoom.

Electronics Club Team Member

Aug 2017 - May 2018

- Involved in conducting various events, notably the Arduino Workshop and the Circuits Workshop.