Council for Technical Education and Vocational Training

## Office of the Controller of Examinations

Sanothimi, Bhaktapur

## Regular/Scholarship Exam-2080, Bhadra

Diploma in IT / Computer Engg. Program: Full Marks: 80 Year/Part: II/I (2022) Pass Marks: 32 Basic Electrical & Electronics Subject: Time: 3 hrs. Engineering Candidates are required to give their answers in their own words as far as practicable. The figures in the margin indicate full marks. www.arjun00.com.np Attempt Any Five questions. Define electromotive Force and potential difference. [4] Explain Kirchhoff's voltage law with suitable example. Using Thevenin's theorem find the current through C) [8]  $12\Omega$  resistor from below circuit. ₩₩  $3\Omega$  $4\Omega$ 36V 6Ω≸  $12\Omega$ State and prove maximum power transfer theorem. [4] Define the following forms. b) [4x2=8]i) Instantaneous value ii) Average value iv) RMS value iii) Peak value Explain VCVS and CCVS. [4] Derive the expression for the r.m.s. and average value of sinusoidal current and voltage. A 230V, 50Hz AC supply is applied a coil of 0.06H [4x2=8]b) inductance and  $5\Omega$  resistance connected in series with a capacitor of 10µF. Calculate the following. ii) Circuit current i) Impedance

> principle of half-wave rectifier with waveform. www.arjun00.com.np Cont.....

What do you mean by rectifier? Explain the working

iv) Active power

[8]

iii) Power factor

	b)	What do you mean by transistor? Explain BJT as a switch with neat diagram.	[8]
5.		Explain the construction and working of CMOS.	[8]
	b)	What do you mean by operational amplifier? Explain	
		the ideal and roof observed in amplifier? Explain	[8]
		the ideal and real characteristics of operational	
		amplifier.	
6.	Write	e short notes on : (Any Four)	
•		Any Four)	[4x4=16]
	a) N	forton's theorem b) Semiconductor diodo	
		o) bounconductor drode	
	C) D	JT as a logic gate  d) MOSFET	
	e) C	lipper circuit	
		Good Luck!	

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