Council for Technical Education and Vocational Training



Office of the Controller of Examinations

Sanothimi, Bhaktapur

Regular/Scholarship Exam-2080 Bhadra

Program: Diploma in Information Technology/

Full Marks: 80

Computer Engineering

Year/Part: II/I (2022)

Pass Marks: 32

Subject: Digital Logic

Time: 3 hrs.

Candidates are required to give their answers in their own words as far as practicable. The figures in the margin indicate full marks.

Attempt any FIVE questions. WWW.arjun00.com.np

- 1. a. Define signal. Differentiate between analog and digital signal [1+3] with suitable example.
 - b. Convert the following number system:

[4×2]

i. $(10111.01)_2 = (?)_{10}$

- ii. $(3471)_8 = (?)_{16}$
- iii. $(BCDE.4A)_{16}=(?)_8$
- iv. $(3567.350)_{10} = (?)_2$
- c. Perform the following operation:

 $[2\times2]$

- i. Multiply: (11101.11*101)₂
- ii. Divide: (110100.110/110)₂
- a. Subtract the following (11001100)₂ from (11110000)₂ using [4]
 2's complement.
 - Realize basic gate using NAND gate only with clear diagram [4+4] and truth table. Also, state and prove De-Morgan's Theorem in brief.
 - c. Explain XOR and NOR gate with truth table and symbol. [4]
- 3. a. Simplify the following expression using Boolean algebra: [4×2]
 - i. A'B'C' + A'BC' + AB'C' + ABC' = C'
 - ii. A(A' + C)(A'B + C)(A'BC + C') = 0
 - b. Simplify the following expression using k-map. $[4\times2]$
 - i. $\sum f(A, B, C, D) = \pi M(2, 3, 4, 5, 7, 10, 11, 14) + \sum d(0, 1, 6, 15)$ Draw logic diagram.
 - ii. $\sum f(A,B,C,D) = \sum m (0,1,4,8,11,12,15) + \sum d (2,3,5,6,7)$ Draw logic diagram. WWW.arjun() com.np
- 4. a. Define encoder. Explain the decimal to binary encoder with [2+6] suitable diagram and truth table.

| | b. | Differentiate between combinational and sequential circuit | [4] |
|----|----|--|-------|
| | | with example. | |
| | c. | Design 1:4 De-multiplexer with clear circuit diagram and | [4] |
| | | truth table in brief. | |
| 5. | a, | Design RS flip-flop with necessary diagram. Write the | [6+2] |
| | | advantages of JK flip-flop. | . , |
| | b. | Define shift register. Explain the operation of ripple counter | [2+6] |
| | | with clear diagram. | . , |
| 6. | Wı | rite short notes on: (any FOUR) | [4×4] |
| | a. | 7 segments display | . , |
| | b. | SIPO shift register | |
| | c. | D flip-flop | |
| | d. | Half adder | |
| | e. | ASCII code | |

Good Luck!

f. BCD code

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