

UNIVERSITY OF MORATUWA, SRI LANKA

Katubedda, Moratuwa 10400,

Sri Lanka.

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Serial Number: A 015498

ACADEMIC TRANSCRIPT

Full Name

: Maddage Dilshan Deshapriya Perera

Registration No : 170439T

Date of Birth

: 15 March 1997

Gender

: Male

Degree Awarded

: Bachelor of the Science of Engineering Honours

Field of Specialization: Electrical Engineering

Date of Graduation

: 31 July 2022

Overall GPA

: 3.19

Academic Standing

: Second Lower

This course is conducted entirely in the English medium.

Deputy Registrar - Examinations

(Not valid without the embossed seal)

15-November-2022

Full Name : Maddage Dilshan Deshapriya Perera

Registration No : 170439T Date of Birth : 15 March 1997 Gender : Male

Field of Specialization : Electrical Engineering

Module Title 9 Semester 4 (Aug 2019 - Jan 2020) Meditation Circuit Theory Electrical Machines & Drives I Power Systems I Laboratory Practice III Individual Project Communication and Presentation Skills Linear Algebra Graph Theory	BB-B-A-A-CC	2.0 2.0 2.0 2.0 2.0	Non GPA
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Circuit Theory Electrical Machines & Drives I Power Systems I Laboratory Practice III Individual Project Communication and Presentation Skills Linear Algebra	B- A- B+ A	2.0 2.0 2.0 1.0	WA DE
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Linear Algebra	KI ERCT OF	2.0	\$A → }
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Graph Theory	C+	2.0	- -
	A+	2.0	VA CAN
Basic Thermal Sciences and Applications	Α-	3.0	th (11). To the
Entrepreneurial Leadership	Α-	2.0	14 3.9. 14
0 Semester 5 (Feb 2020 - Sep 2020)	NUFRENCH VERN NUFRENCH VERN NUFRENCH VERN	HERATUS VICEATUS	VA SRI Na SRI
Control Systems I	*	2.0	V 1
Electrical Machines & Drives II	C	2.0	41. (1) (1) (1) (4) -
Power Systems II	В	2.0	11.77 14.27 14.4
Electrical Installations I	В-	2.0	(433) (435) (445)
Laboratory Practice IV	A+	1.0	in Sel Verille Verille
Applied Statistics	Α-	2.0	idea Na 64 Na - 80
Numerical Methods	A ^	2.0	14 55. 14 55. 14 - .
Business Economics and Financial Accounting	Α-	3.0	14 × × × × × × × × × × × × × × × × × × ×
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0 Semester 6 (May 2021 - Dec 2021)	MANEROLD CALL MANEROLD CALL	AL PATES MIRALIN	VALUE VALUE VALUE
Energy Systems	NATA AND A	2.0	λΑ -
Automation & Control Technologies	N. A. C.	2.0	100 000 100 000 104 4
Electrical Installations II	November Mil November voer Kiva B art oer	2.0	NG 580 NA 580 NA 5 40
Computer Aided Design & Simulation		2.0	4043. 3433. 44 5 .
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1 Semester 7 (Jul 2021 - Dec 2021)	V-12-2-2-2-2-2-2-2-2-2-2-2-2-2-2-2-2-2-2	WORKEN WORKEN	MATS.
	B+	2.0	AA SEL
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2	Electrical Installations II Computer Aided Design & Simulation	Electrical Installations II B Computer Aided Design & Simulation A+ Included in the Dean's List for the 21 Semester 7 (Jul 2021 - Dec 2021)	Electrical Installations II B 2.0 Computer Aided Design & Simulation A+ 2.0 Included in the Dean's List for the Sem 21 Semester 7 (Jul 2021 - Dec 2021)

Full Name : Maddage Dilshan Desha	priya Perera	e greek i de son. Ne rege es nist	ALL LAND A	MARKET OF LAR
Registration No : 170439T	Date of Birth: 15 March 1997	Gender	: Male	SAUDINE DE NORA
Field of Specialization : Electrical Engineering	The same of the sa	THE WORLD'S	, va šir i telion tva sre LANKA	AN ENGINE OF MORAL MORAL MORE OF MORAL MOR

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EE 3023	Control Systems II	C	2.0	A SR U
EE 3033	Electrical Machines & Drives III		2.0	A ·
EE 3043	Power Systems III		2.0	A (F. 1) (A 5 <u>H</u>)
EE 3053	Power Electronics & Applications I		2.0	A GET
EE 4183	183 Laboratory Practice VI		1.0	A B
EE 4243	Nuclear Power Engineering	Α-	2.0	7 7 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
EE 4903	Field Visit	B+	Metalli, Market	1.0
MN 4022	Engineering Economics	A	2.0	A SW E
2020/20	21 Semester 8 (Jan 2022 - Jun 2022)	VICTOR OFFICE OFFICE	NUSAL S MCHALU LIBAT D	12 va. 45 us 148.
EE 4023	High Voltage Engineering II	A +	2.0	ukserij var≟ u velik ⊥
EE 4043	Electrical Machines & Drives IV	B+	2.0	Wilder William Wilder
EE 4053	4053 Power Systems IV		2.0	(Algueri NAC⊒EL NASAUL
EE 4063	E 4063 Power Electronics & Applications II		2.0	456 7 - 2 2 - 5 - 5
EE 4193	EE 4193 Laboratory Practice VII		1.0	ASE.
EE 4203	E 4203 Design Project		10.0	1. 1. 1. 1. - 1. 5.4
EE 4223	E 4223 Renewable Energy & Environment		2.0	10 1 12 1 10 1 <u>-</u> 10 1 10
MN 4122	Human Resource Management and Industrial Relations	s A-	2.0	aliji Vir≌ Januari
MN 4900	Professional Ethics	Α-	-	1.0
La Tar Con La C	Total Credits	Earned	137.5	15.5

The Authenticity of the Transcript may be verified by referring the original to the University of Moratuwa.



Deputy Registrar - Examinations (Not valid without the embossed seal)

15-November-2022

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I/I-we	CATOMA TOWNS OF	Incomplete-Written Examination
l-ca	PATUMA SPILINKA III	Incomplete-Continuous Assessment
F		Fail
Р		Pass (non-graded)
N - The state of	Paris Table 10	Academic Concession
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Weightage for each semester or term is taken as uniform.

Criterion for Dean's List:

Full time undergraduate students who achieve a semester grade point average of 3.80 or better, have completed at least the minimum number of credits on a letter graded basis during the semester considered, and have no "Incomplete" grades or Failures.

Overall GPA	Academic Standing
3.70 - 4.20	First Class
3.30 - 3.69	Second Class - Upper Division
3.00 - 3.29	Second Class - Lower Division
2.00 - 2.99	Pass

University of Moratuwa is a government institute established under 1978 act as amended & empowered to award Bachelors, Masters, Doctorates & Diplomas

Overview of the University of Moratuwa, Sri Lanka

The University of Moratuwa, Sri Lanka is a leading technical University in Sri Lanka. It is located in Katubedda, Moratuwa about 20km south of the Colombo City Centre. It first gained University status on 15 February 1972 as the Katubedda Campus of the University of Ceylon. The University of Moratuwa is deemed to be established by an Order under the University Act No. 16 of 1978 on 1 January 1979. The University presently has six Faculties, namely the Faculty of Architecture, Faculty of Business, Faculty of Engineering, Faculty of Graduate Studies, Faculty of Information Technology and Faculty of Medicine. The University awards the following degrees.

Abbreviation	Name of the Degree	Normal Duration
BArchHons	Bachelor of Architecture Honours	5 years full time
BBScHons	Bachelor of Business Science Honours	4 years full time
BDesHons	Bachelor of Design Honours	4 years full time
BDesHons (FD&PD)	Bachelor of Design Honours in Fashion Design & Product Development	4 years full time
BIT (External)	Bachelor of Information Technology External Degree (Certificate in IT is awarded on completion of Level 1 and the Diploma in IT is awarded on completion of Level 2)	3 years
BScHons (AI)	Bachelor of Science Honours in Artificial Intelligence	4 years full time
BScEngHons	Bachelor of the Science of Engineering Honours	4 years full time
BScHons (FM)	Bachelor of Science Honours in Facilities Management	4 years full time
BSc (IT)	Bachelor of Science in Information Technology	3 years full time
BScHons (IT)	Bachelor of Science Honours in Information Technology	4 years full time
BScHons (IT&M)	Bachelor of Science Honours in Information Technology & Management	4 years full time
BScHons (T&CP)	Bachelor of Science Honours in Town & Country Planning	4 years full time
BScHons (T&LM)	Bachelor of Science Honours in Transport & Logistics Management	4 years full time
BScHons (QS)	Bachelor of Science Honours in Quantity Surveying	4 years full time
BLAHons	Bachelor of Landscape Architecture Honours	4 years full time
MBBS	Bachelor of Medicine and Bachelor of Surgery	5 years full time
PGDip	Postgraduate Diploma	1 year part time
МВА	Master of Business Administration	1 year full time 2 years part time
MEng	Master of Engineering	1 year full time 2 years part time
MGem	Master of Gemmology	1 year full time 2 years part time
MSc	Master of Science	1 year full time 2 years full time 2 years part time
MSPMD	Master of Spatial Planning, Management & Design	2 years full time
MUD	Master of Urban Design	1 year full time 2 years part time
MPhil	Master of Philosophy	2 years full time 3 years part time
PhD	Doctor of Philosophy	3 years full time 5 years part time

 $^{{\}bf * These\ postgraduate\ degrees\ will\ normally\ have\ several\ programmes,\ each\ of\ which\ could\ be\ identified\ following\ the\ name\ and\ the\ abbreviation.}$



(For online verification)