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<b>Started on</b>	Tuesday, 8 November 2022, 10:10 AM
<b>State</b>	Finished
<b>Completed on</b>	Tuesday, 8 November 2022, 10:24 AM
<b>Time taken</b>	14 mins 54 secs
<b>Grade</b>	6.00 out of 10.00 (60%)

## Question 1

Correct Mark 1.00 out of 1.00

What is the answer of the following Binary Fractions converts into Decimal Fractions?

$10111.111_2$

Select one:

- ☐ a. 22.875
- ☒ b. 23.875 ✓
- ☐ c. 23.877
- ☐ d. 23.855

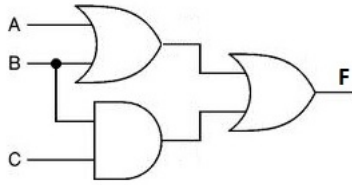
Your answer is correct.

The correct answer is: 23.875

## Question 2

Correct Mark 1.00 out of 1.00

Which of the following expression represents the Output (F) of this circuit?



Select one:

- ☒ a.  $(A+B)+(BC)$  ✓
- ☐ b.  $(A+B).(BC)$
- ☐ c.  $(AB).(B+C)$
- ☐ d.  $(A+B)+(B+C)$

Your answer is correct.

The correct answer is:

$(A+B)+(BC)$

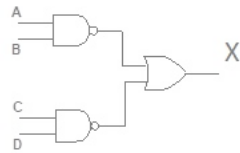
### Question 3

Correct Mark 1.00 out of 1.00

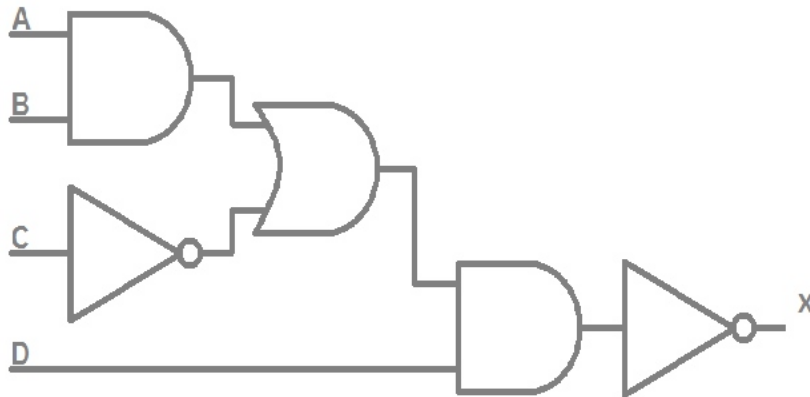
What the suitable circuit for the given expression?

$$X = (AB)' + (CD)'$$

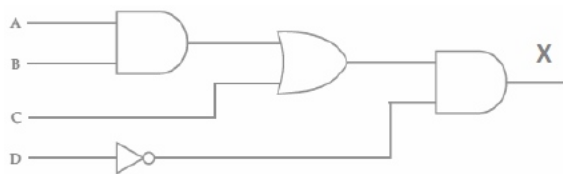
Select one:



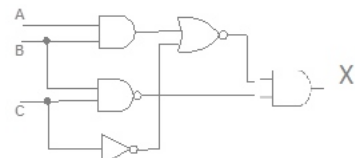
☒ a.



☐ b.

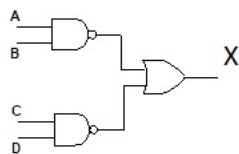


☐ c.



☐ d.

Your answer is correct.



The correct answer is:

## Question 4

Correct Mark 1.00 out of 1.00

What would be the answer of the following binary addition?

$$1011001 + 111010 = ?$$

8 bits representative

Select one:

- ☐ a. 10011011
- ☐ b. 01010011
- ☐ c. 10010010
- ☒ d. 10010011 ✓

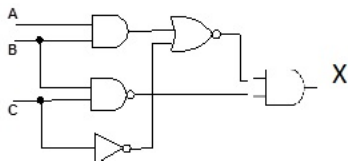
Your answer is correct.

The correct answer is: 10010011

## Question 5

Correct Mark 1.00 out of 1.00

What is the expression of the following logical circuit?



Select one:

- ☐ a.  $X = (BC)'(AC + C)'$
- ☐ b.  $X = (AC)'(AB + C)'$
- ☐ c.  $X = (BA)'(AB + C)'$
- ☒ d.  $X = (BC)'(AB + C)'$  ✓

Your answer is correct.

The correct answer is:  $X = (BC)'(AB + C)'$

## Question 6

Incorrect Mark 0.00 out of 1.00

8 bits digital system is required to subtract the following two decimal numbers

115-27

What would be the answer in binary form?

Select one:

- ☐ a. 01010111
- ☐ b. 01011111
- ☒ c. 10101011 ✖
- ☐ d. 11010101

Your answer is incorrect.

The correct answer is: 01010111

## Question 7

Incorrect Mark 0.00 out of 1.00

What are the derived logic gates ?

Select one:

- ☐ a. XNOR and OR
- ☐ b. NAND and NOR
- ☒ c. NAND and AND ✖
- ☐ d. AND and OR

Your answer is incorrect.

The correct answer is:  
NAND and NOR

## Question 8

Correct Mark 1.00 out of 1.00

One of the disadvantage of the signed magnitude is?

Select one:

- ☒ a. One of the bit patterns is wasted ✓
- ☐ b. One of the bit patterns is not wasted
- ☐ c. It is easy to determine whether a number is negative or non negative
- ☐ d. ALU overflow

Your answer is correct.

The correct answer is: One of the bit patterns is wasted

## Question 9

Incorrect Mark 0.00 out of 1.00

What are the two types of Registers in the processor?

Select one:

- ☒ a. Use and User visible ✗
- ☐ b. Visible and Invisible
- ☐ c. Control and Non control
- ☐ d. User-visible and Control and status

Your answer is incorrect.

The correct answer is: User-visible and Control and status

## Question 10

Incorrect Mark 0.00 out of 1.00

Identify the three types of Hazards

Select one:

- ☒ a. Resources, hard disk and management ✗
- ☐ b. Disk, control and user input
- ☐ c. Resources, data and control
- ☐ d. Control, data and fetch

Your answer is incorrect.

The correct answer is: Resources, data and control

◀ Lecture 12 (Processor Structure)

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