





























































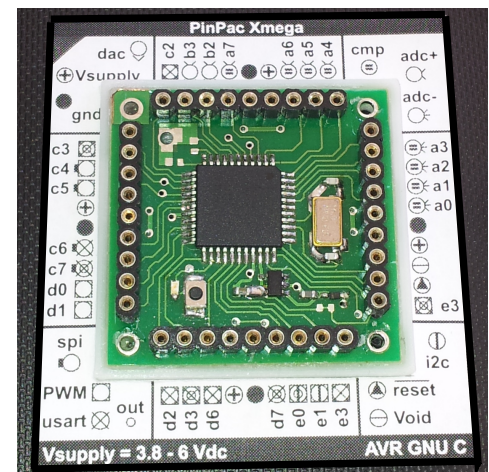
PinPac Xmega processor Module

Many Xmega modules can operate on the same pin stack where each has a unique ID. Code, is uploaded into a selected module through a single pin without interrupting other processors on the stack. Xmega modules will only store code with a proper address on flash memory. On next startup this code will be executed from the Xmega application section. For easy flash read and write, the user has access to specialized functions stored in the Xmega boot section.



Hx7 I/O map: Pin numbers **1 - 44** correspond to the Atxmega 128A48, 64A4U/ 32A4U and 16A4U for further information go to atmel.com and view the datasheet for these micro controllers.

PinPac Xmega									
dac 		c2  b3  b2  a7  a6  a5  a4 				cmp 		adc+ 	
⊕ Vsupply		      							
● gnd		12 7 6 3 2 1 44						adc- 	
usart: universal async receiver transmitter									
c3 		spi: serial perhiperal interface				43		 a3	
c4 		adc+: analog input positive				42		 a2	
c5 		adc-: analog input negative				41		 a1	
		cmp: comparator inputs				40		 a0	
c6 		dac: analog output						●	
c7 		i2c: two wire interface				34			
d0 		void: don't use this pin				35			
d1 						33		 e3	
spi 		PWM: pulse width modulation and timer outputs							
		22 23 24 27 28 29 32						i2c	
PWM 		      				  		reset 	
usart 		d2  d3  d6  d7  e0  e1 				 		Void 	
out 									
Vsupply = 3.8 - 6 Vdc						AVR GNU C			



PinPac Processor module with overlay