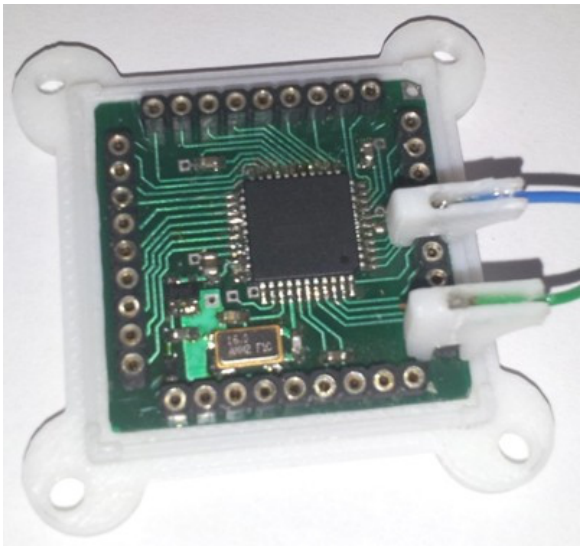
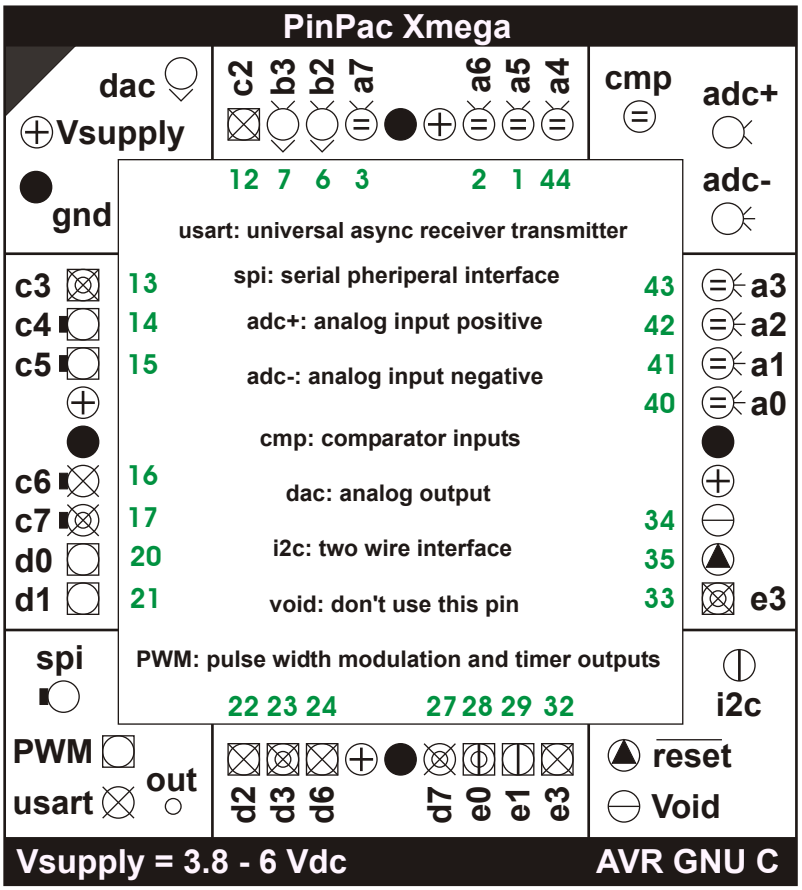
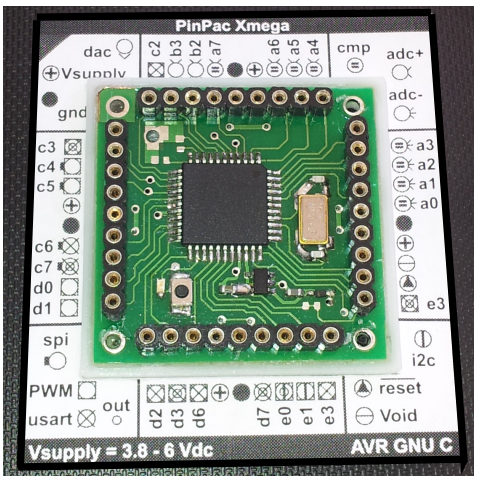
PinPac Xmega processor Module

Many Xmega modules can operate on the same pin stack where each has a unique ID. Code, is uploaded into a selected module through a single pin without interrupting other processors on the stack. Xmega modules will only store code with a proper address on flash memory. On next startup this code will be executed from the Xmega application section. For easy flash read and write, the user has access to specialized functions stored in the Xmega boot section.



Hx7 I/O map: Pin numbers **1 - 44** correspond to the Atxmega 128A48, 64A4U/ 32A4U and 16A4U for further information go to atmel.com and view the datasheet for these micro controllers.





PinPac Processor module with overlay