

This project uses a USB powered ESP8266, a DAC, a few opamps and a N-MOSFET to create an electronic load. The source code is available at:

Sheet: / Dimitris Tassopoulos  
File: eload.sch

**Title:** Electronic load using an ESP8266 and external DAC

Size: A4 Date: 01.10.2018 Rev: 1.0

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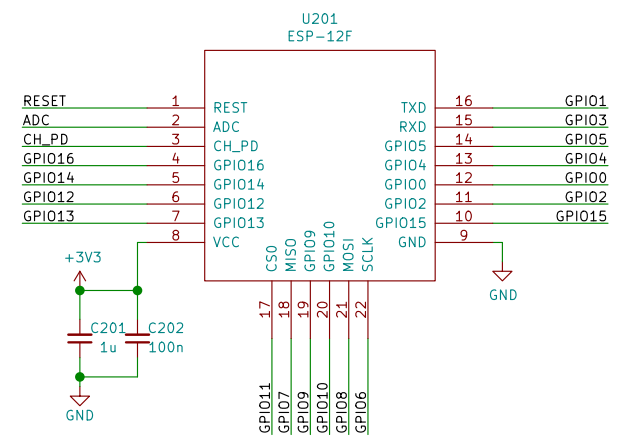
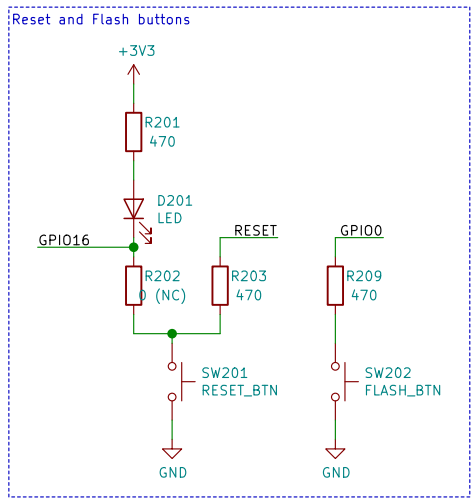
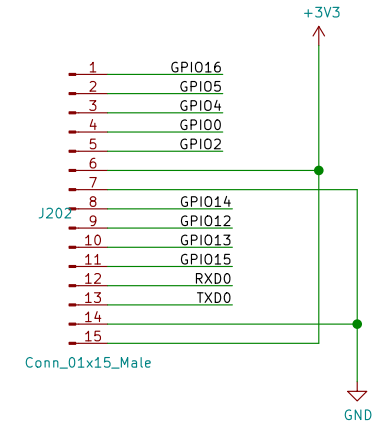
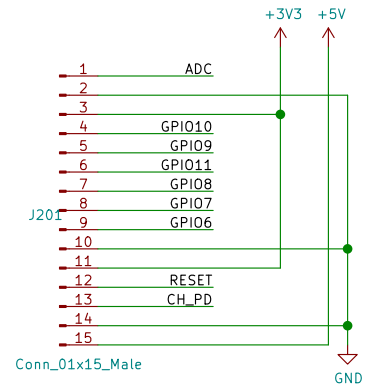
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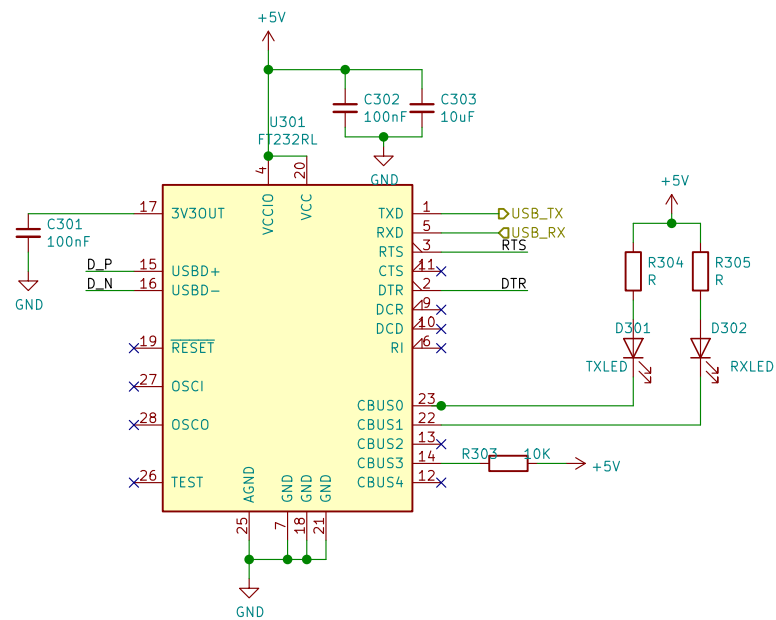
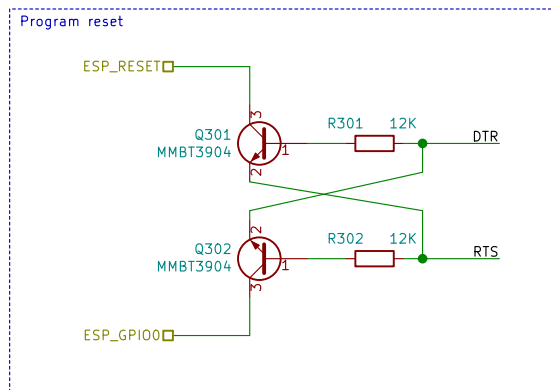
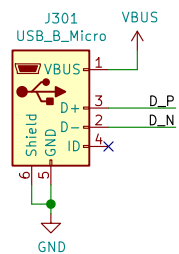
Multipurpose pinout

GPIO0	<u>SPI_CS_2</u>	GPIO6	<u>SDIO_CLK</u>	<u>SPI_CLK</u>	
GPIO1	<u>SPI_CS_1</u>	TXD0	GPIO7	<u>SDIO_DATA0</u>	<u>SPI_MISO</u>
GPIO2	<u>TXD1</u>	GPIO8	<u>SDIO_DATA1</u>	<u>SPI_MOSI</u>	<u>RXD1</u>
GPIO3	<u>RXD0</u>	GPIO9	<u>SDIO_DATA2</u>	<u>SPIHD</u>	<u>HSPIHD</u>
		GPIO10	<u>SDIO_DATA3</u>	<u>SPIWP</u>	<u>HSPIWP</u>
		GPIO11	<u>SDIO_CMD</u>	<u>SPI_CS0</u>	
GPIO12	<u>MTDI</u>	<u>HSPI_MISO</u>			
GPIO13	<u>MTCK</u>	<u>HSPI_MOSI</u>	<u>CTS0</u>		
GPIO14	<u>MTMS</u>	<u>HSPI_CLK</u>			
GPIO15	<u>MTDO</u>	<u>HSPI_CS</u>	<u>RTS0</u>		
		GPIO16	<u>XPD_DCDC</u>		

PIN exports:

TXD0	ESP_TXD0	GPIO0	ESP_GPIO0	HSPI_MISO	ESP_SPL_MISO
RXD0	ESP_RXD0	RESET	ESP_RST	HSPI_MOSI	ESP_SPL_MOSI
		ADC	ESP_ADC	HSPI_CLK	ESP_SPL_CLK
				HSPI_CS	ESP_SPL_CS0



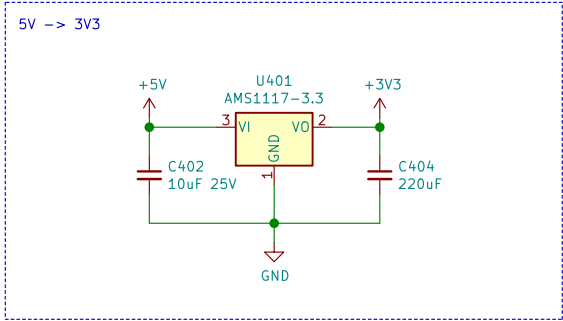
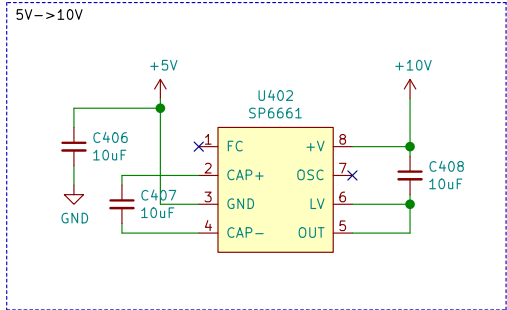
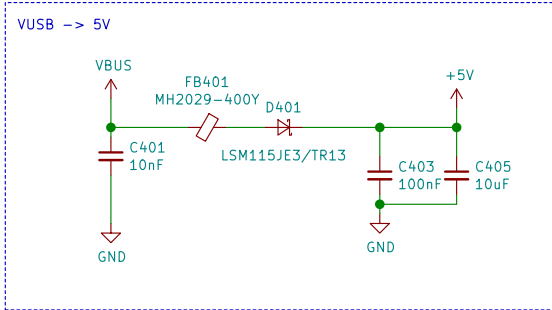


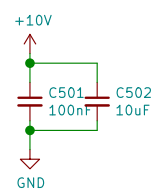
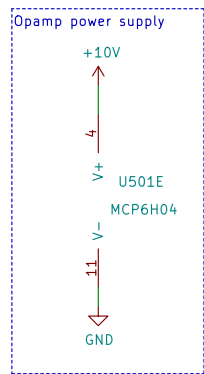
Sheet: /USB to UART/  
File: FT232RL.sch

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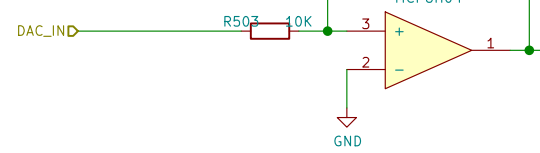
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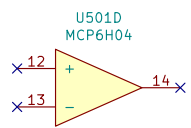
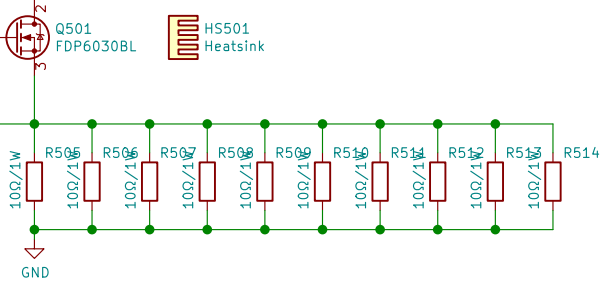
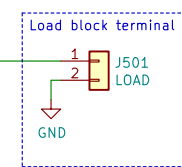
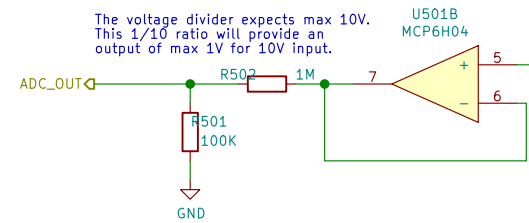


For max 4.096V input and  $R_1=10K$ ,  $R_f=12K$  the output will be 9V, which means 9A on the load

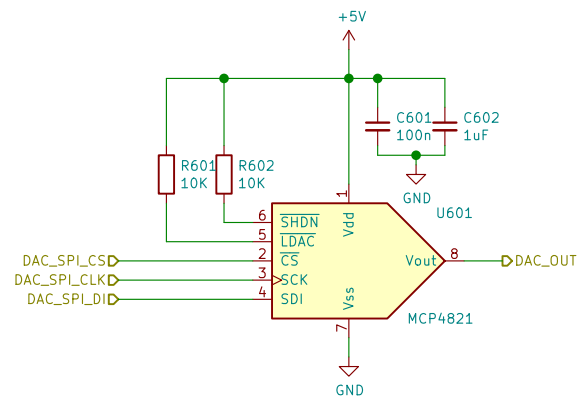
DAC range 0–4.096V



The voltage divider expects max 10V. This 1/10 ratio will provide an output of max 1V for 10V input.



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