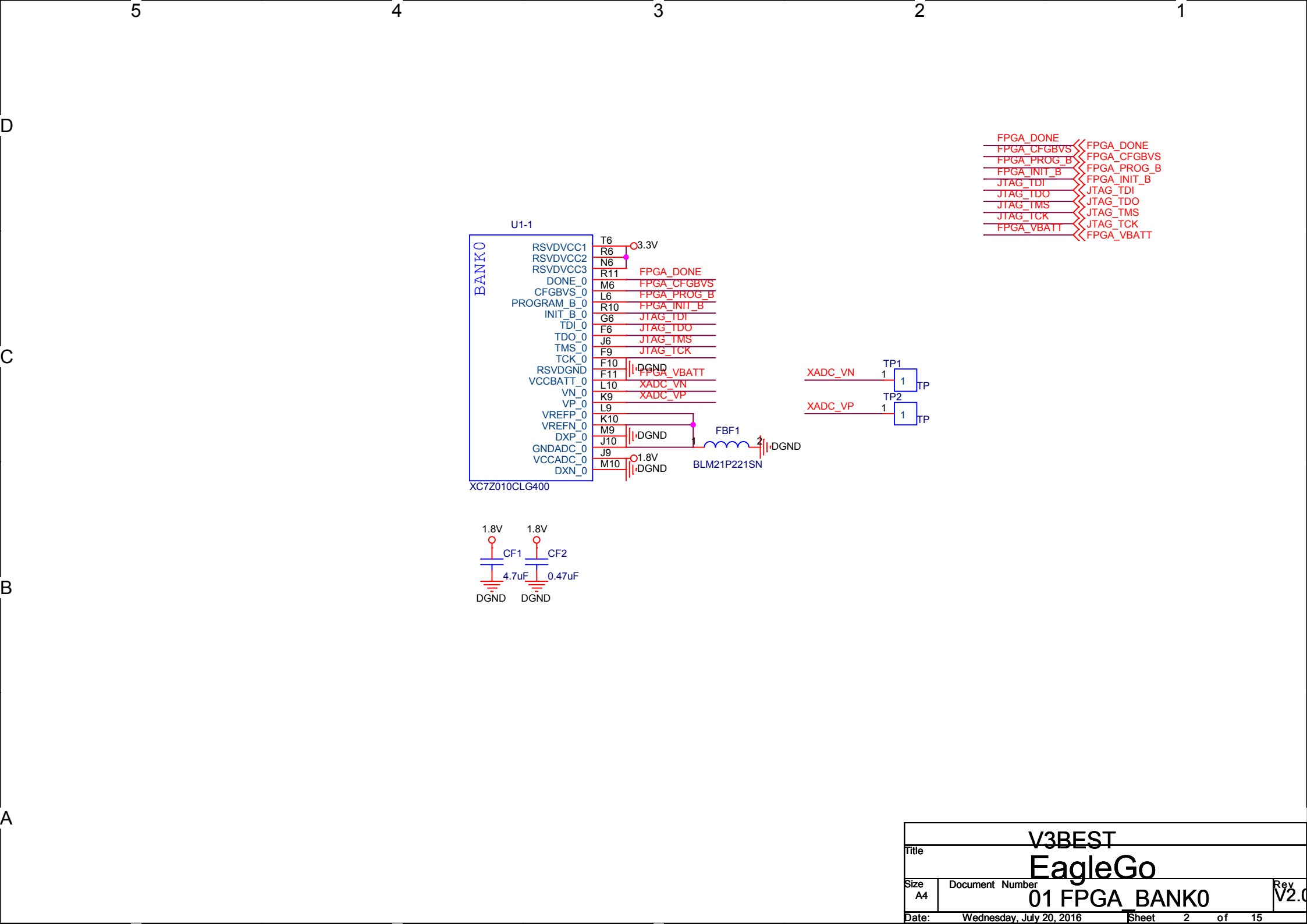


EagleGo V2.0

PAGE01	00	TOP
PAGE02	01	FPGA_BANK0
PAGE03	02	FPGA_BANK13_NC
PAGE04	03	FPGA_BANK34_35
PAGE05	04	FPGA_BANK500_501
PAGE06	05	FPGA_BANK502
PAGE07	06	FPGA_POWER
PAGE08	07	DDR3L
PAGE09	08	FLASH
PAGE10	09	LAN
PAGE11	10	USB
PAGE12	11	PIO
PAGE13	12	HDMI
PAGE14	13	CONFIG
PAGE15	14	SYS POWER

power on sequence
1 1.0V-1.8V-3.3V &1.35V



D

C

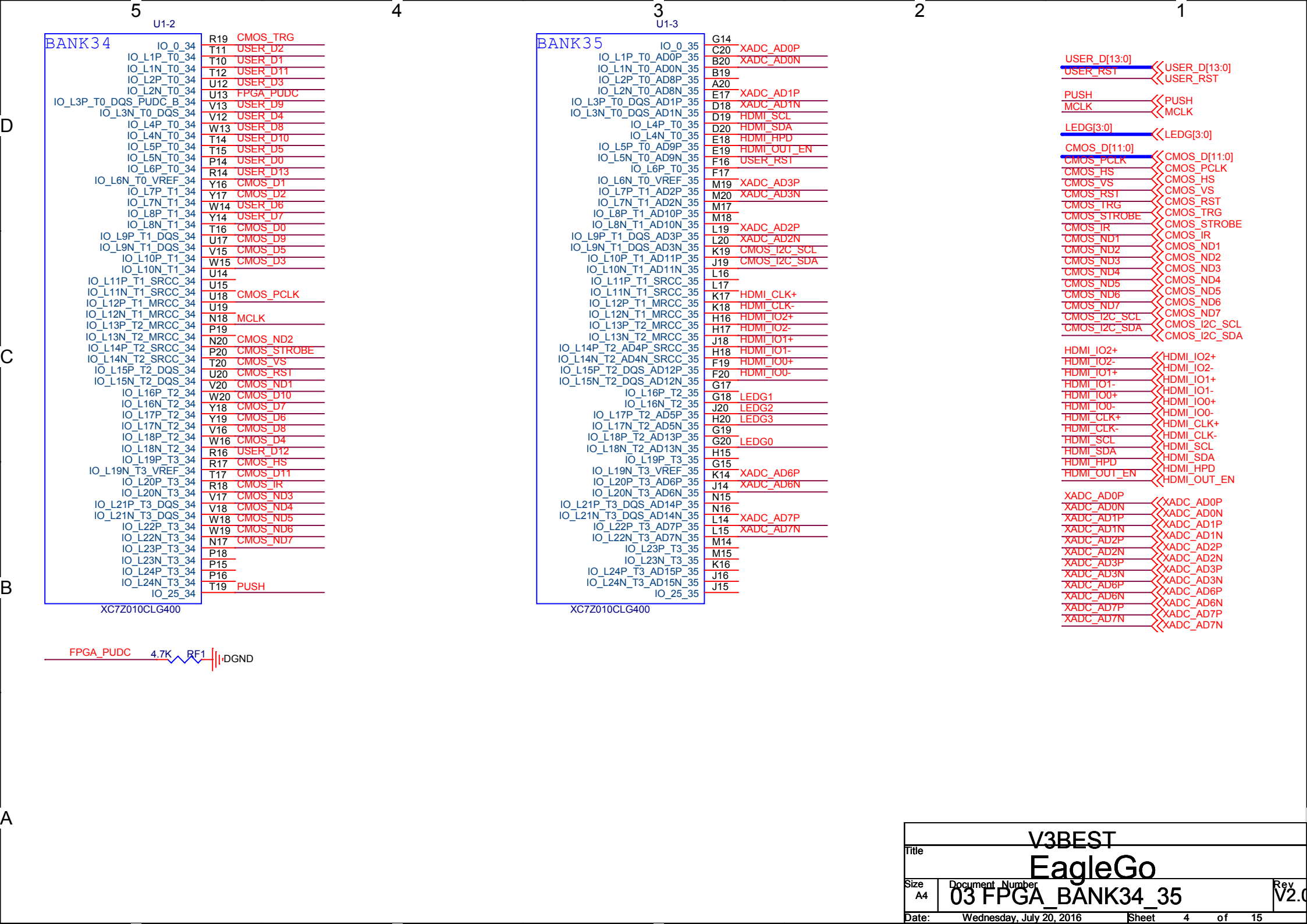
B

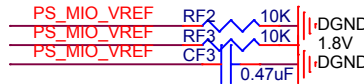
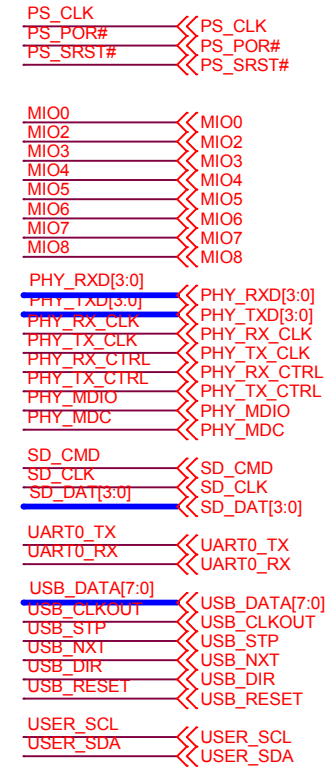
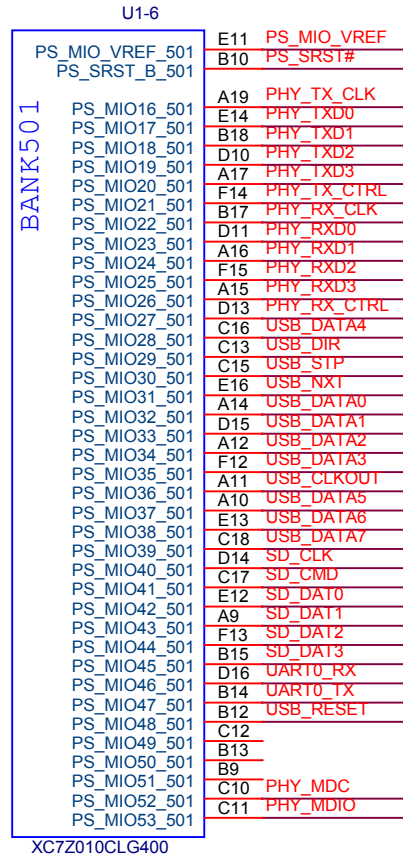
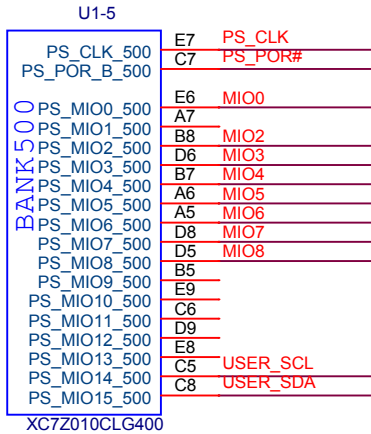
A

U1-4

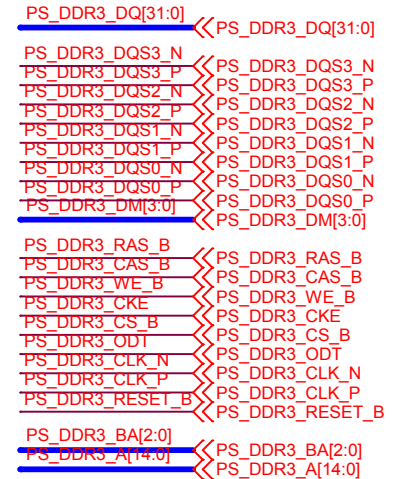
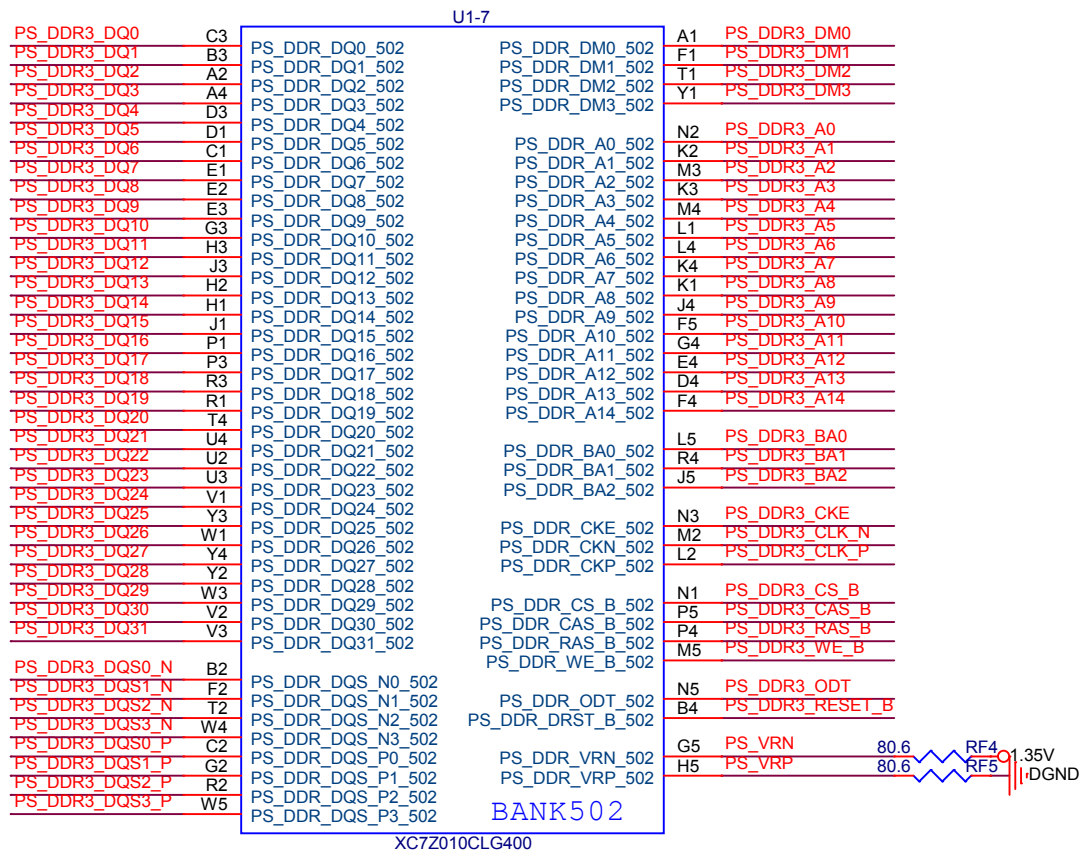
NC FOR 7010 BANK13	IO_L6N_T0_VREF_13	V5
	IO_L11P_T1_SRCC_13	U7
	IO_L11N_T1_SRCC_13	V7
	IO_L12P_T1_MRCC_13	T9
	IO_L12N_T1_MRCC_13	U10
	IO_L13P_T2_MRCC_13	Y7
	IO_L13N_T2_MRCC_13	Y6
	IO_L14P_T2_SRCC_13	Y9
	IO_L14N_T2_SRCC_13	Y8
	IO_L15P_T2_DQS_13	V8
	IO_L15N_T2_DQS_13	W8
	IO_L16P_T2_13	W10
	IO_L16N_T2_13	W9
	IO_L17P_T2_13	U9
	IO_L17N_T2_13	U8
	IO_L18P_T2_13	W11
	IO_L18N_T2_13	Y11
	IO_L19P_T3_13	T5
	IO_L19N_T3_VREF_13	U5
	IO_L20P_T3_13	Y12
	IO_L20N_T3_13	Y13
	IO_L21P_T3_DQS_13	V11
	IO_L21N_T3_DQS_13	V10
	IO_L22P_T3_13	V6
	IO_L22N_T3_13	W6

XC7Z010CLG400





set to 0.9V with VCCO_MIO1 at 1.8V.
In any other case, tie to VCCO_MIO1

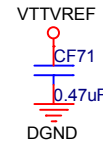
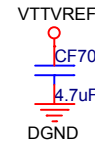
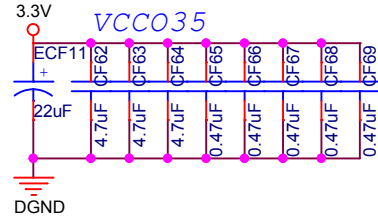
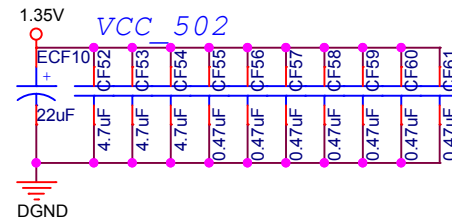
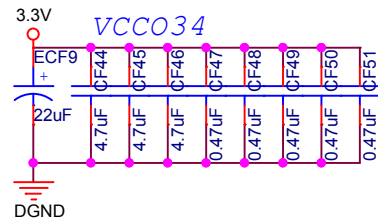
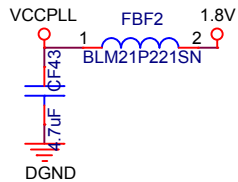
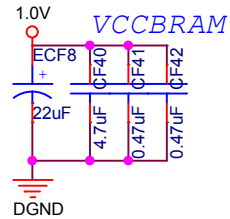
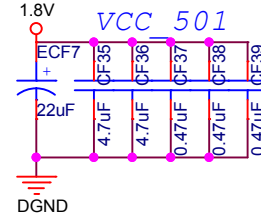
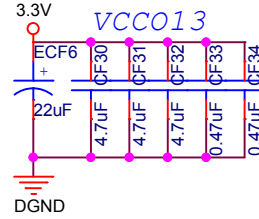
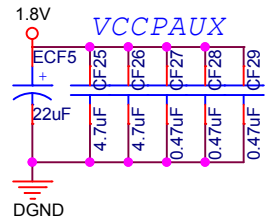
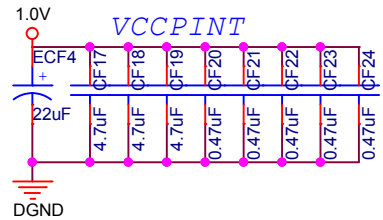
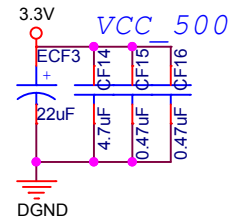
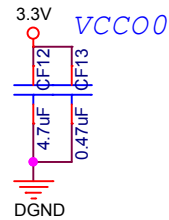
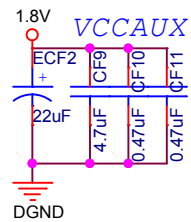
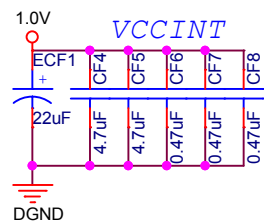
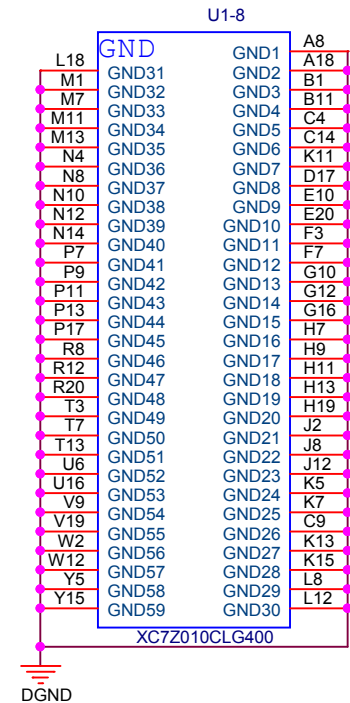
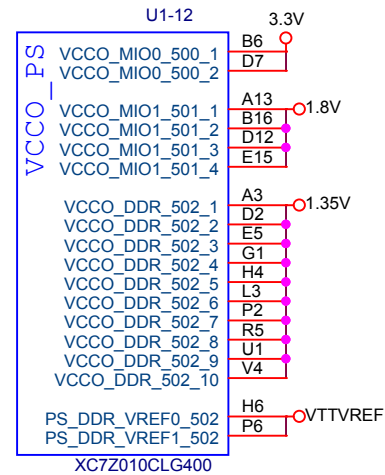
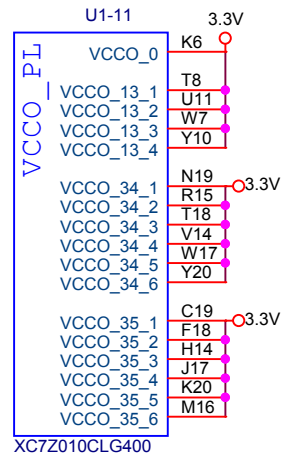
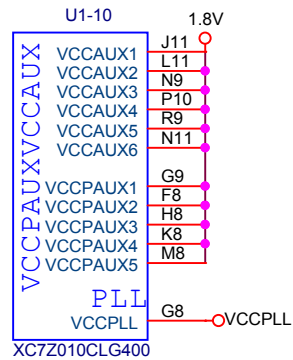
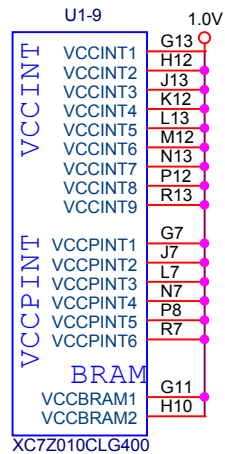


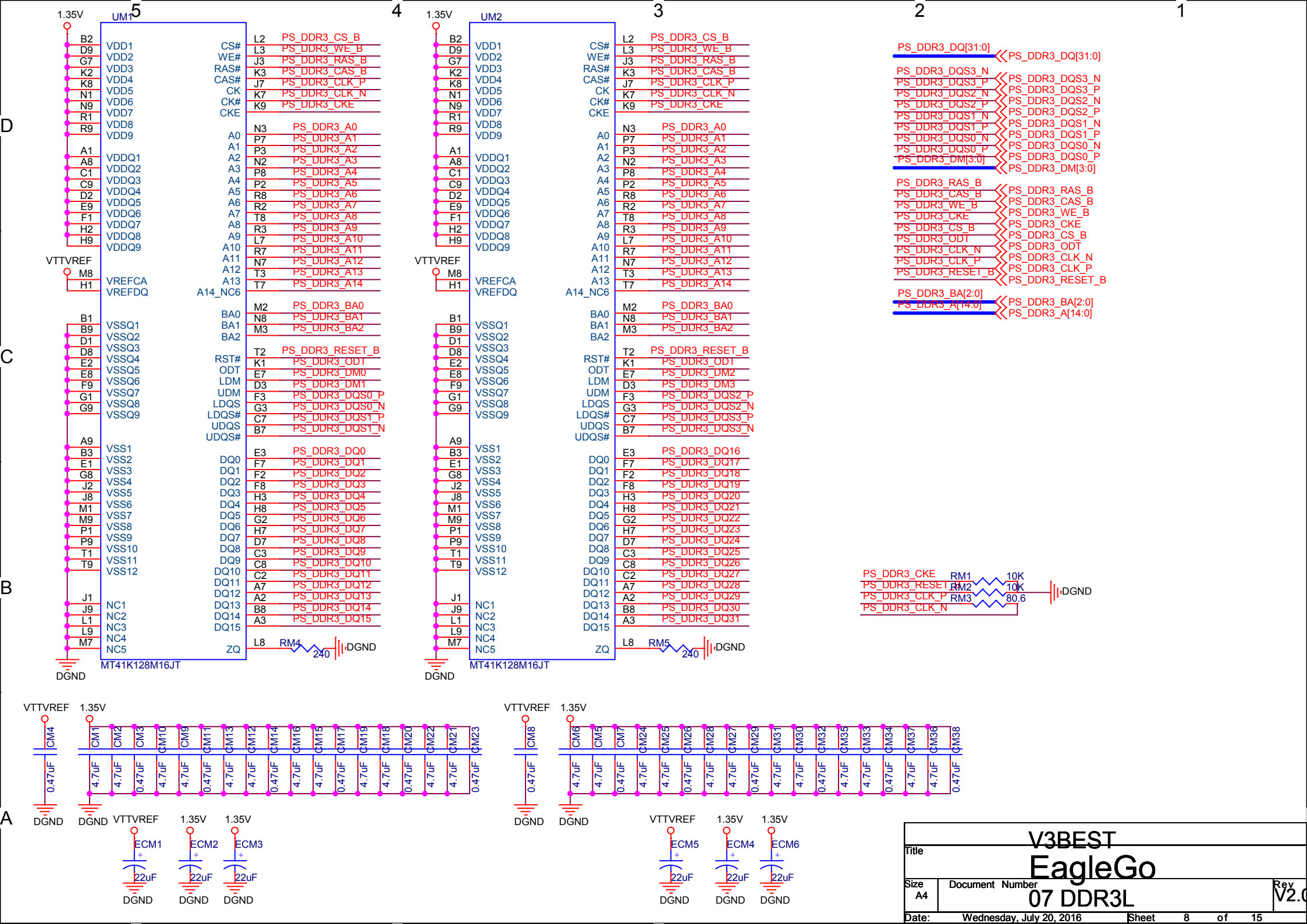
D

C

B

A



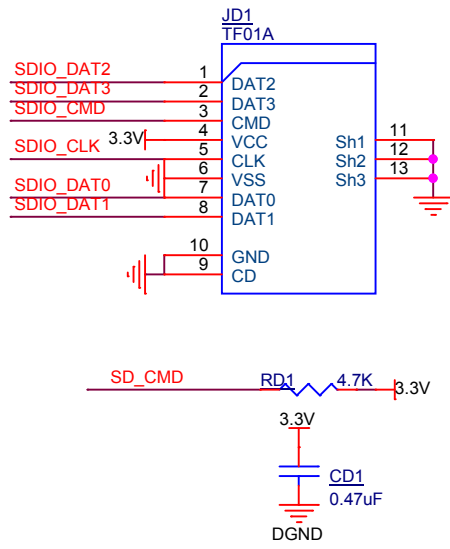
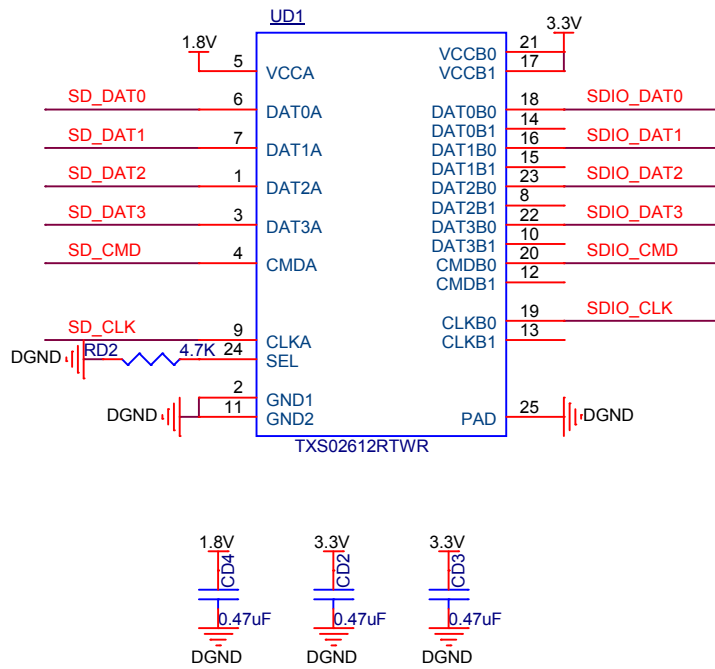
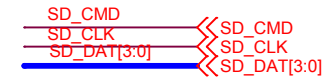


D

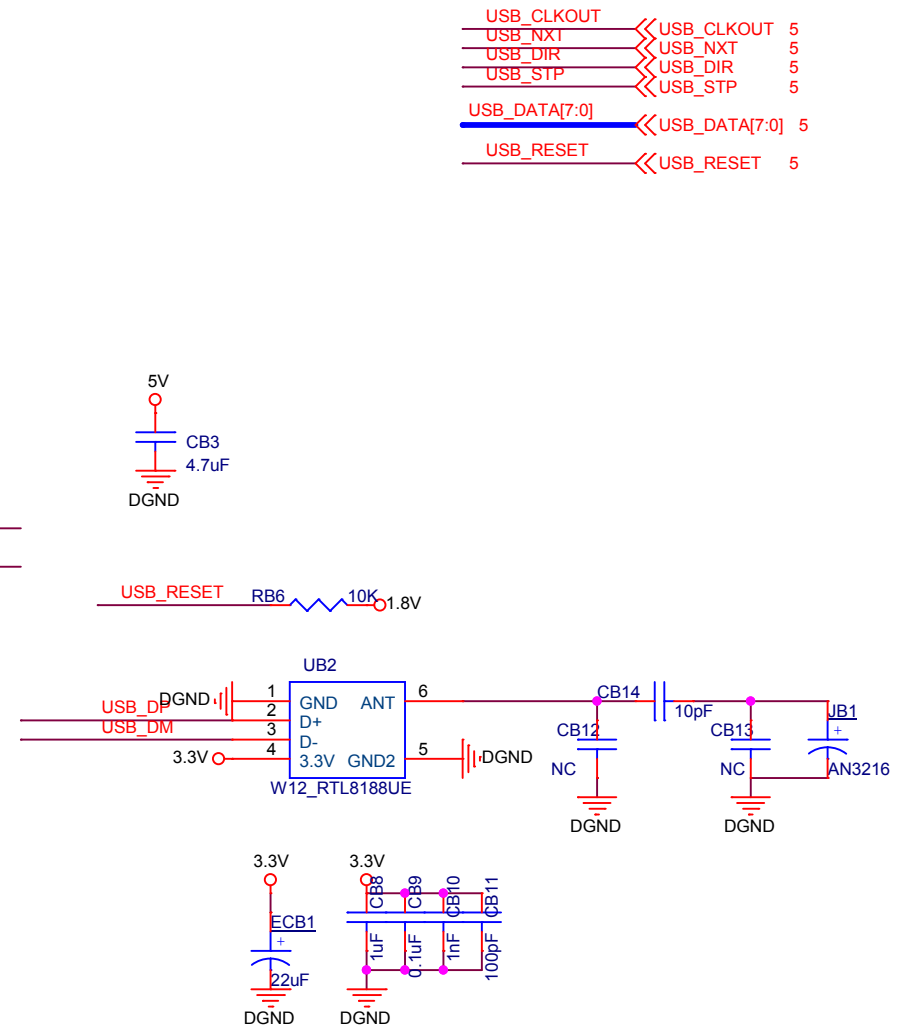
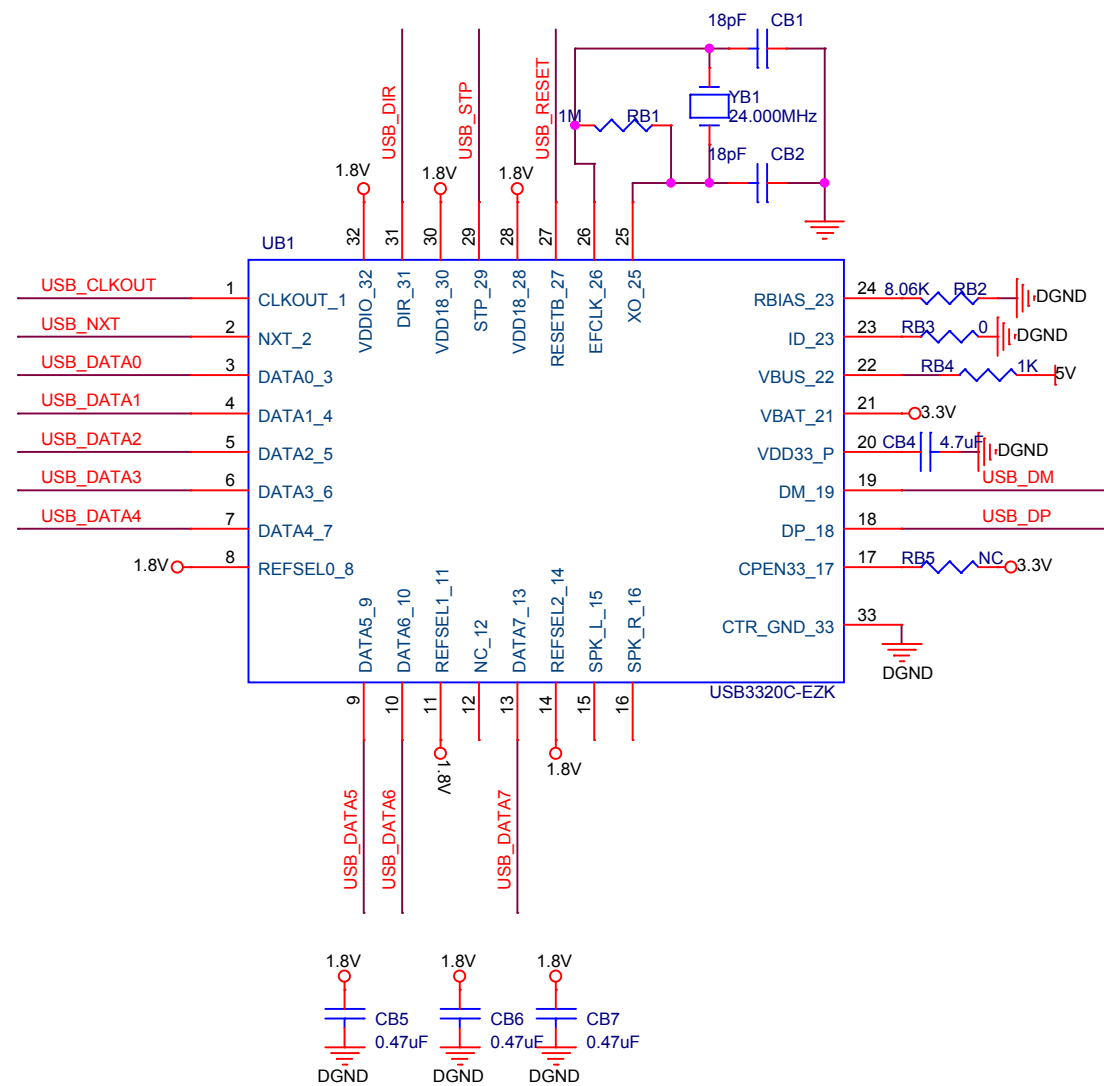
C

B

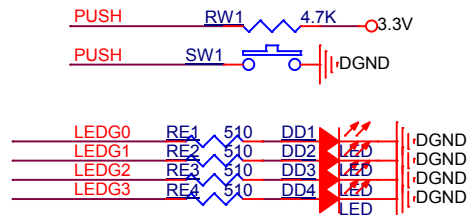
A



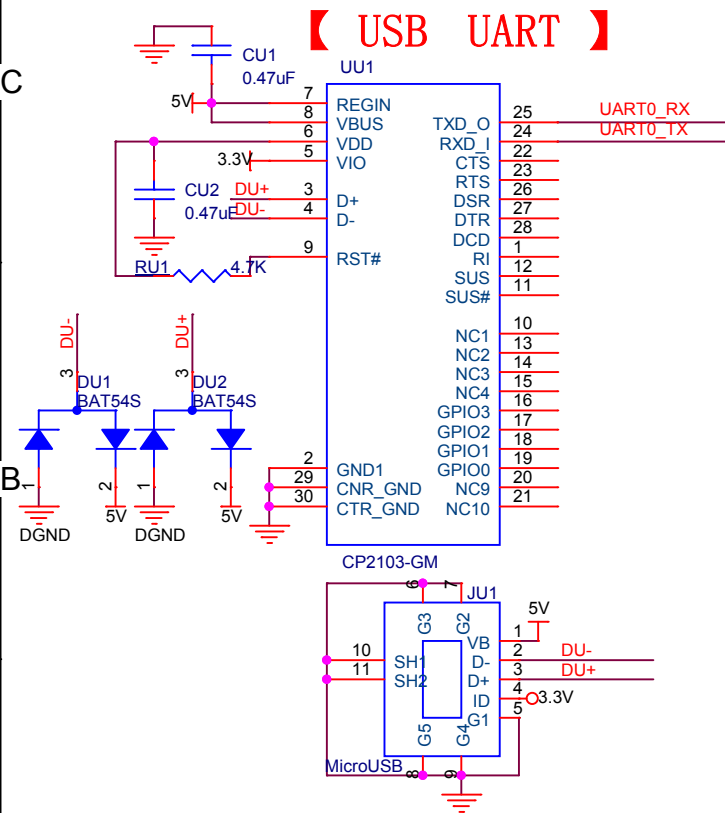




D

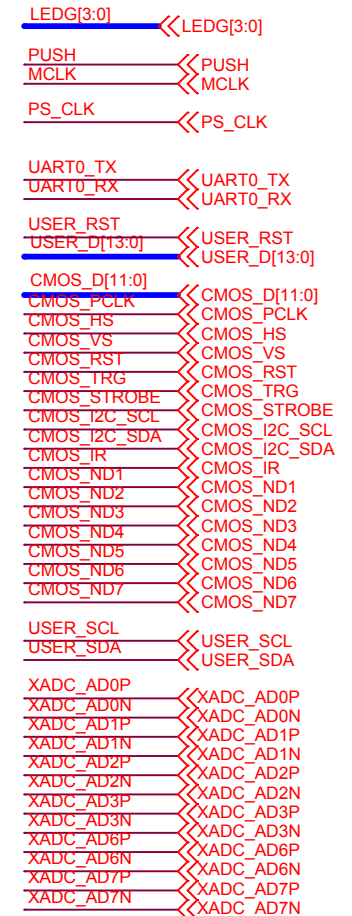
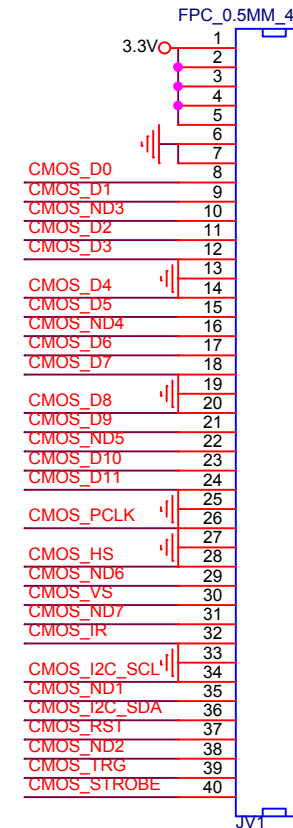
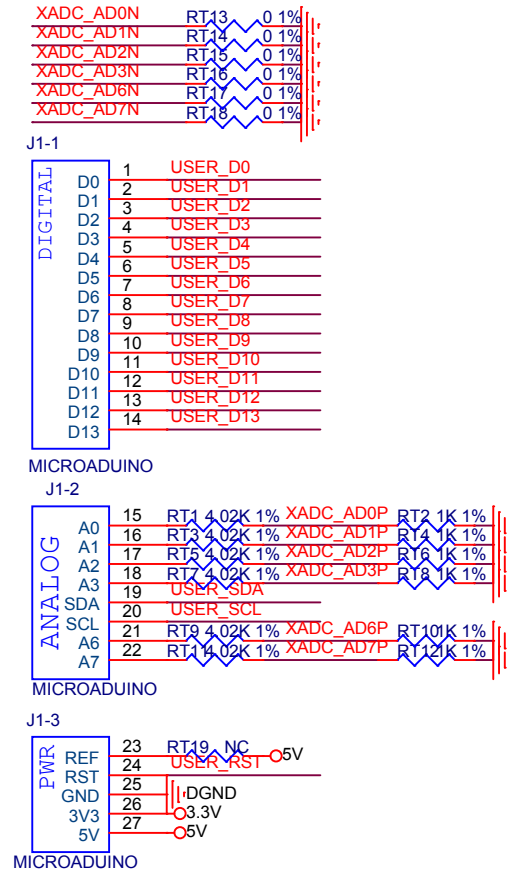
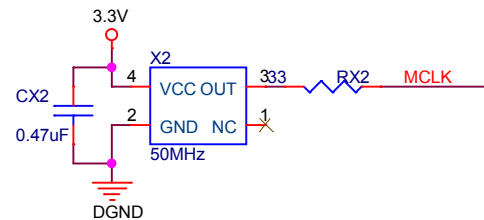
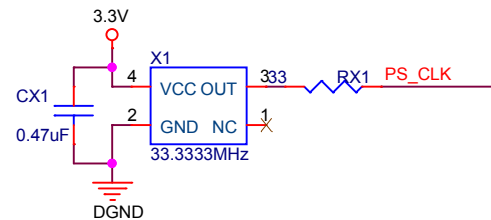


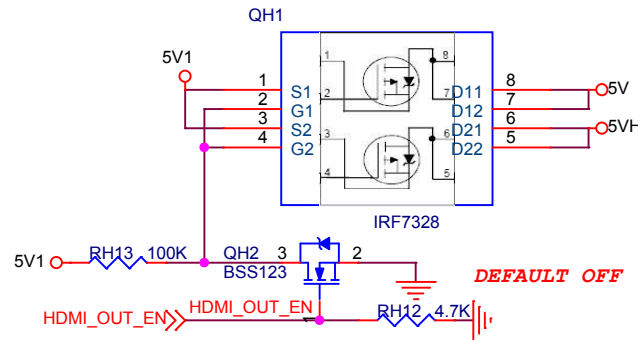
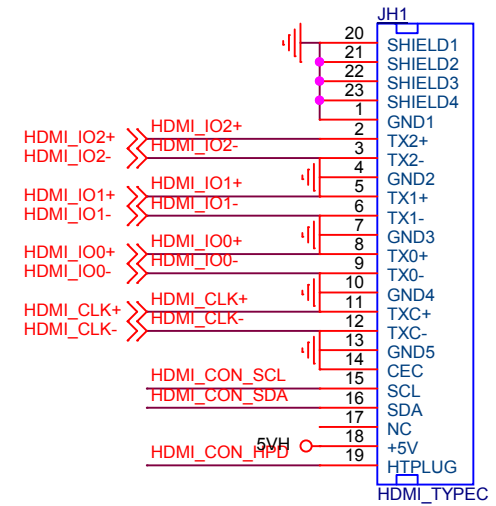
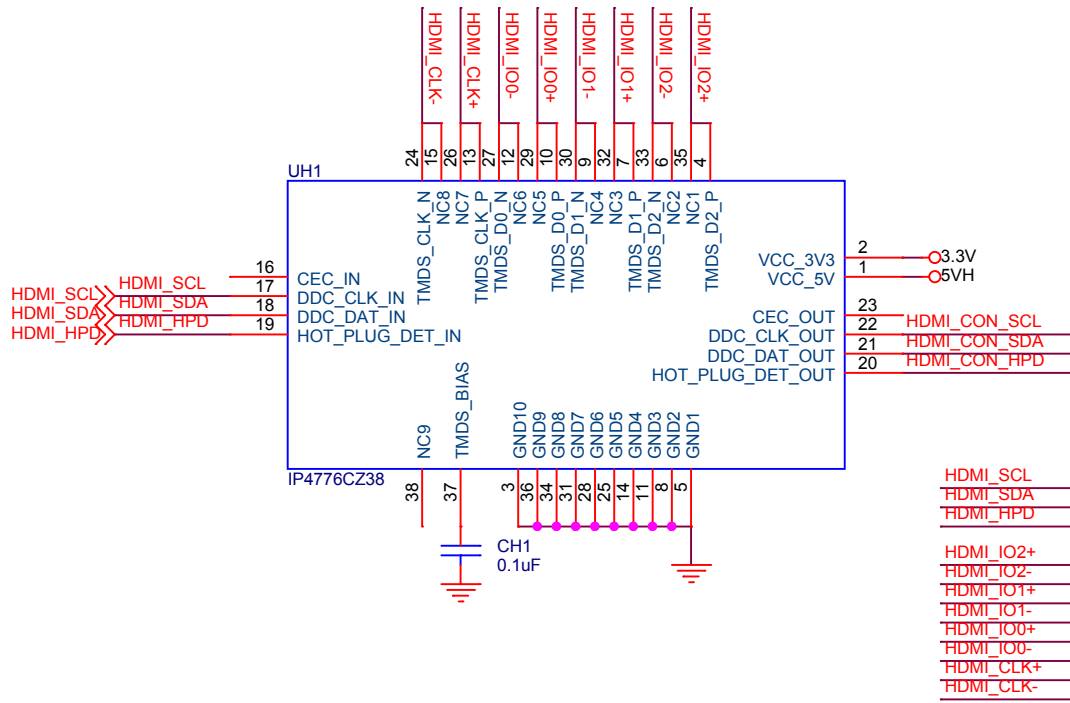
C

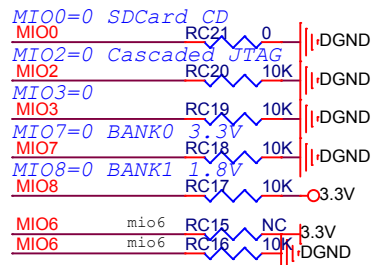
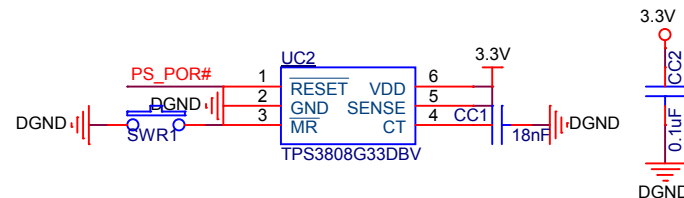
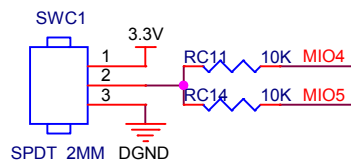
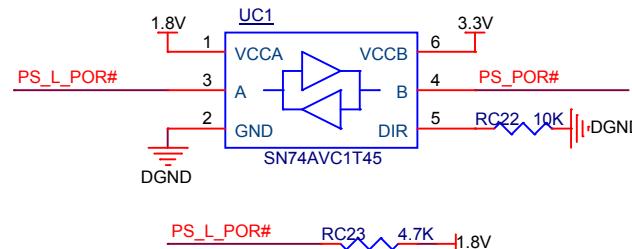
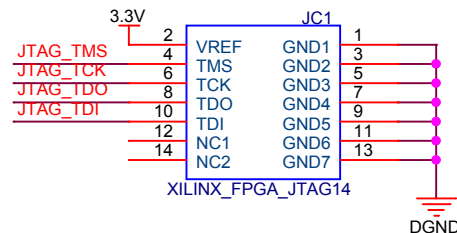
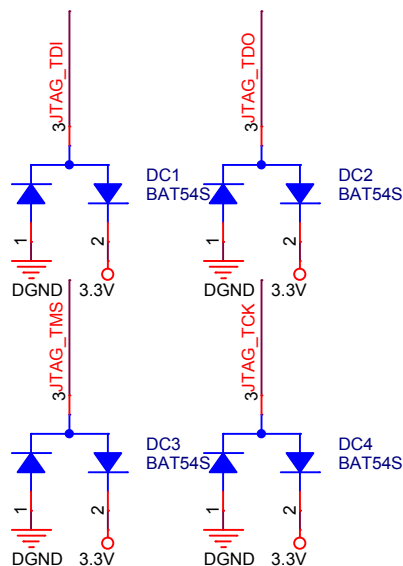
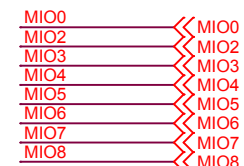
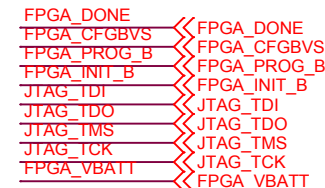
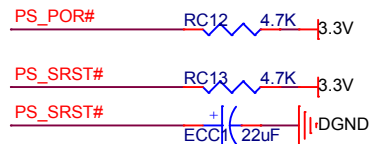
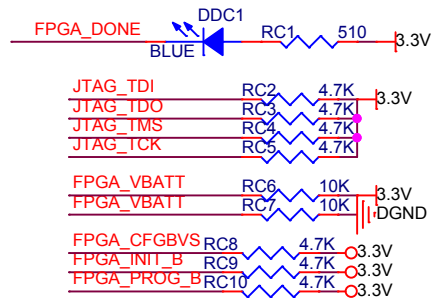


B

A







JTAG
SD
PLL Used 0
PLL Bypassed 1

MIO Bank1 Voltage
MIO8 0 2.5 V, 3.3 V
MIO8 1 1.8 V

D

C

B

A

