

Work Log

2.5.1 The Journal

Date	Task	Who
2/2/2022	Working on the min-term form of the 10 equations from 2.4.3	Wonjin, Syed, Yiwei, Sam, Brendan
2/2/2022	Building Work log & Project #1 documentation	Wonjin, Yiwei
2/4/2022	Software Research	Sam, Brendan, Syed
2/9/2022	Checking minterm solutions and starting Verilog	Wonjin, Syed, Yiwei, Sam, Brendan
2/10/2022	Debug session and all running program	Wonjin, Syed, Yiwei, Sam, Brendan
2/11/2022	Final phase 1 meeting for submission	Wonjin, Syed, Yiwei, Sam, Brendan

2.5.2 Affirmation

- Sam had Verilog working on February 9th.
- Brendan had Verilog working on February 9th.
- Yiwei had Verilog running on February 10th.
- Syed had Verilog working on February 11.
- Wonjin had Verilog working on February 11.
- Everyone was able to run the final program on February 11th.